

VICE MLB

2/4/2010 PVT K48-DRI

REV	ECN	DESCRIPTION OF REVISION	CK APPD	DATE
B	0000854735	PRODUCTION RELEASED		2010-02-04

1. ALL RESISTANCE VALUES ARE IN OHMS, 0.1 WATT +/- 5%.
2. ALL CAPACITANCE VALUES ARE IN MICROFARADS.
3. ALL CRYSTALS & OSCILLATOR VALUES ARE IN HERTZ.

D
C
B
A

D
C
B
A

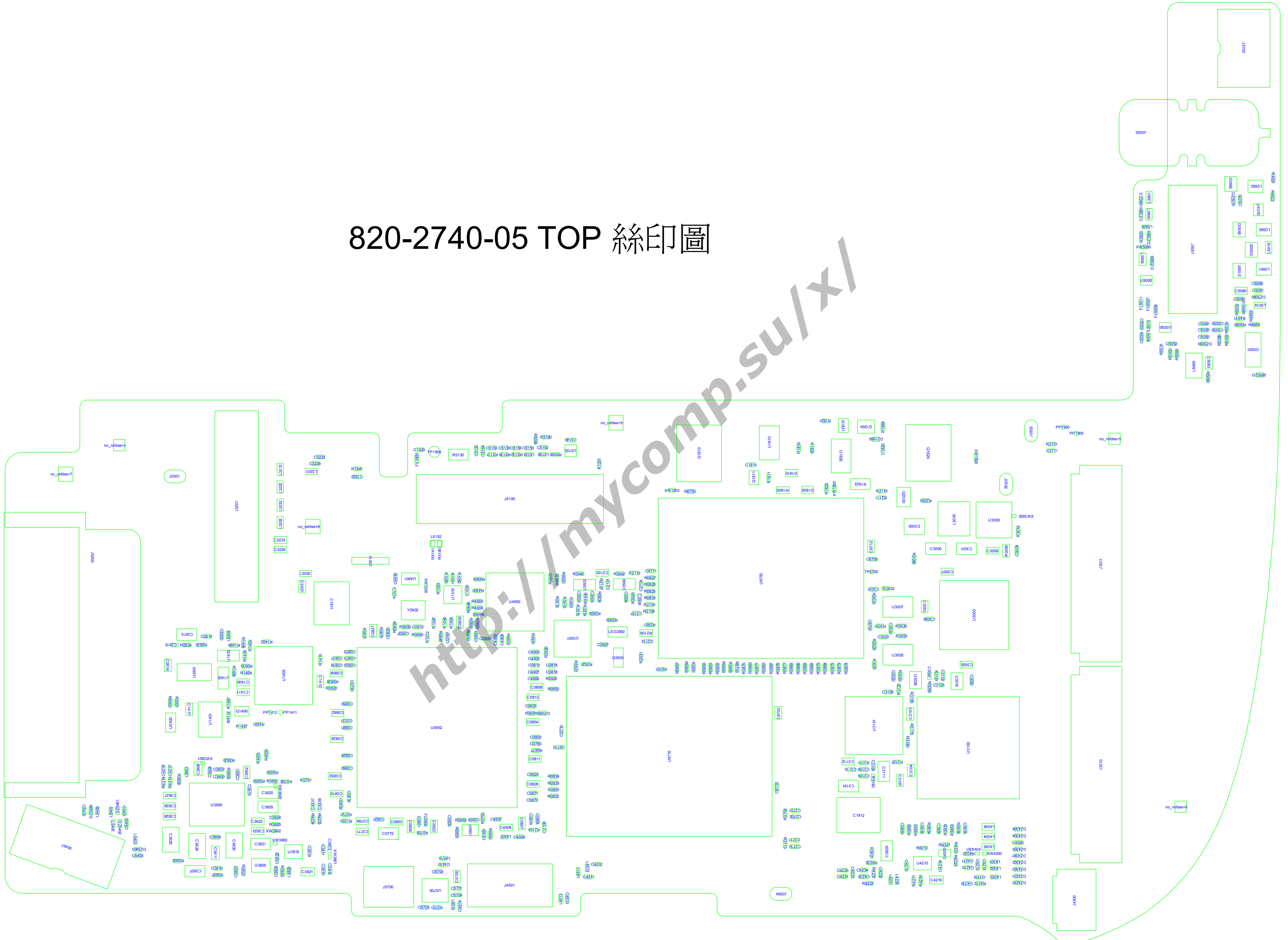
PDF	CSA	CONTENTS	SYNC MASTER	DATE	PDF	CSA	CONTENTS	SYNC MASTER	DATE
1	1	TABLE OF CONTENTS			32	40	AUDIO: AUDIENCE	AUDIO	12/04/2009
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4	4	CONFIGURATION OPTIONS	MIAMI	08/06/2009	35	45	ALS CONNECTOR	MIAMI	09/16/2009
5	5	FUNC/ICT TEST/BRACKETS	MIAMI	09/16/2009	36	48	I/O EXPANDER	JAMES	12/21/2009
6	6	AP MAIN	JAMES	12/21/2009	37	49	DISPLAY PORT SWITCH	JAMES	12/21/2009
7	7	AP PWR, AP BB&WIFI	JAMES	12/21/2009	38	50	44-PIN LANDSCAPE DOCK CONN	JAMES	12/21/2009
8	8	AP NAND & GPIO, NOR	JAMES	12/21/2009	39	51	60-PIN PORTRAIT DOCK CONN	JAMES	12/21/2009
9	9	AP RGB/CLCD, CAMERA	JAMES	12/21/2009	40	54	BUTTONS CONNECTOR	MIAMI	09/16/2009
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11	11	3G AND DEBUG MUXES	JAMES	12/21/2009	42	57	PROX SENSOR	MARKSIN	10/14/2009
12	12	AP MISC & ALIASES	JAMES	12/21/2009	43	67	FLASH	MIAMI	09/16/2009
13	14	MLC	MIAMI	09/16/2009	44	100	CONSTRAINTS	MIAMI	09/16/2009
14	15	MLC ALIASES	MIAMI	09/16/2009	45	101	MORE CONSTRAINTS	MIAMI	09/16/2009
15	17	Power Conn / Alias	MARK	12/04/2009	46	106	PHYSICAL/SPACING RULES	MIAMI	09/16/2009
16	18	DCIN POWER PATH	MARK	12/04/2009	47	113	Cross Reference Page		
17	19	CHARGER	MARK	12/04/2009	48	114	Cross Reference Page		
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22	29	DEBUG RESET ACCESS	MIAMI	09/16/2009	53	119	Cross Reference Page		
23	30	GRAPE 1 OF 2	JAMES	12/21/2009					
24	31	GRAPE 2 OF 2	JAMES	12/21/2009					
25	32	LVDS CONNECTOR	MIAMI	09/16/2009					
26	34	MOTION, GYRO, COMPASS/THERM	MIAMI	09/16/2009					
27	35	USB MUX/BRK DET	MIAMI	09/16/2009					
28	36	L61 AUDIO INTERFACE	AUDIO	12/04/2009					
29	37	AUDIO: SPEAKER AMP	AUDIO	12/04/2009					
30	38	AUDIO: HEADPHONE OUT	AUDIO	12/04/2009					
31	39	AUDIO: LINE OUT DOCK ESD CIRCUIT	AUDIO	12/04/2009					

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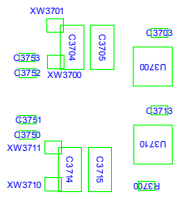
DRAWING
TITLE=0230
ASSEMBY DRAWING
LAST_MODIFIED=Thu Feb 4 09:41:44 2010

DRAWING TITLE		VICE MLB	
DRAWING NUMBER		051-8245	SIZE
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820-2740-05 TOP 絲印圖



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no_refdes+1

820-2740-05 BOTTOM 絲印圖



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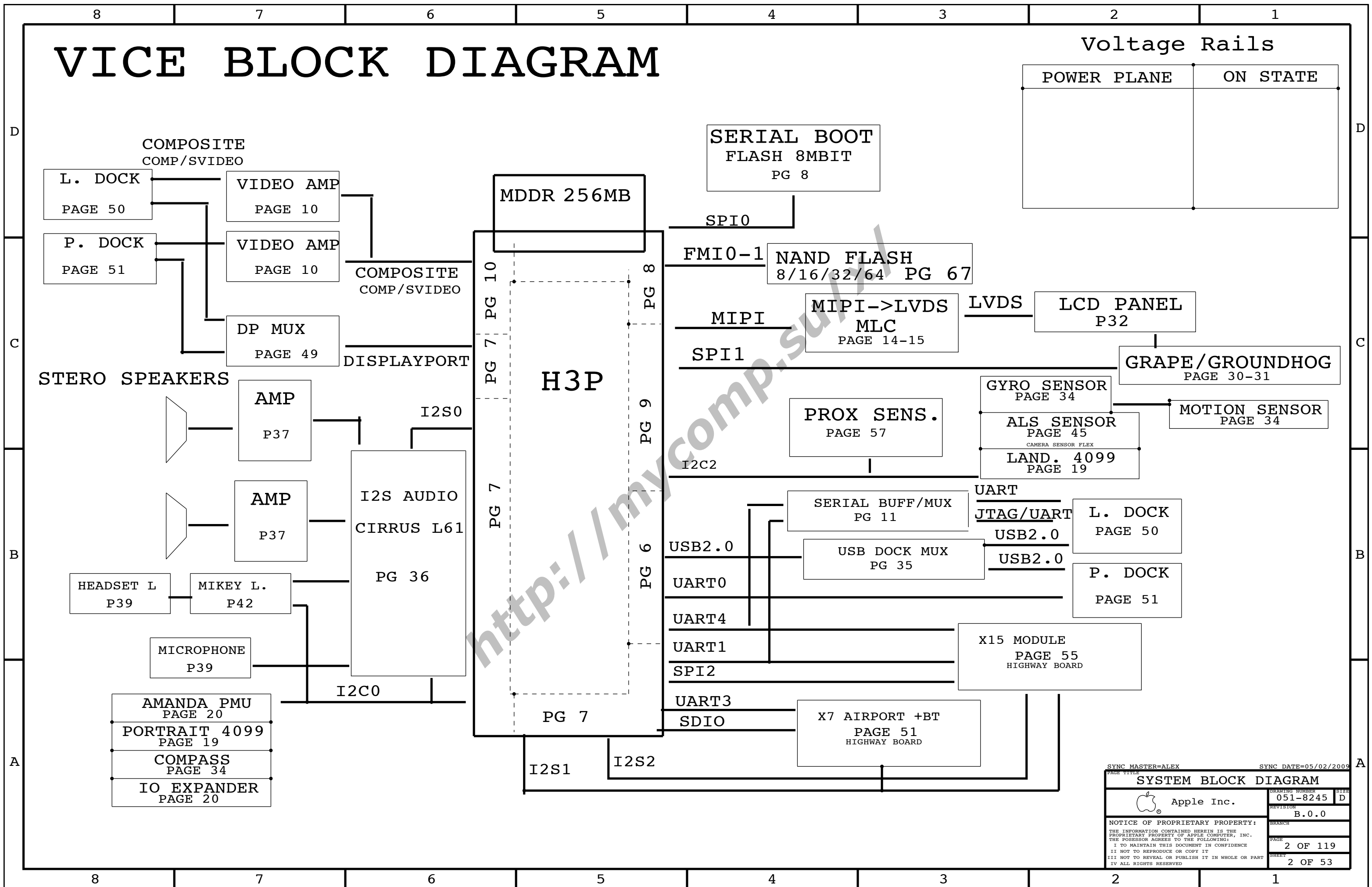
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U340

VICE BLOCK DIAGRAM

Voltage Rails

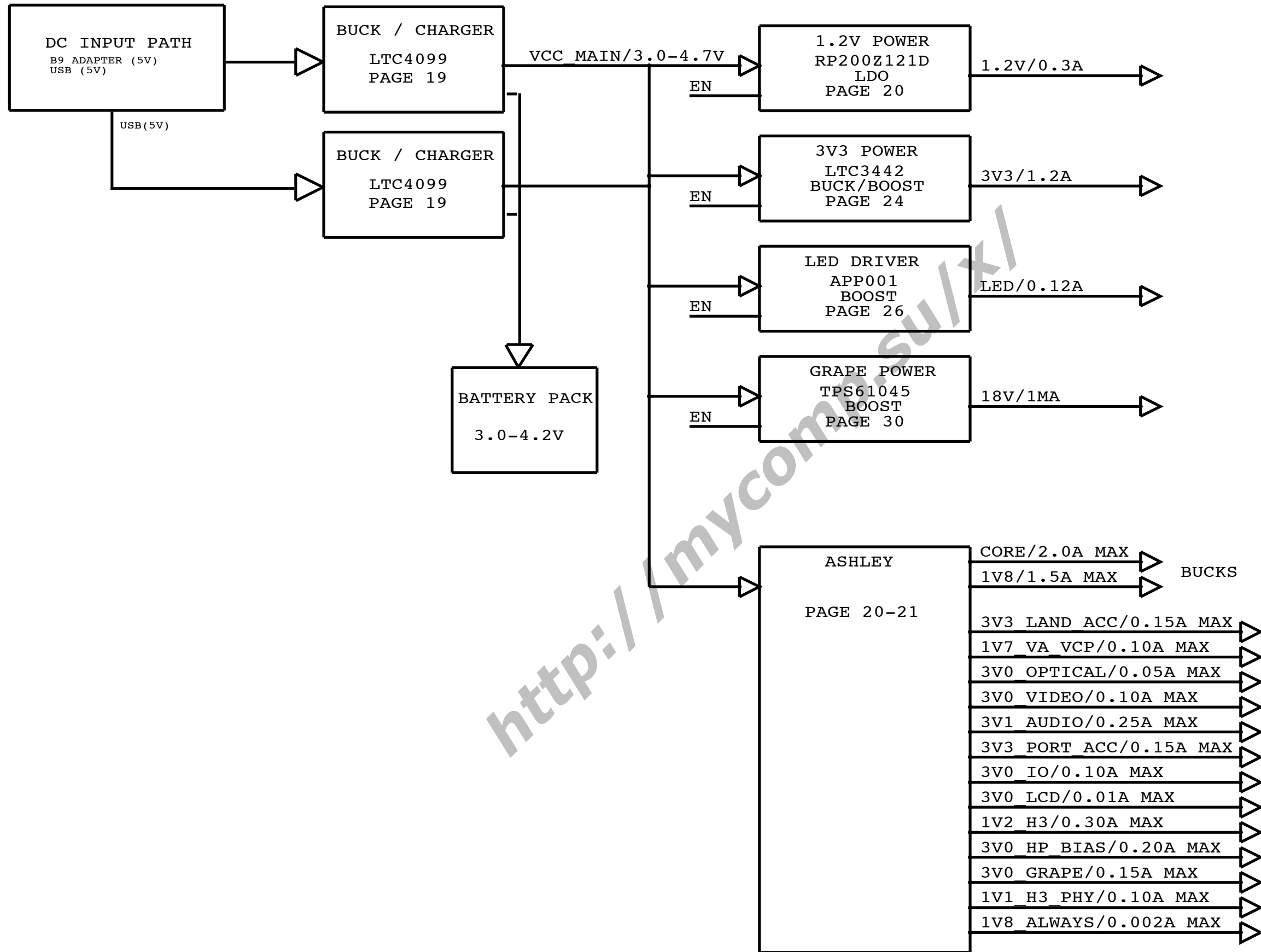
POWER PLANE	ON STATE



SYNC MASTER=ALEX SYNC DATE=05/02/2009

SYSTEM BLOCK DIAGRAM	
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POWER BLOCK DIAGRAM



SYNC MASTER=MARK		SYNC DATE=12/04/2009	
POWER BLOCK DIAGRAM			
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Page Notes

Power aliases required by this page:
(NONE)

Signal aliases required by this page:
(NONE)

BOM options provided by this page:

ALL AVAIL BOM OPTIONS

COMMON
ALTERNATE
DEMUX
32GB_FLASH_SAM
32GB_FLASH
12GB_FLASH
16GB_FLASH
16GB_FLASH_TOSH
8GB_FLASH
8GB_FLASH_SAM
BKLT_PLL
CAMERA
JTAG_2_WIRE
JTAG_1_WIRE
PRODUCTION
DEVELOPMENT
ADD19
MIKEY
INTERNAL_MIC
LANDSCAPE_DOCK
LEFT_HS
LINE_OUT_1
LINE_OUT_2
PORTRAIT_DOCK

SPEAKER

ADD DEVELOPMENT AND OTHER BOMS ONCE YOU GET BOM NUMBERS

BOM GROUP	BOM OPTIONS
BASIC	COMMON, ALTERNATE
AUDIO	LEFT_HS, SPEAKER, INTERNAL_MIC

USE SCHUTIL BOMCONFIG TO GENERATE CONFIG FILE.
PUT CONFIG FILE AT SAME LEVEL AS .CPM FILE
USE "READ BOM-CONFIG" BUTTON ON DMS TO READ IN BOMS

BOM OPTIONS

PROGRAMMABLE PARTS

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
-------	-----	-------------	-------------------------	------------

SCH AND BOARD P/N

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
051-8245	1	SCHM,VICE,MLB,K48	SCH1	
820-2740	1	PCBA,VICE,MLB,K48	PCB1	

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
085-1028	1	DEV,VICE,MLB,K48	DEV1	K48_DEV
085-1133	1	DEV,VICE,MLB,K48H	DEV1	K48M_DEV

VICE
BOM
OPTIONS

USE 825-6447
NEED MORE LINE ITEMS FOR OTHER CONFIGURATIONS
BARCODE LABEL/EEE CODES

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
825-7456	1	EEE FOR 639-0455 (16G)	EEE_BWY	CRITICAL	EEE_16G
825-7456	1	EEE FOR 639-0601 (32G)	EEE_D66	CRITICAL	EEE_32G
825-7456	1	EEE FOR 639-0598 (64G)	EEE_D61	CRITICAL	EEE_64G
825-7456	1	EEE FOR 639-0602 (16G)M	EEE_D67	CRITICAL	EEE_16G_M
825-7456	1	EEE FOR 639-0599 (32G)M	EEE_D62	CRITICAL	EEE_32G_M
825-7456	1	EEE FOR 639-0600 (64G)M	EEE_D63	CRITICAL	EEE_64G_M

SYNC MASTER=MIAMI SYNC DATE=08/06/2009

CONFIGURATION OPTIONS

Apple Inc.

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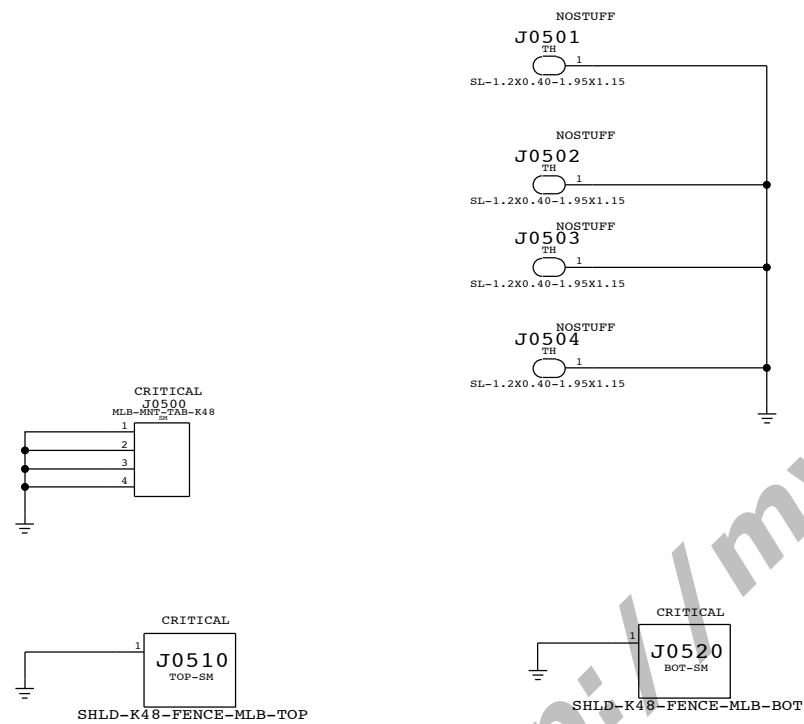
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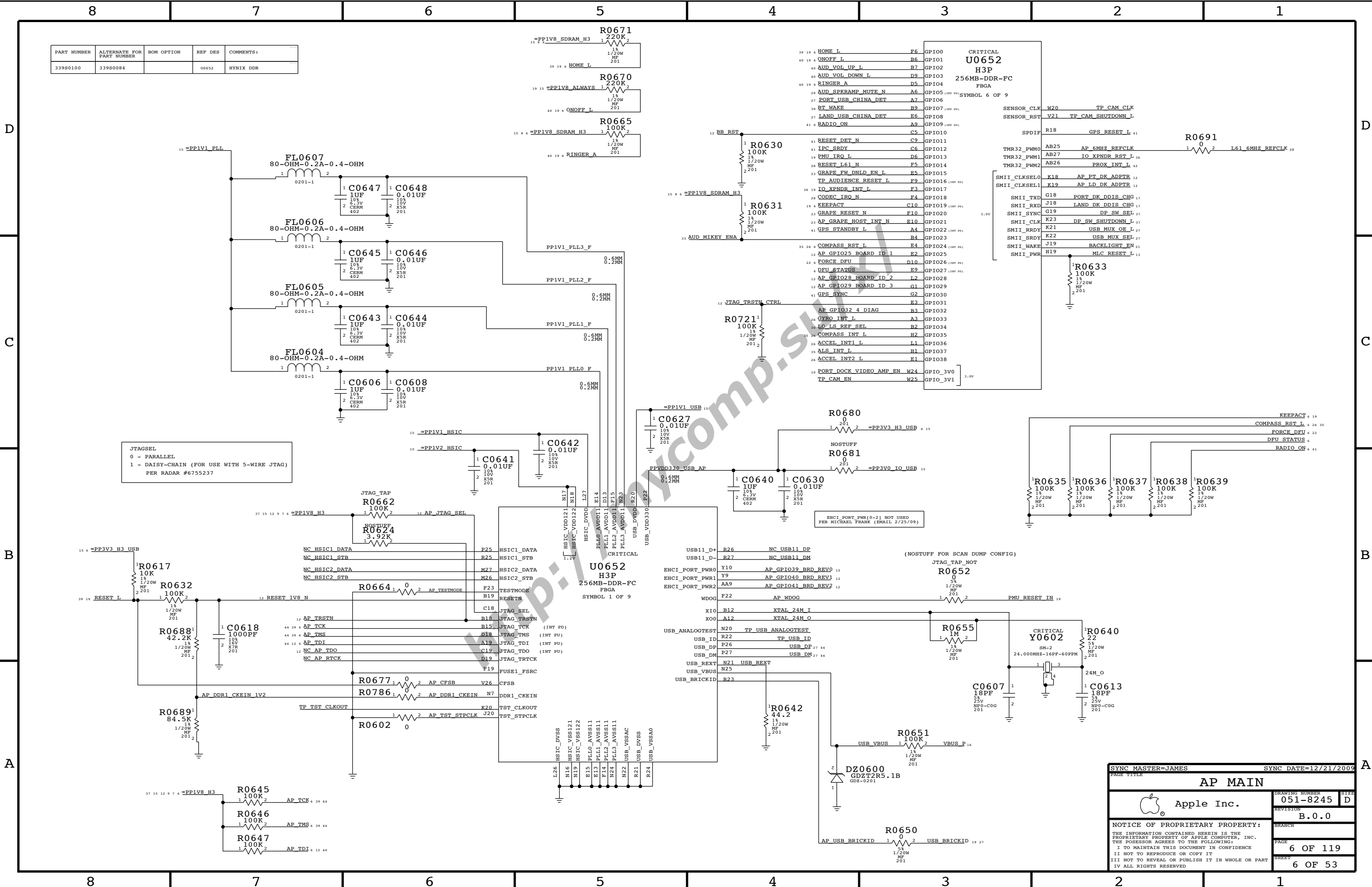
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PAGE TITLE FUNC/ICT TEST/BRACKETS			
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PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
339S0100	339S0084		U0652	HYNIX DDR



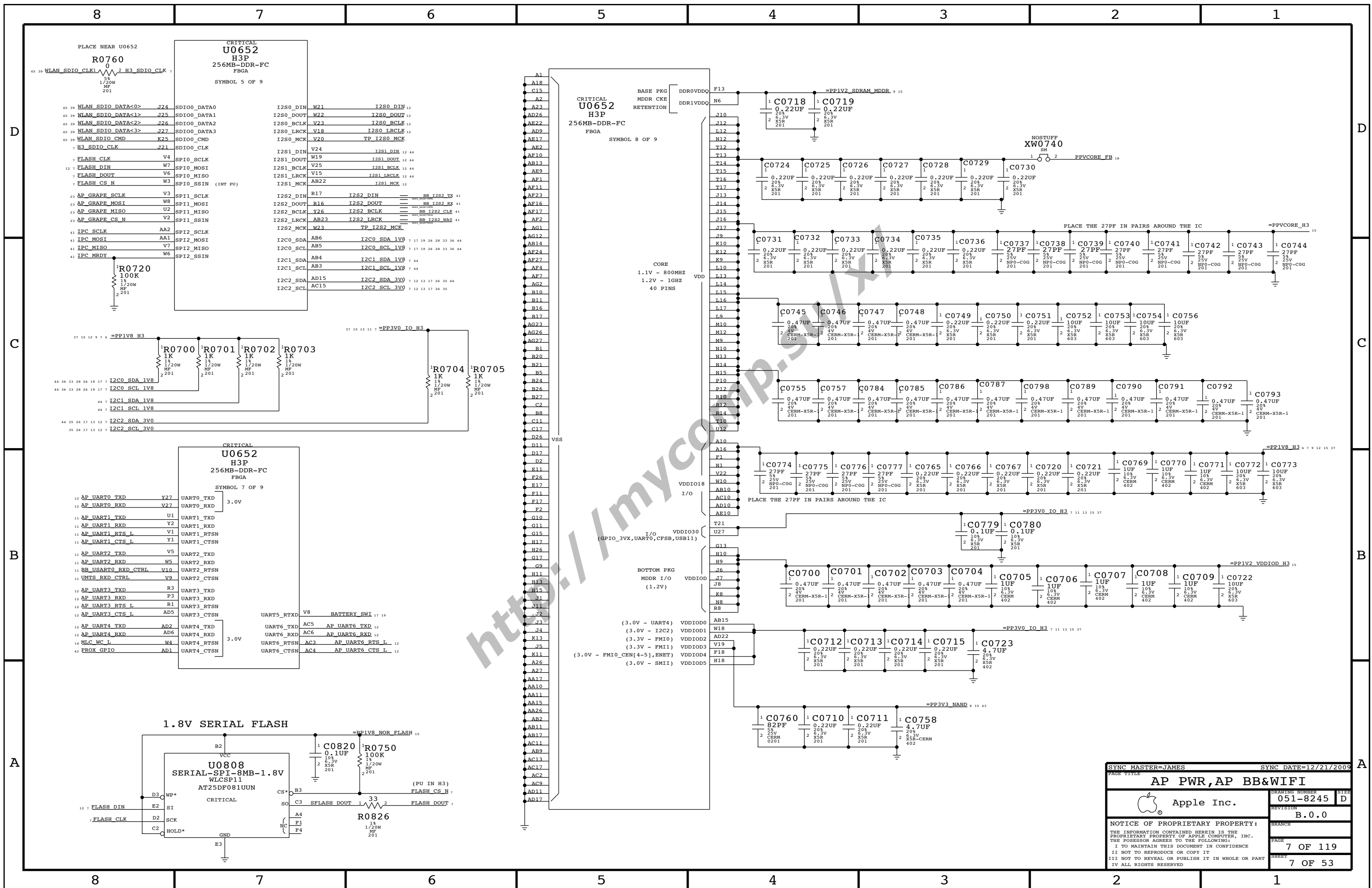
JTAGSEL
 0 - PARALLEL
 1 - DAISY-CHAIN (FOR USE WITH 5-WIRE JTAG)
 PER RADAR #6755237

EHCI PORT PWR(0-2) NOT USED
 PER MICHAEL FRANK (EMAIL 2/25/09)

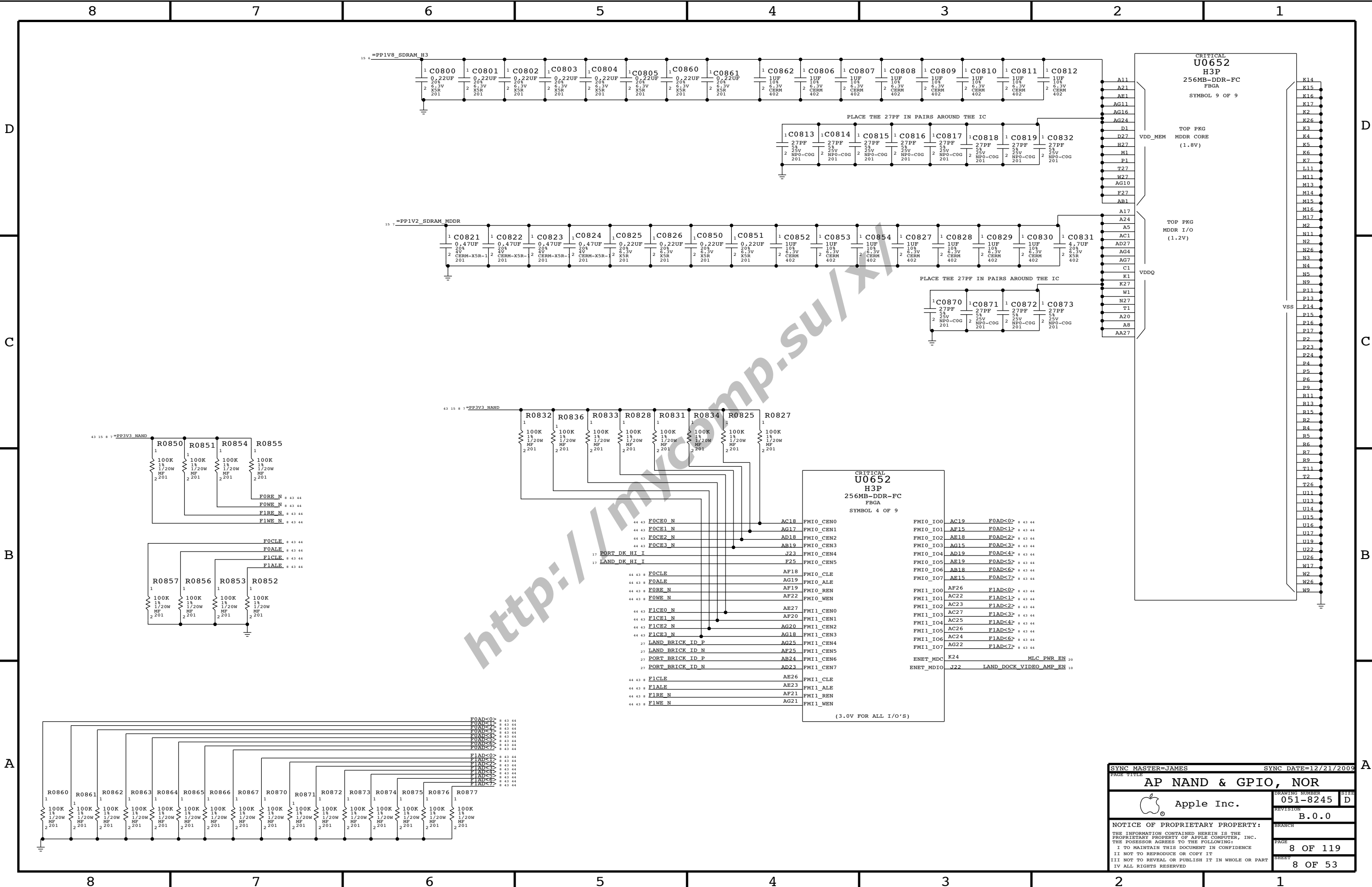
(NOSTUFF FOR SCAN DUMP CONFIG)

USB11_D+	R26	NC USB11_DP
USB11_D-	R27	NC USB11_DM
EHCI_PORT_PWR0	Y10	AP_GPIO39_BRD_REV0
EHCI_PORT_PWR1	Y9	AP_GPIO40_BRD_REV1
EHCI_PORT_PWR2	AA9	AP_GPIO41_BRD_REV2
WD0G	F22	AP_WDOG
X10	B12	XTAL_24M_I
X00	A12	XTAL_24M_O
USB_ANALOGTEST	N20	TP_USB_ANALOGTEST
USB_ID	R22	TP_USB_ID
USB_DP	P26	USB_DP_27_44
USB_DM	P27	USB_DM_27_44
USB_REXT	N21	USB_REXT
USB_VBUS	N25	USB_VBUS
USB_BRICKID	R23	USB_BRICKID

PAGE TITLE		SYNC DATE=12/21/2009	
AP MAIN			
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AP PWR, AP BB&WiFi		DRAWING NUMBER	SIZE
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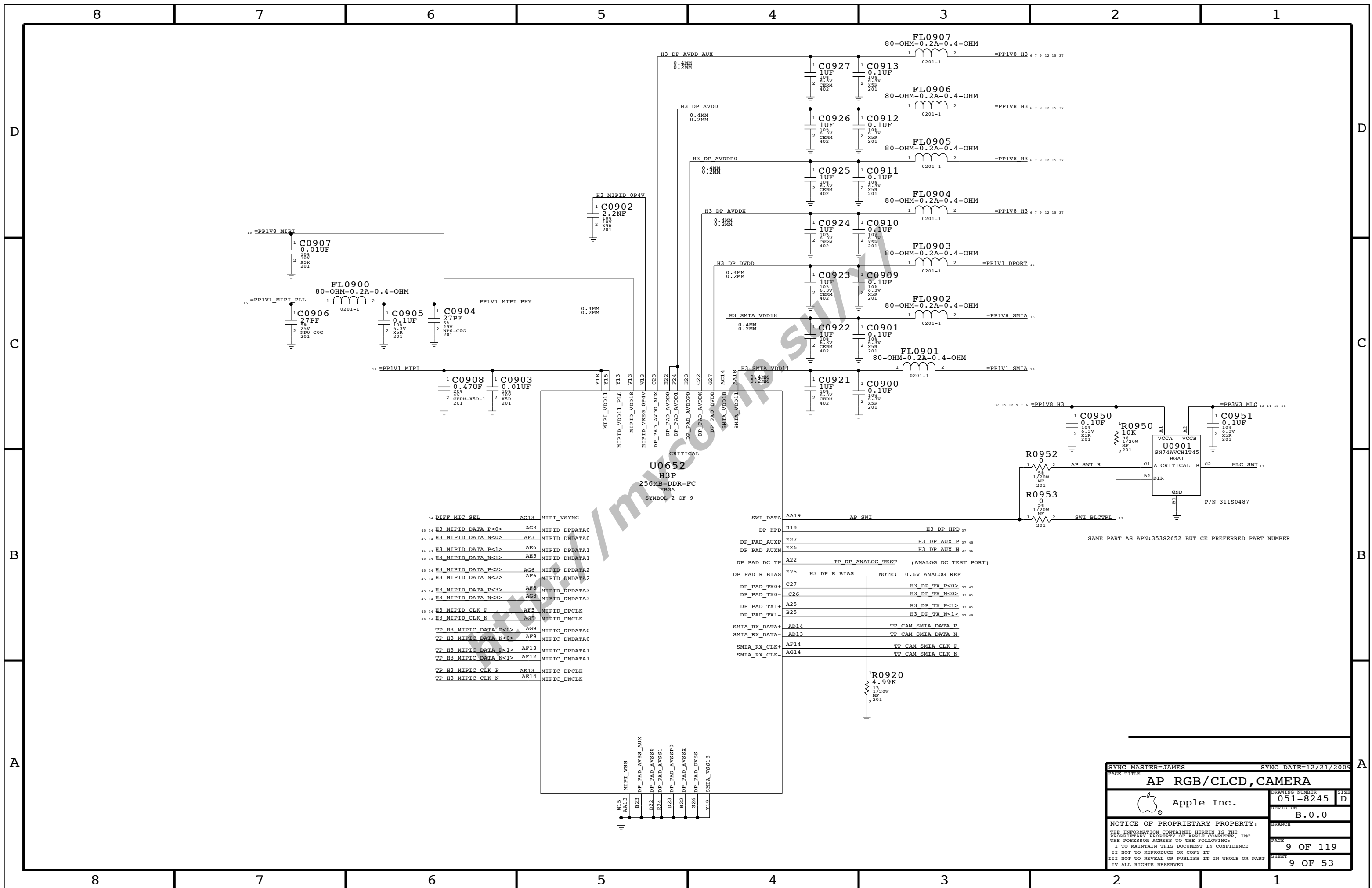
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CRITICAL
U0652
H3P
256MB-DDR-FC
FBGA
SYMBOL 4 OF 9

44 43	FOCE0_N	AC18	FMIO_CEN0	FMIO_I00	AC19	FOAD<0>	43 44
44 43	FOCE1_N	AG17	FMIO_CEN1	FMIO_I01	AF15	FOAD<1>	43 44
44 43	FOCE2_N	AD18	FMIO_CEN2	FMIO_I02	AE18	FOAD<2>	43 44
44 43	FOCE3_N	AB19	FMIO_CEN3	FMIO_I03	AG15	FOAD<3>	43 44
17	PORT_DK_HI_I	J23	FMIO_CEN4	FMIO_I04	AD19	FOAD<4>	43 44
17	LAND_DK_HI_I	F25	FMIO_CEN5	FMIO_I05	AE19	FOAD<5>	43 44
44 43	FOCLE	AF18	FMIO_CLE	FMIO_I06	AB18	FOAD<6>	43 44
44 43	FOALE	AG19	FMIO_ALE	FMIO_I07	AE15	FOAD<7>	43 44
44 43	FORE_N	AF19	FMIO_REN	FMII_I00	AF26	FIAD<0>	43 44
44 43	FOWE_N	AF22	FMIO_WEN	FMII_I01	AC22	FIAD<1>	43 44
44 43	FICE0_N	AE27	FMII_CEN0	FMII_I02	AC23	FIAD<2>	43 44
44 43	FICE1_N	AF20	FMII_CEN1	FMII_I03	AC27	FIAD<3>	43 44
44 43	FICE2_N	AG20	FMII_CEN2	FMII_I04	AC25	FIAD<4>	43 44
44 43	FICE3_N	AG18	FMII_CEN3	FMII_I05	AC26	FIAD<5>	43 44
27	LAND_BRICK_ID_P	AG25	FMII_CEN4	FMII_I06	AC24	FIAD<6>	43 44
27	LAND_BRICK_ID_N	AE25	FMII_CEN5	FMII_I07	AG22	FIAD<7>	43 44
27	PORT_BRICK_ID_P	AB24	FMII_CEN6	ENET_MDC	K24	MLC_PWR_EN	20
27	PORT_BRICK_ID_N	AD23	FMII_CEN7	ENET_MDIO	J22	LAND_DOCK_VIDEO_AMP_EN	10
44 43	FICLE	AE26	FMII_CLE				
44 43	FIALE	AE23	FMII_ALE				
44 43	FIRE_N	AF21	FMII_REN				
44 43	FIWE_N	AG21	FMII_WEN				

(3.0V FOR ALL I/O'S)

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AP NAND & GPIO, NOR			
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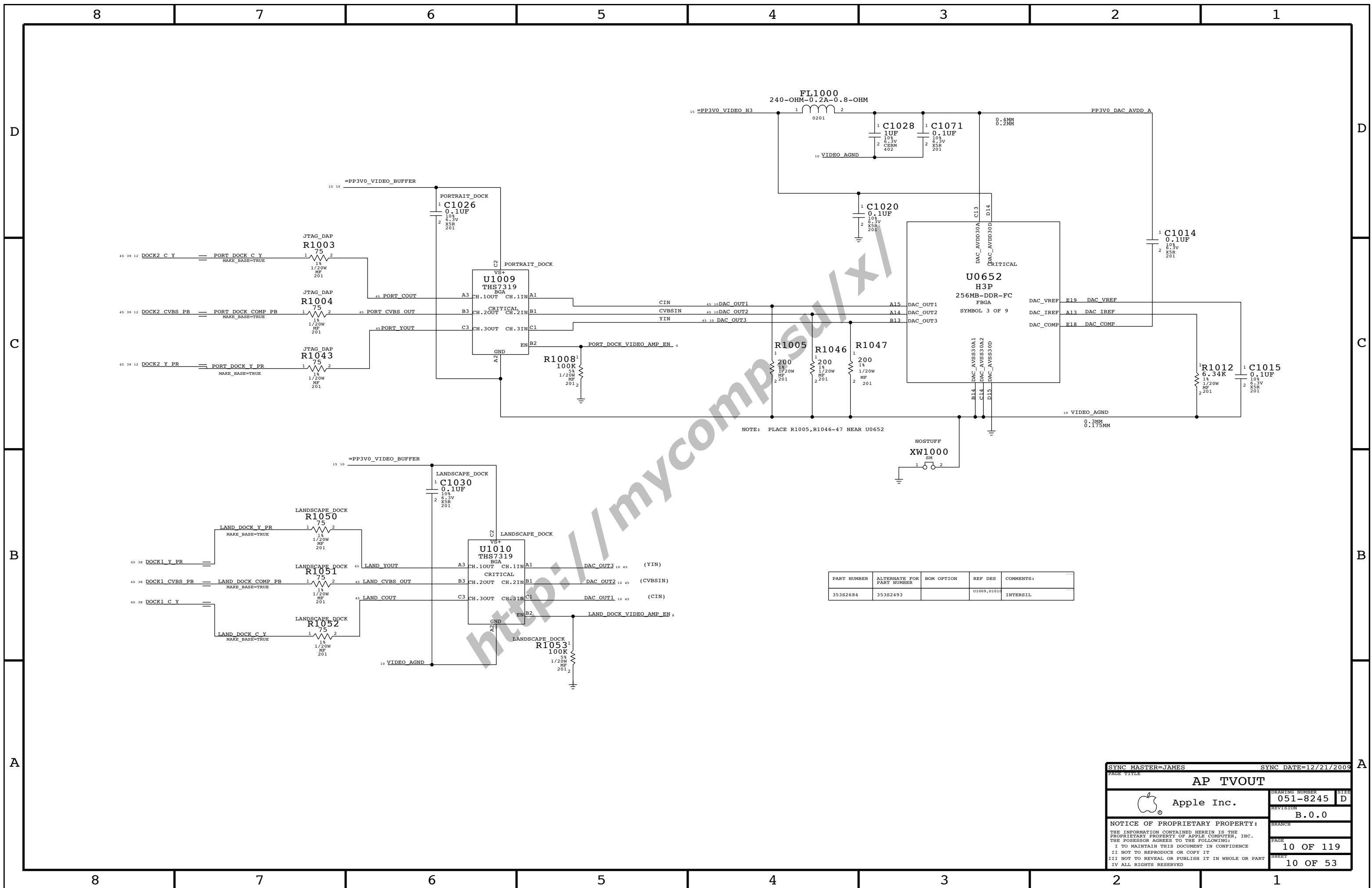
U0652
H3P
256MB-DDR-FC
FBGA
SYMBOL 2 OF 9

34 DIFF MIC SEL	AG13	MIPI_VSYNC
45 H3_MIPID_DATA_P<0>	AG3	MIPI_DPDATA0
45 H3_MIPID_DATA_N<0>	AF3	MIPI_DNDATA0
45 H3_MIPID_DATA_P<1>	AE6	MIPI_DPDATA1
45 H3_MIPID_DATA_N<1>	AE5	MIPI_DNDATA1
45 H3_MIPID_DATA_P<2>	AG6	MIPI_DPDATA2
45 H3_MIPID_DATA_N<2>	AF6	MIPI_DNDATA2
45 H3_MIPID_DATA_P<3>	AF8	MIPI_DPDATA3
45 H3_MIPID_DATA_N<3>	AG8	MIPI_DNDATA3
45 H3_MIPID_CLK_P	AF5	MIPI_DPCLK
45 H3_MIPID_CLK_N	AG5	MIPI_DNCLK
TP H3_MIPIC_DATA_P<0>	AG9	MIPI_DPDATA0
TP H3_MIPIC_DATA_N<0>	AF9	MIPI_DNDATA0
TP H3_MIPIC_DATA_P<1>	AF13	MIPI_DPDATA1
TP H3_MIPIC_DATA_N<1>	AF12	MIPI_DNDATA1
TP H3_MIPIC_CLK_P	AE13	MIPI_DPCLK
TP H3_MIPIC_CLK_N	AE14	MIPI_DNCLK

SWI_DATA	AA19	AP_SWI
DP_HPD	R19	H3_DP_HPD
DP_PAD_AUXP	E27	H3_DP_AUX_P
DP_PAD_AUXN	E26	H3_DP_AUX_N
DP_PAD_DC_TP	A22	TP_DP_ANALOG_TEST (ANALOG DC TEST PORT)
DP_PAD_R_BIAS	E25	H3_DP_R_BIAS
DP_PAD_TX0+	C27	H3_DP_TX_P<0>
DP_PAD_TX0-	C26	H3_DP_TX_N<0>
DP_PAD_TX1+	A25	H3_DP_TX_P<1>
DP_PAD_TX1-	B25	H3_DP_TX_N<1>
SMIA_RX_DATA+	AD14	TP_CAM_SMIA_DATA_P
SMIA_RX_DATA-	AD13	TP_CAM_SMIA_DATA_N
SMIA_RX_CLK+	AF14	TP_CAM_SMIA_CLK_P
SMIA_RX_CLK-	AG14	TP_CAM_SMIA_CLK_N

SYNC MASTER=JAMES		SYNC DATE=12/21/2009	
PAGE TITLE			
AP RGB/CLCD, CAMERA			
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SAME PART AS APN:353S2652 BUT CE PREFERRED PART NUMBER



NOTE: PLACE R1005, R1046-47 NEAR U0652

NOSTUFF

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
35382684	35382493		U1009, U1010	INTERSIL

SYNC MASTER=JAMES SYNC DATE=12/21/2009

AP TVOUT

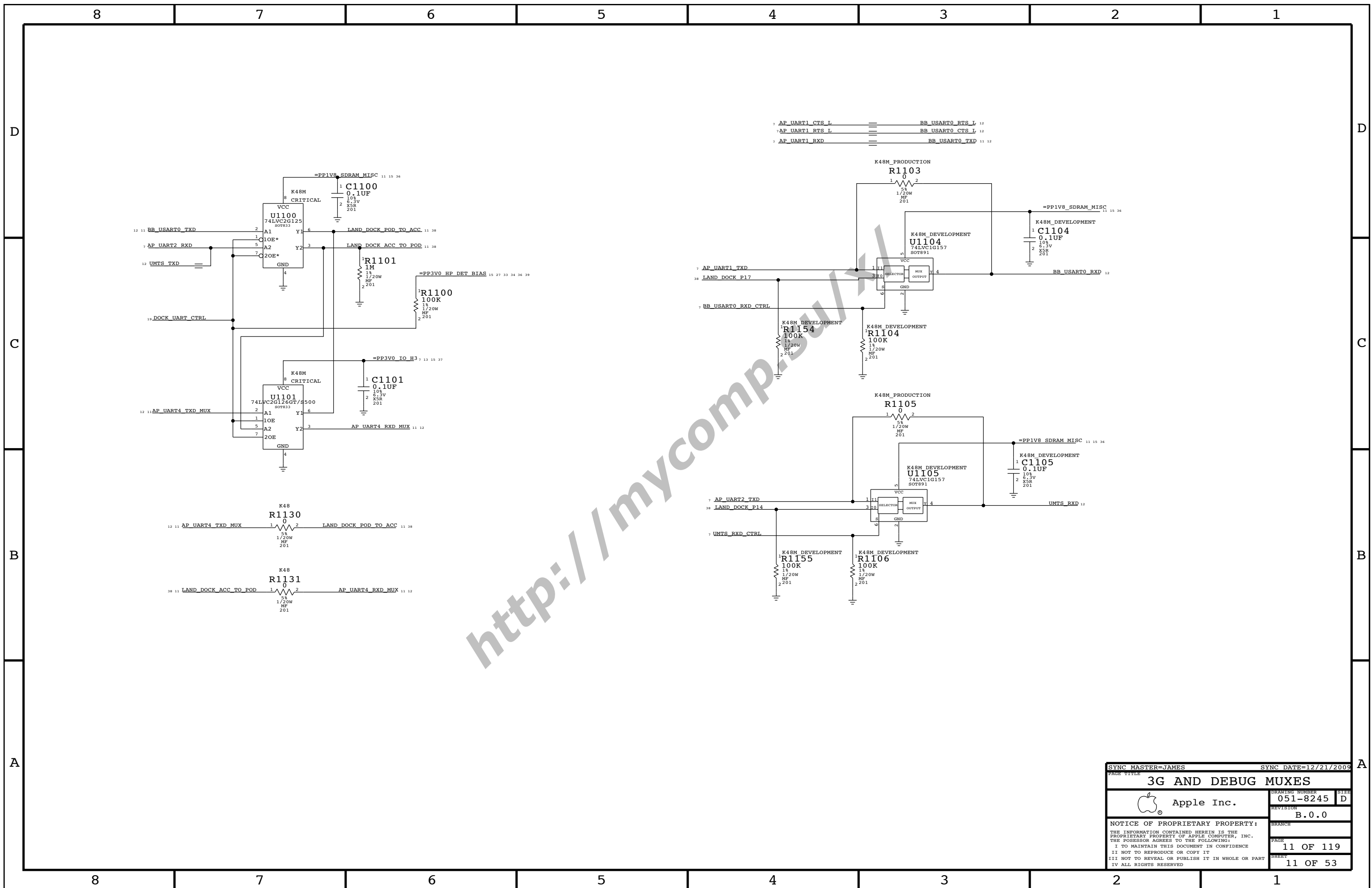
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REVISION: B.0.0

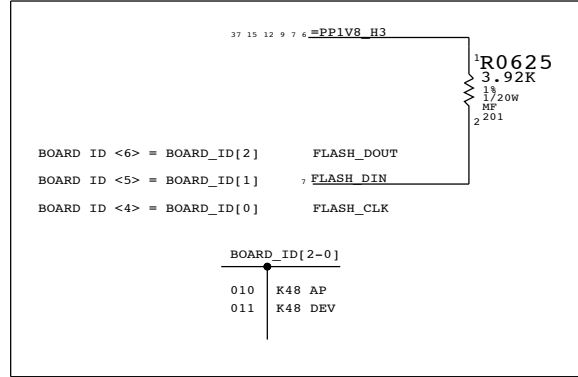
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SYNC MASTER=JAMES		SYNC DATE=12/21/2009	
PAGE TITLE 3G AND DEBUG MUXES			
Apple Inc.	DRAWING NUMBER	051-8245	SIZE D
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BOARD ID



ALIASES FROM PAGE 7

- 7 I2S0 DIN == AUD_I2S_SDOUT_L61 28
- 7 I2S0 DOUT == AUD_I2S_SDIN_L61 28
- 7 I2S0 BCLK == AUD_I2S_BITCLK_L61 28
- 7 I2S0 LRCLK == AUD_I2S_LRC_L61 28
- 44 I2S1 DIN == BB_I2S1_RX 32 41
- 44 I2S1 DOUT == BB_I2S1_TX 32 41
- 44 I2S1 BCLK == BB_I2S1_CLK 32 41
- 44 I2S1 LRCLK == BB_I2S1_WAG 32 41
- 7 I2S1 MCK == L61_I2S_MCLK 28
- 7 AP_UART0_TXD == PORT_DOCK_POD_TO_ACC 39
- 7 AP_UART0_RXD == PORT_DOCK_ACC_TO_POD 39
- 7 AP_UART6_TXD == GPS_UART_RX 41
- 7 AP_UART6_RXD == GPS_UART_TX 41
- 7 AP_UART6_RTS_L == GPS_UARTS_CTS_L 41
- 7 AP_UART6_CTS_L == GPS_UARTS_RTS_L 41

ALIASES FROM PAGE 11

- 11 BB_USART0_TXD == BB_USART0_TXD_CONN 41
- 11 BB_USART0_RXD == BB_USART0_RXD_CONN 41
- 11 BB_USART0_RTS_L == BB_USART0_RTS_L_CONN 41
- 11 BB_USART0_CTS_L == BB_USART0_CTS_L_CONN 41
- 11 UMTS_TXD == UMTS_TXD_CONN 41
- 11 UMTS_RXD == UMTS_RXD_CONN 41
- 7 AP_UART4_TXD == AP_UART4_TXD_MUX 11
- 7 AP_UART4_RXD == AP_UART4_RXD_MUX 11
- 12 AP_GPIO41_BRD_REV2 == BB_FLASH_ACTIVE 41

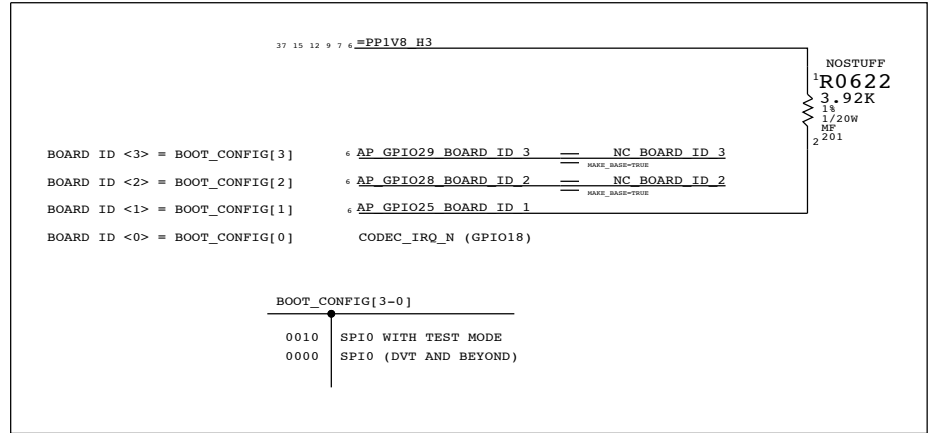
ALIASES FROM PAGE 50

- 39 LAND_USB_PWR == USB_PWR_A 16
- 39 CONN_AP_UART3_RTS_L == AP_UART3_RTS_L 7
- 39 CONN_AP_UART3_CTS_L == AP_UART3_CTS_L 7
- 39 CONN_AP_UART3_TXD == AP_UART3_TXD 7
- 39 CONN_AP_UART3_RXD == AP_UART3_RXD 7

ALIASES FROM PAGE 51

- 39 CONN_AP_UART3_RTS_L == AP_UART3_RTS_L 7
- 39 CONN_AP_UART3_CTS_L == AP_UART3_CTS_L 7
- 39 CONN_AP_UART3_TXD == AP_UART3_TXD 7
- 39 CONN_AP_UART3_RXD == AP_UART3_RXD 7

BOOT CONFIG ID

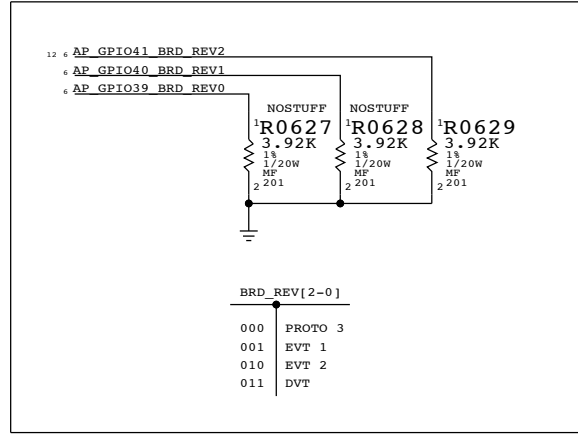


- 6 BB_RST == BB_RST_RADIO 41

ALIASES FOR PAGE 57

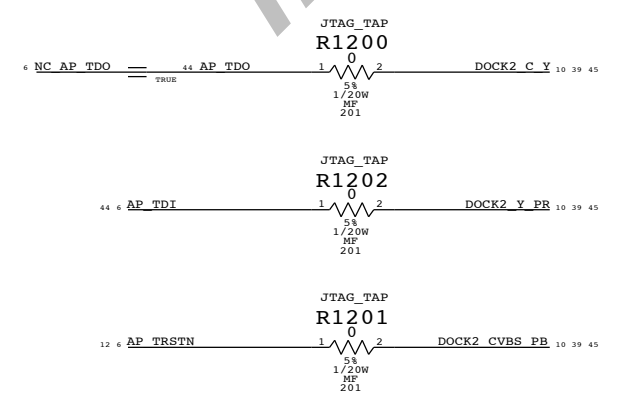
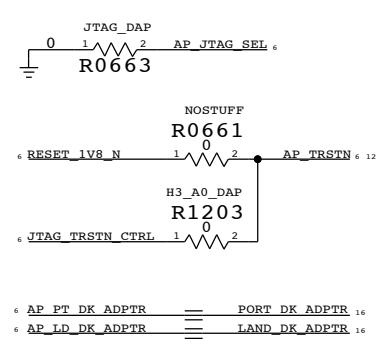
- 42 PROX_SDA_3V0 == I2C2_SDA_3V0 7 13 17 26 35 44
- 42 PROX_SCL_3V0 == I2C2_SCL_3V0 7 13 17 26 35
- 42 PROX_ACSHIELD_CONN == PROX_ACSHIELD 42

BOARD REVISION



http://mycomp.su/xl

NOTE: JTAG SCAN DUMP



FOR REFERENCE

0000	SPI0	BOARD_ID[2:0]
0001	SPI1	010 K48AP
0010	SPI0 W/TEST	011 K48 DEV
0011	SPI1 W/TEST	
0100	FMIO 2CS	
0101	FMIO 4CS	
0110	FMIO 4CS W/TEST	
0111	RESERVED	
1000	FMIO 2 CS	BOARD_REV[2:0]
1001	FMIO 4 CS	000 PROTO3
1010	FMIO 4CS W/TEST	
1100	FMIO/1 2/2 CS	
1101	FMIO/1 4/4 CS	
1110	FMIO/1 4/4 CS W/TEST	
1111	RESERVED	

SYNC MASTER=JAMES SYNC DATE=12/21/2009

AP MISC & ALIASES

Apple Inc.

DRAWING NUMBER: 051-8245 SIZE: D

REVISION: B.0.0

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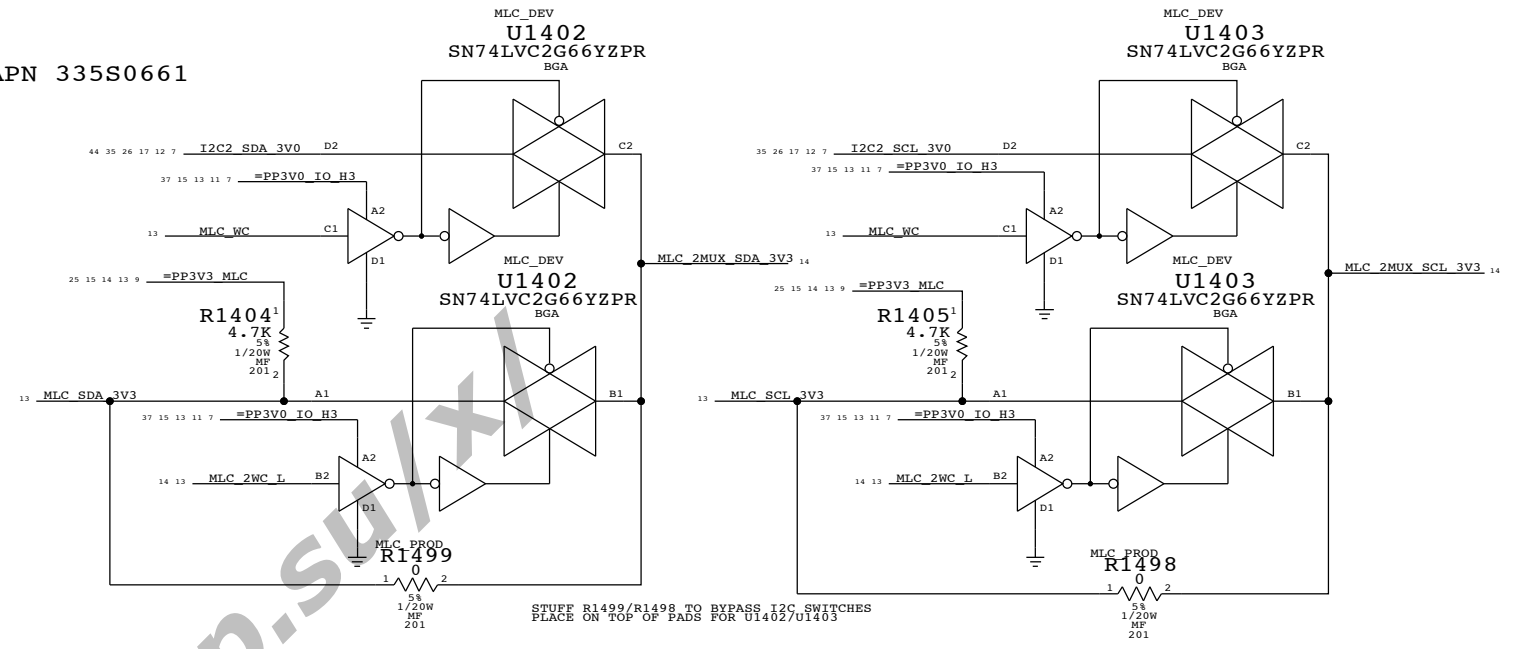
PAGE: 12 OF 119
 SHEET: 12 OF 53

MLC

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
34182604	1	MLC EEPROM 54MHZ LVDS,2MHZ SW	U1401	CRITICAL	54MHZ_PANEL
34182606	1	MLC EEPROM 100MHZ LVDS,2MHZ SW	U1401	CRITICAL	100MHZ_PANEL

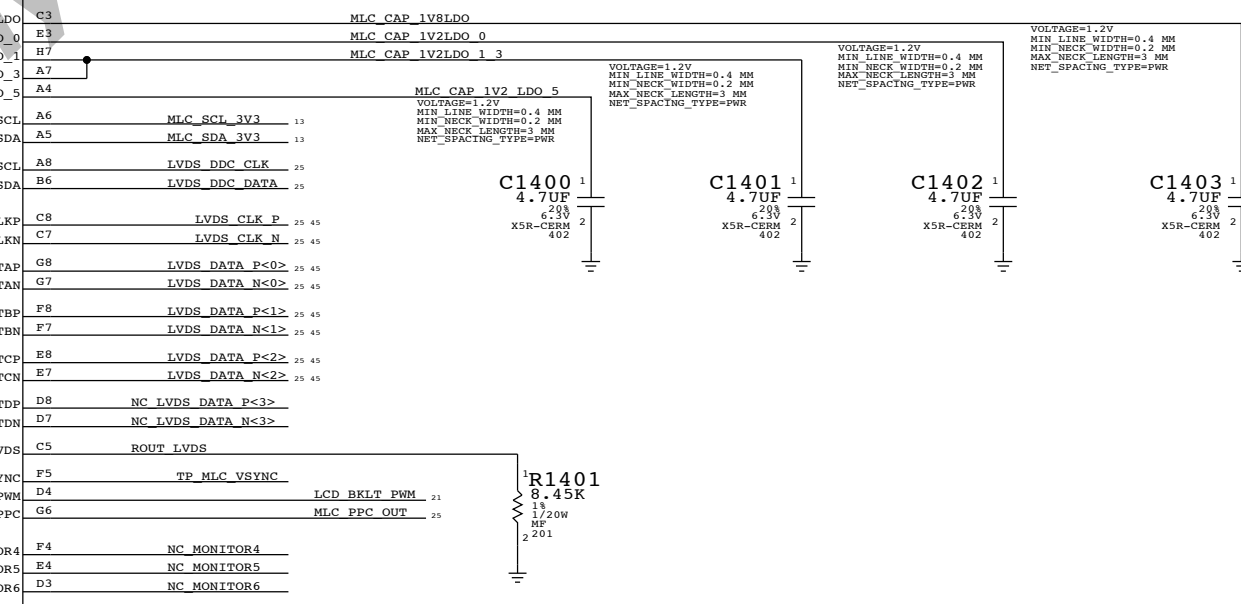
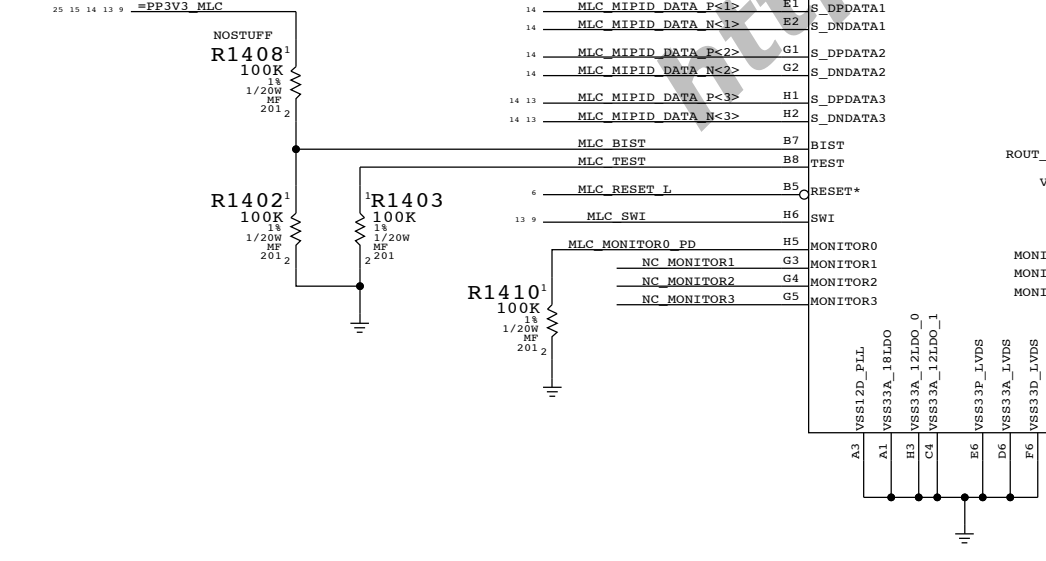
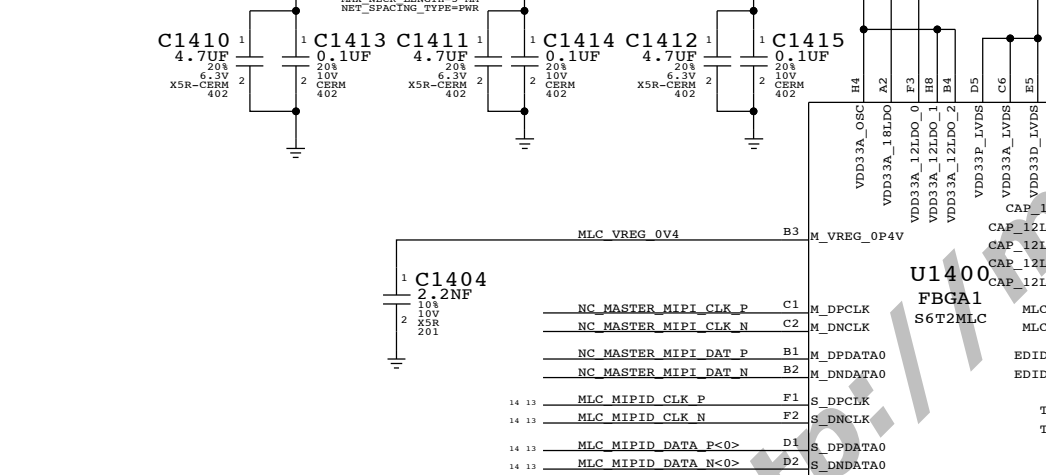
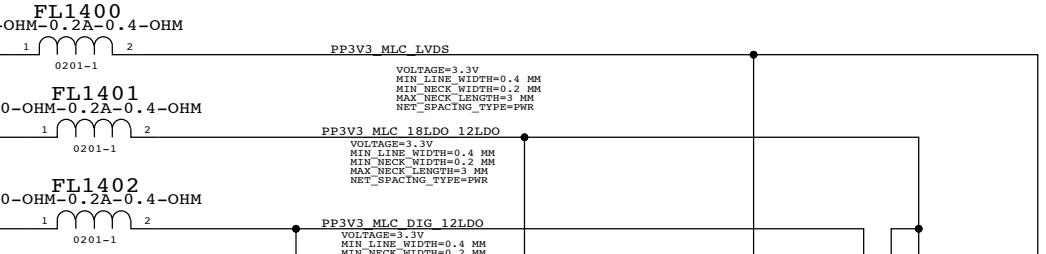
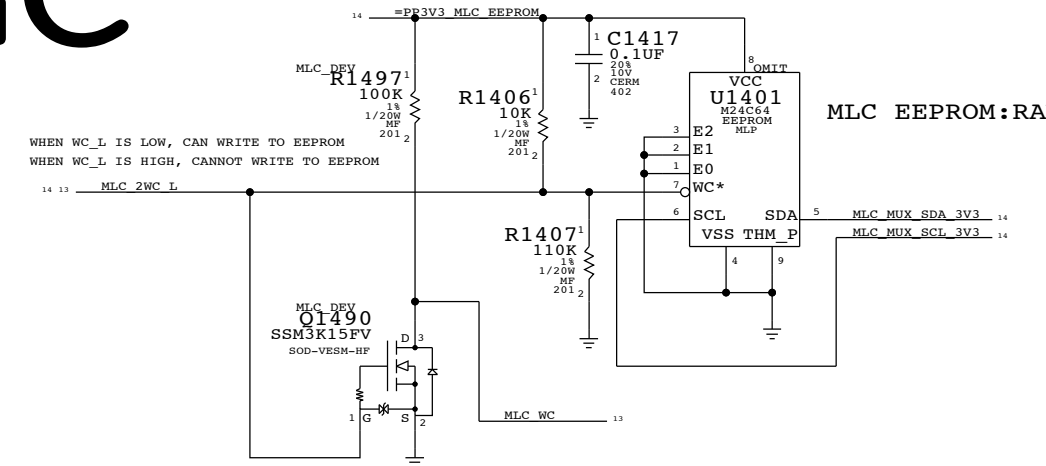
MLC EEPROM:RAW APN 335S0661

I2C MUXING CIRCUITRY



WC_L	SELECTED I2C
0	H3P CAN WRITE
1	MLC CAN READ

STUFF R1499/R1498 TO BYPASS I2C SWITCHES
PLACE ON TOP OF PADS FOR U1402/U1403



PAGE TITLE		SYNC DATE=09/16/2009	
MLC			
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		SHEET	
		13 OF 53	

MLC ALIASES


```

45 9 H3 MIPID_CLK_P == MLC MIPID_CLK_P 13
45 9 H3 MIPID_CLK_N MAKE_BASE=TRUE == MLC MIPID_CLK_N 13
      MAKE_BASE=TRUE
45 9 H3 MIPID_DATA_P<0> == MLC MIPID_DATA_P<0> 13
45 9 H3 MIPID_DATA_N<0> MAKE_BASE=TRUE == MLC MIPID_DATA_N<0> 13
      MAKE_BASE=TRUE
45 9 H3 MIPID_DATA_P<1> == MLC MIPID_DATA_P<1> 13
45 9 H3 MIPID_DATA_N<1> MAKE_BASE=TRUE == MLC MIPID_DATA_N<1> 13
      MAKE_BASE=TRUE
45 9 H3 MIPID_DATA_P<2> == MLC MIPID_DATA_P<2> 13
45 9 H3 MIPID_DATA_N<2> MAKE_BASE=TRUE == MLC MIPID_DATA_N<2> 13
      MAKE_BASE=TRUE
45 9 H3 MIPID_DATA_P<3> == MLC MIPID_DATA_P<3> 13
45 9 H3 MIPID_DATA_N<3> MAKE_BASE=TRUE == MLC MIPID_DATA_N<3> 13
      MAKE_BASE=TRUE

13 MLC_MUX_SDA_3V3 == MLC_2MUX_SDA_3V3 13
13 MLC_MUX_SCL_3V3 MAKE_BASE=TRUE == MLC_2MUX_SCL_3V3 13
      MAKE_BASE=TRUE
13 MLC_2WC_L == MLC_WC_L 7
13 =PP3V3_MLC_EEPROM MAKE_BASE=TRUE ==PP3V3_MLC 9 13 15 25
      MAKE_BASE=TRUE

```

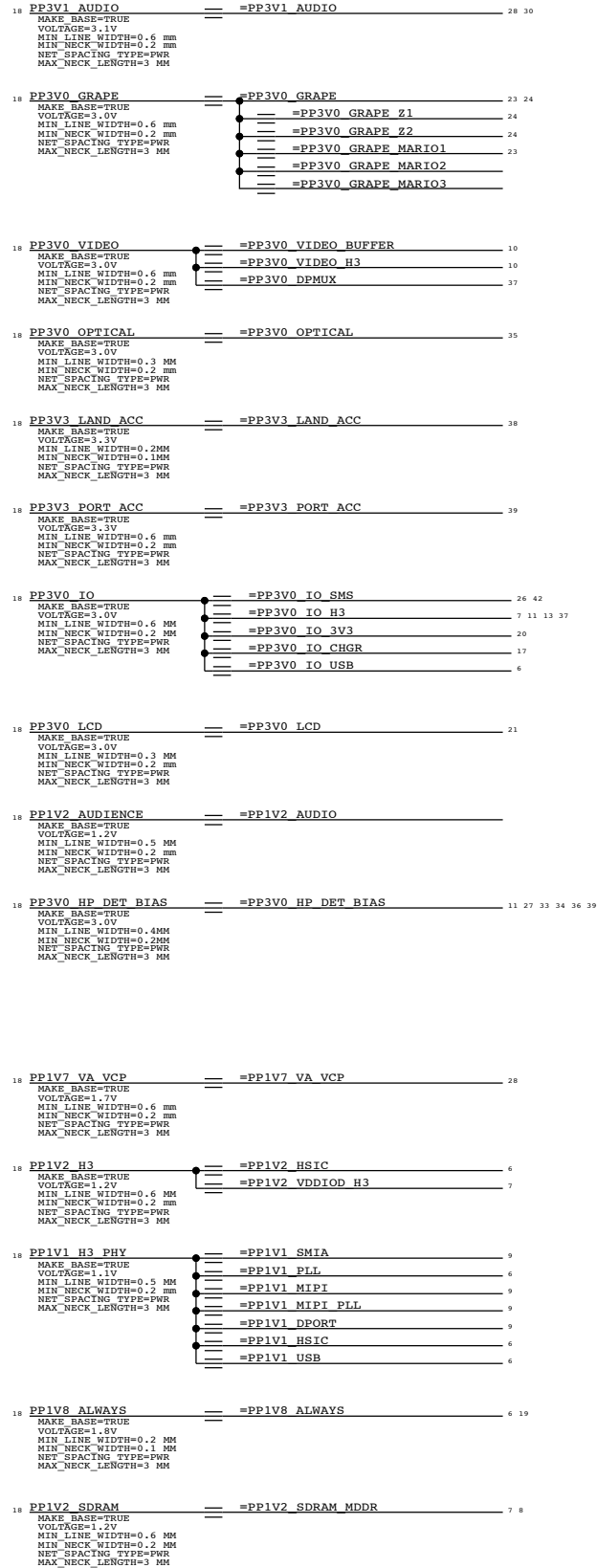
<http://mycomp.su/xl>

SYNC MASTER=MIAMI		SYNC DATE=09/16/2009	
PAGE TITLE			
MLC ALIASES			
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		PAGE	15 OF 119
		SHEET	14 OF 53
		SIZE	D

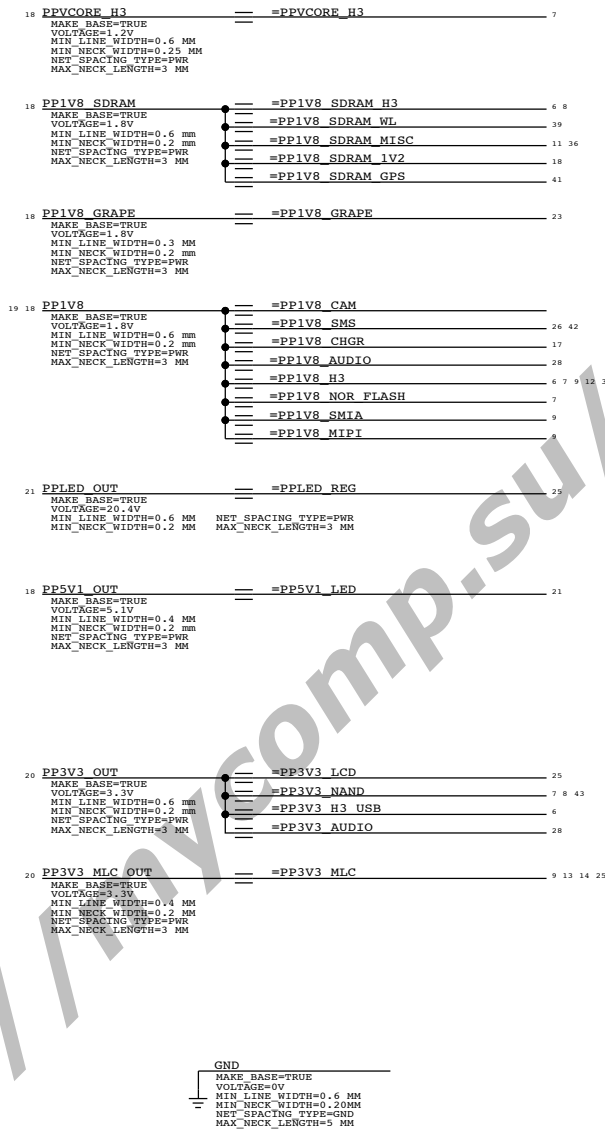
POWER CONN / ALIAS

LDO RAILS

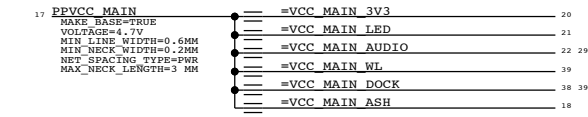
PROGRAMMABLE ON/OFF



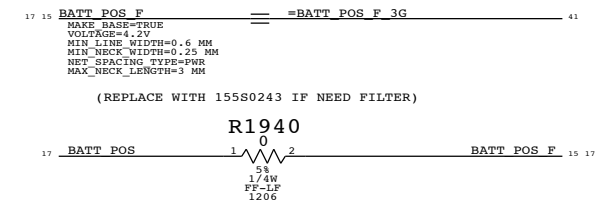
BUCK RAILS



CHARGER MAIN



BATTERY



http://www.su/xl.com

SYNC MASTER=MARK		SYNC DATE=12/04/2009	
Power Conn / Alias			
DRAWING NUMBER		SIZE	
051-8245		D	
REVISION		BRANCH	
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DCIN POWER PATH

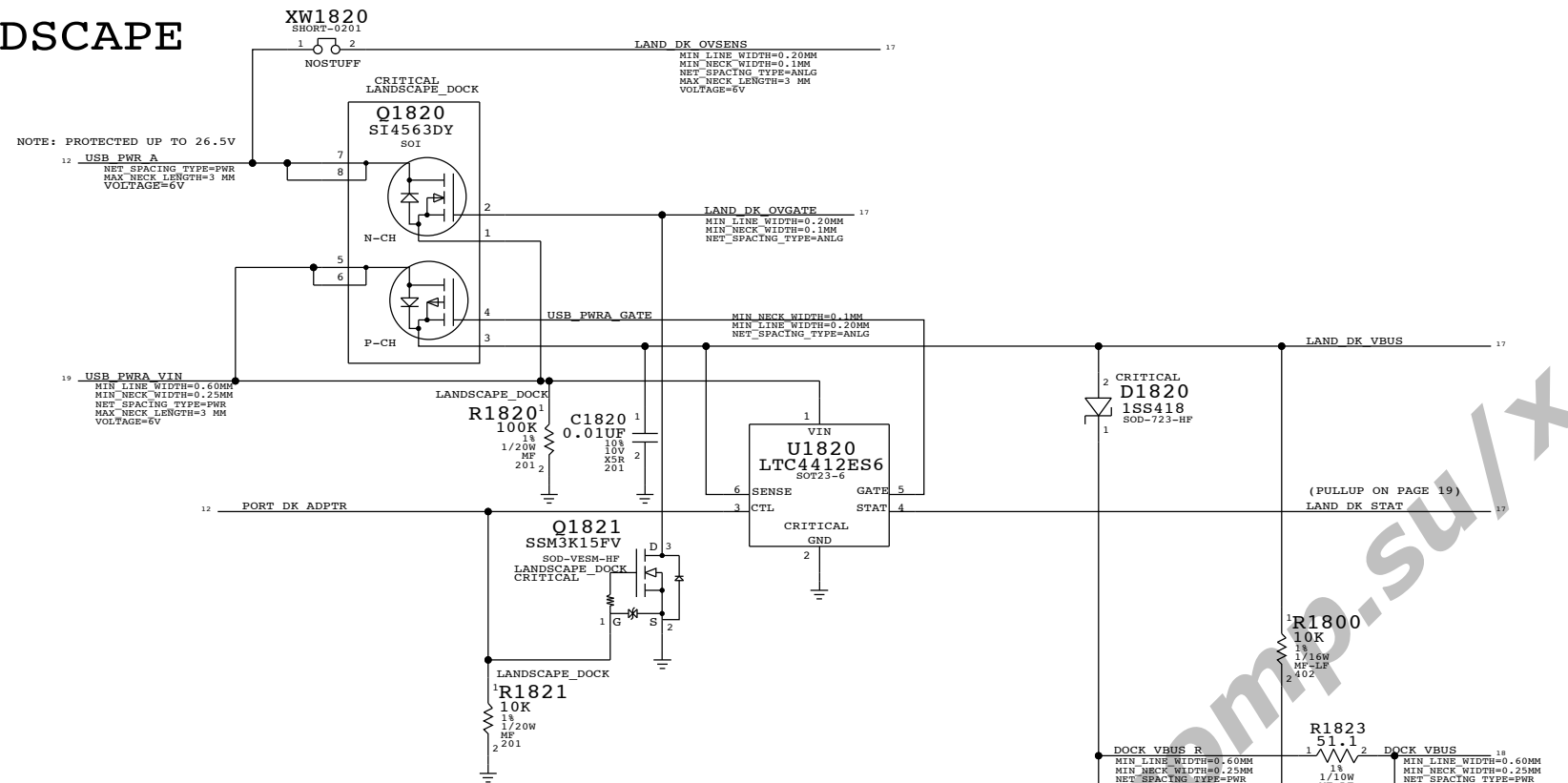
SO-8 DUAL P/N FETS

MOSFET	SI4563DY
CHANNEL	N-TYPE
RDS(ON)	15 MOHM @4.5V
IMAX	8 A
VGS MAX	+/- 16V

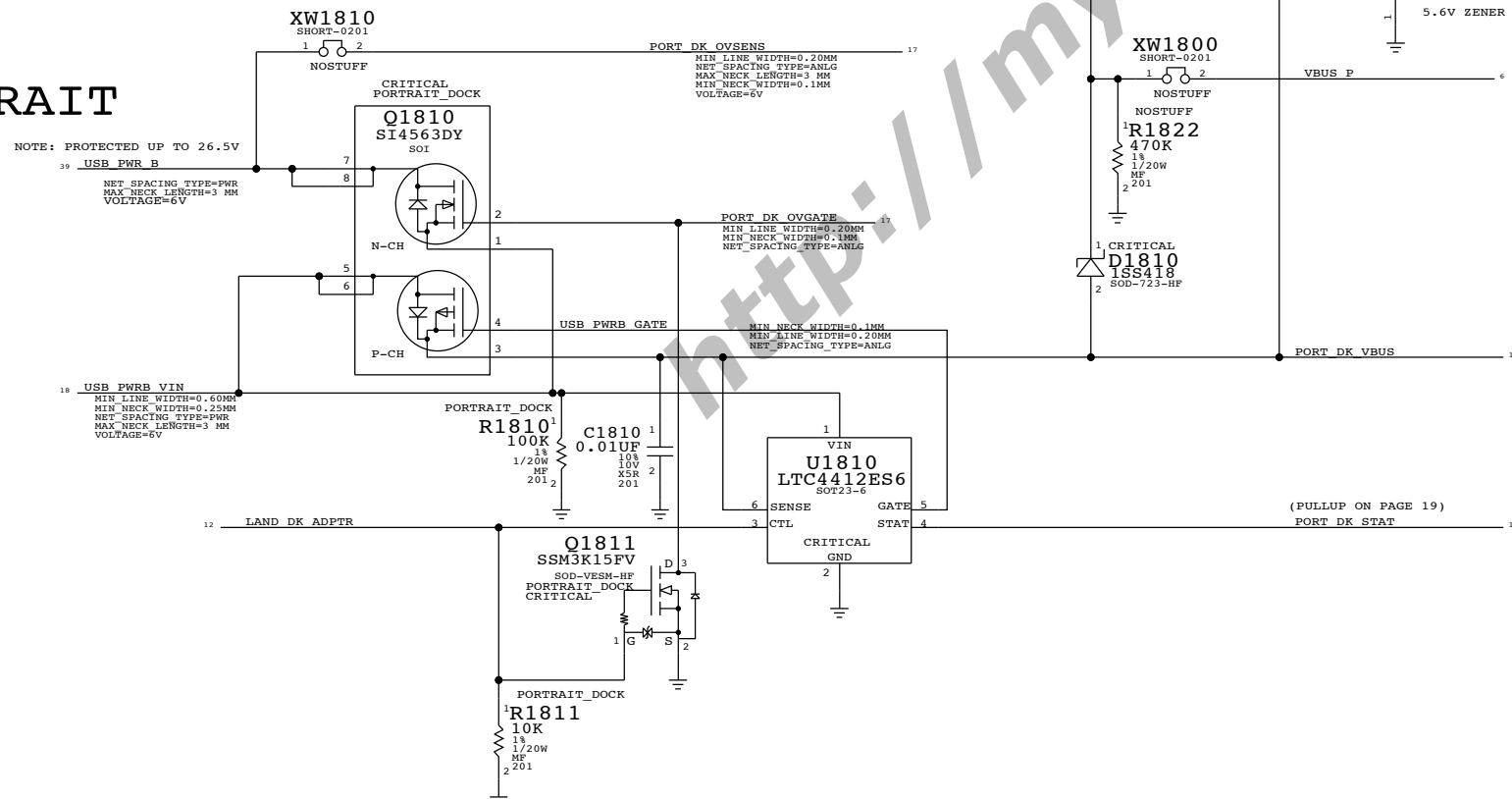
SO-8 DUAL P/N FETS

MOSFET	SI4563DY
CHANNEL	P-TYPE
RDS(ON)	25 MOHM @-4.5V
IMAX	8 A
VGS MAX	+/- 16V


LANDSCAPE



PORTRAIT

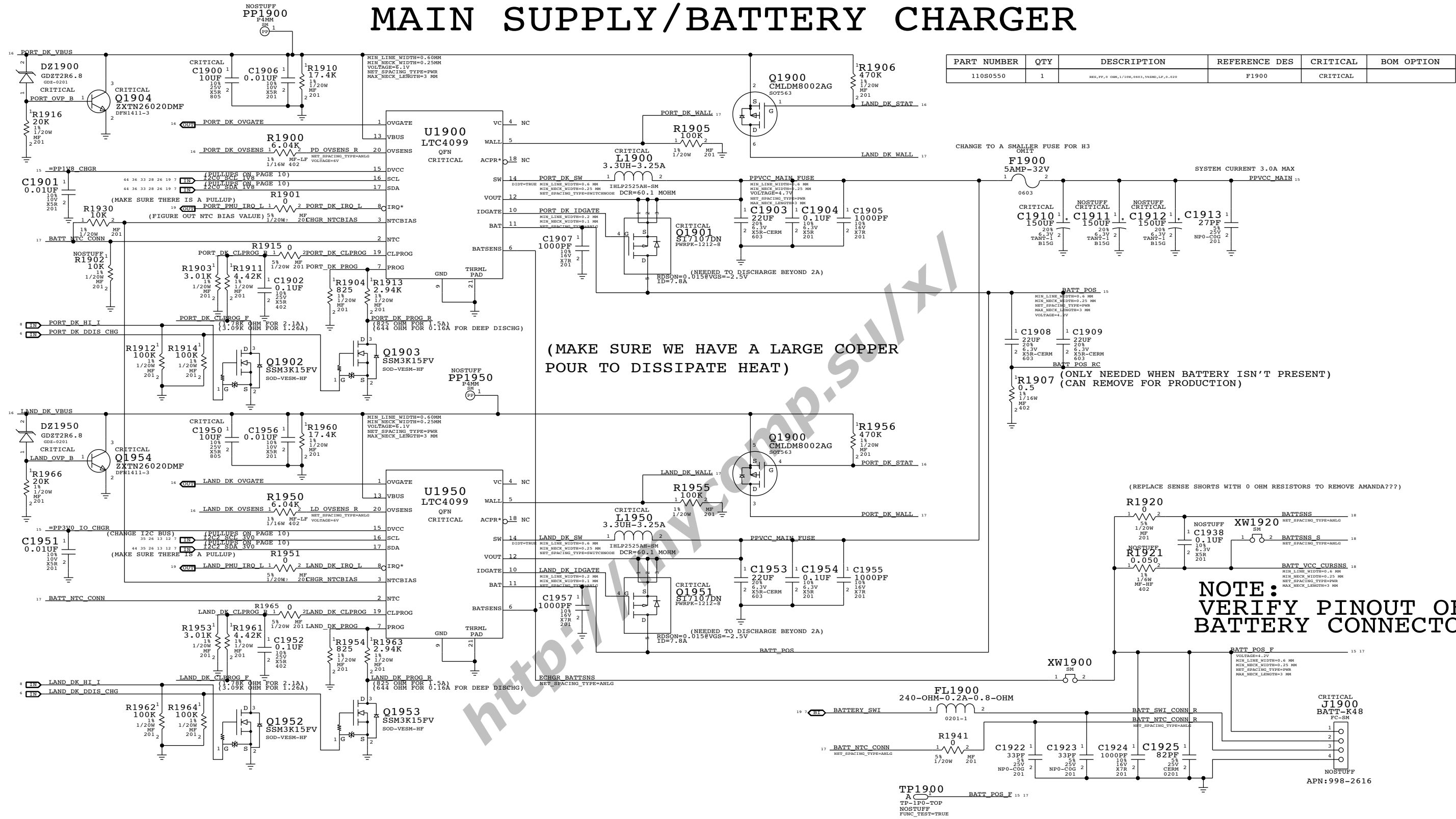


SYNC MASTER=MARK SYNC DATE=12/04/2009

DCIN POWER PATH	
 Apple Inc.	DRAWING NUMBER 051-8245 D
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	SHEET 16 OF 53

MAIN SUPPLY/BATTERY CHARGER

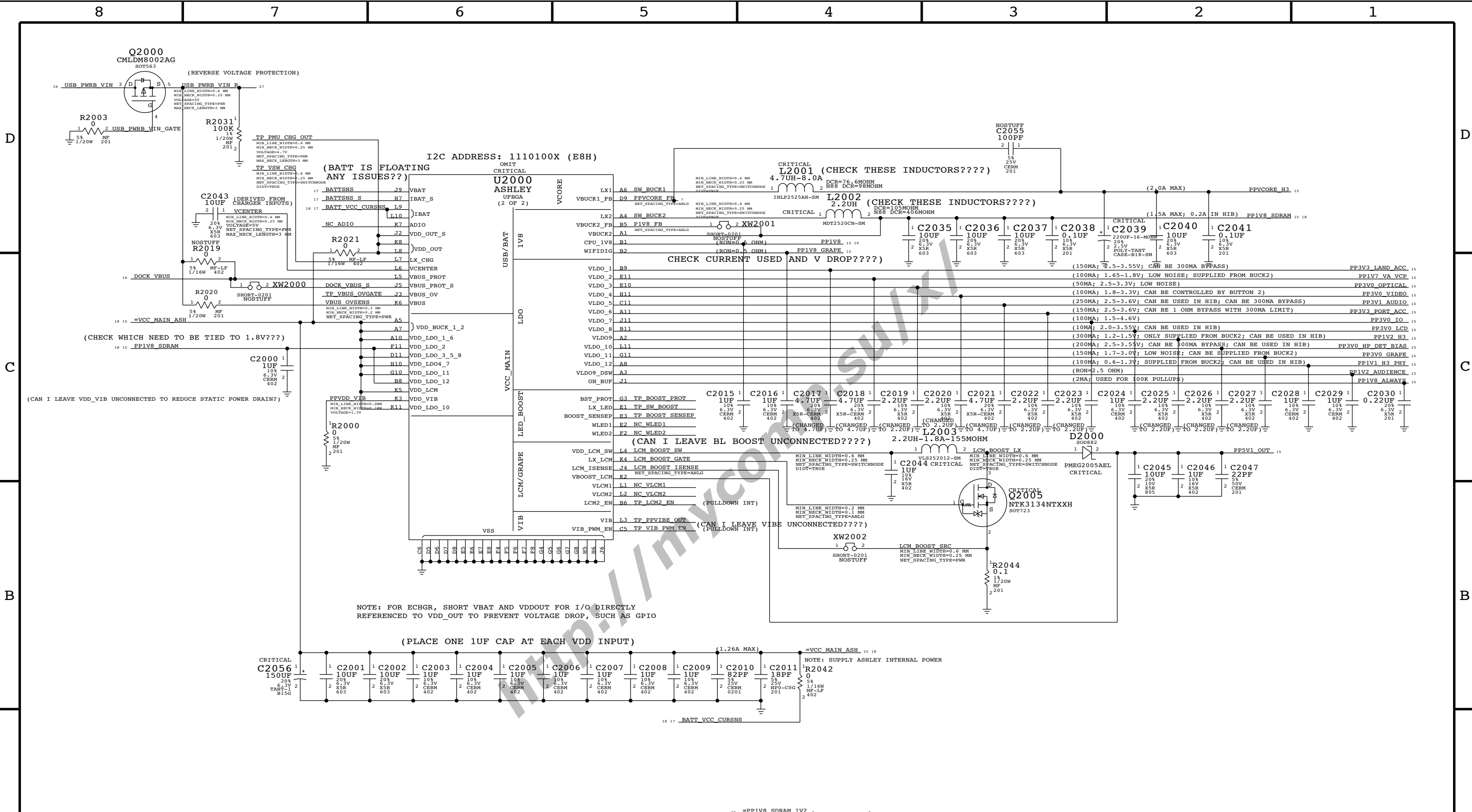
PART NUMBER	QTY	DESCRIPTION	REFERENCE DES	CRITICAL	BOM OPTION
11080550	1	REG.PP.0 00M,1/100,003,1000M,LP.0.020	F1900	CRITICAL	



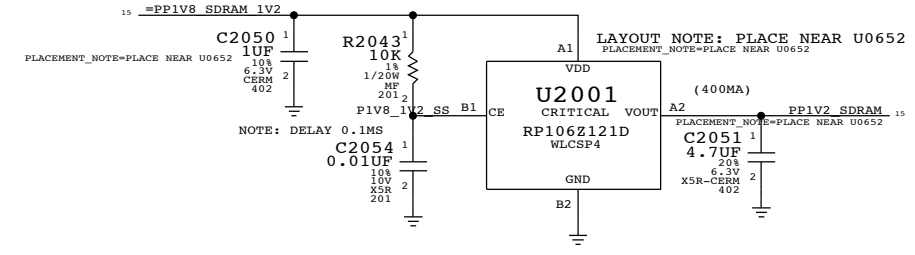
(MAKE SURE WE HAVE A LARGE COPPER POUR TO DISSIPATE HEAT)

NOTE: VERIFY PINOUT OF BATTERY CONNECTOR

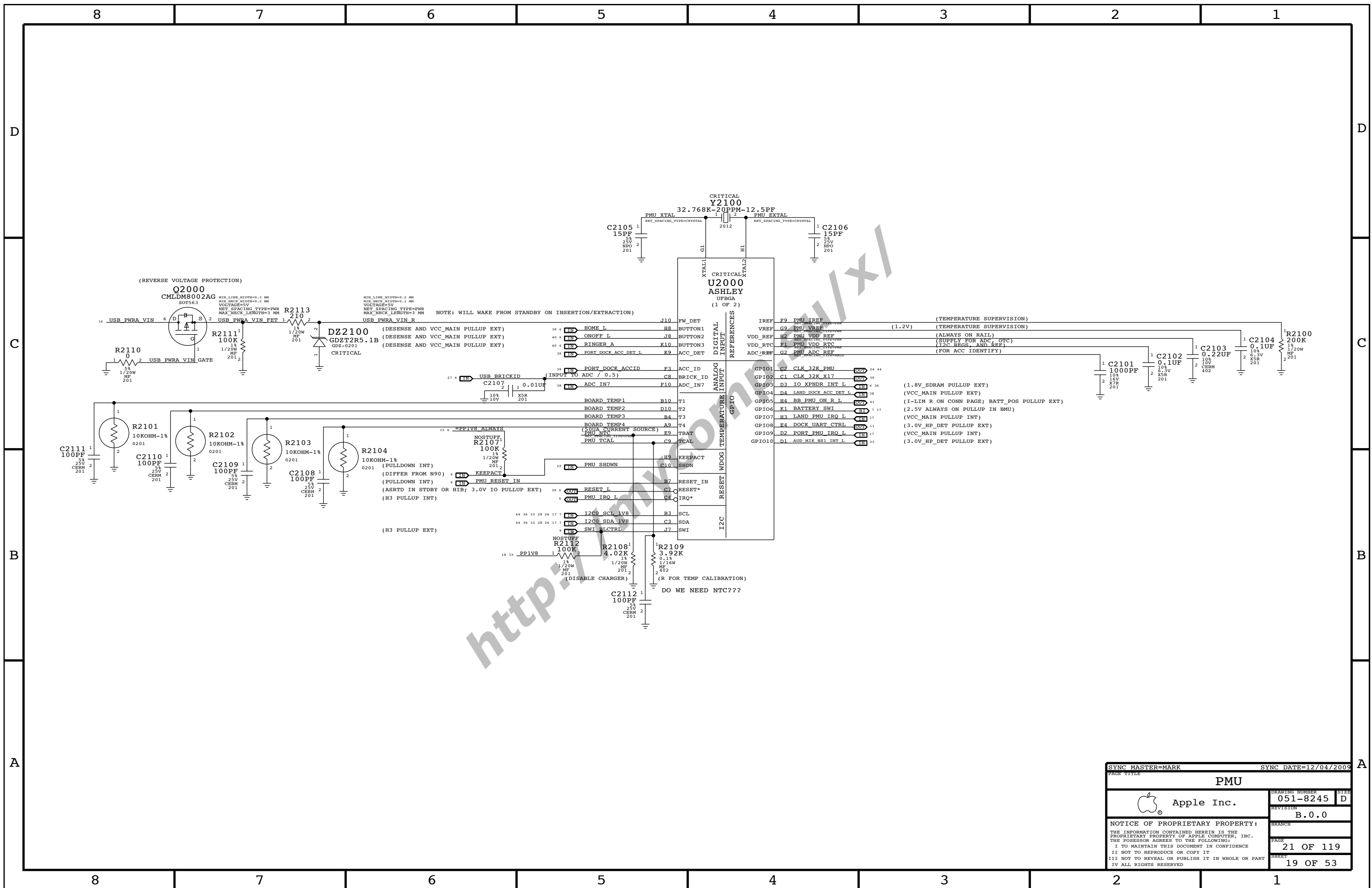
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CHARGER			
Apple Inc.		DRAWING NUMBER	SIZE
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PART NUMBER	QTY	DESCRIPTION	REFERENCE DES	CRITICAL	BOM OPTION
33880805	1	IC, PMU, ASHLEY, U2000, UFBGA, U2000, U2000	U2000	CRITICAL	

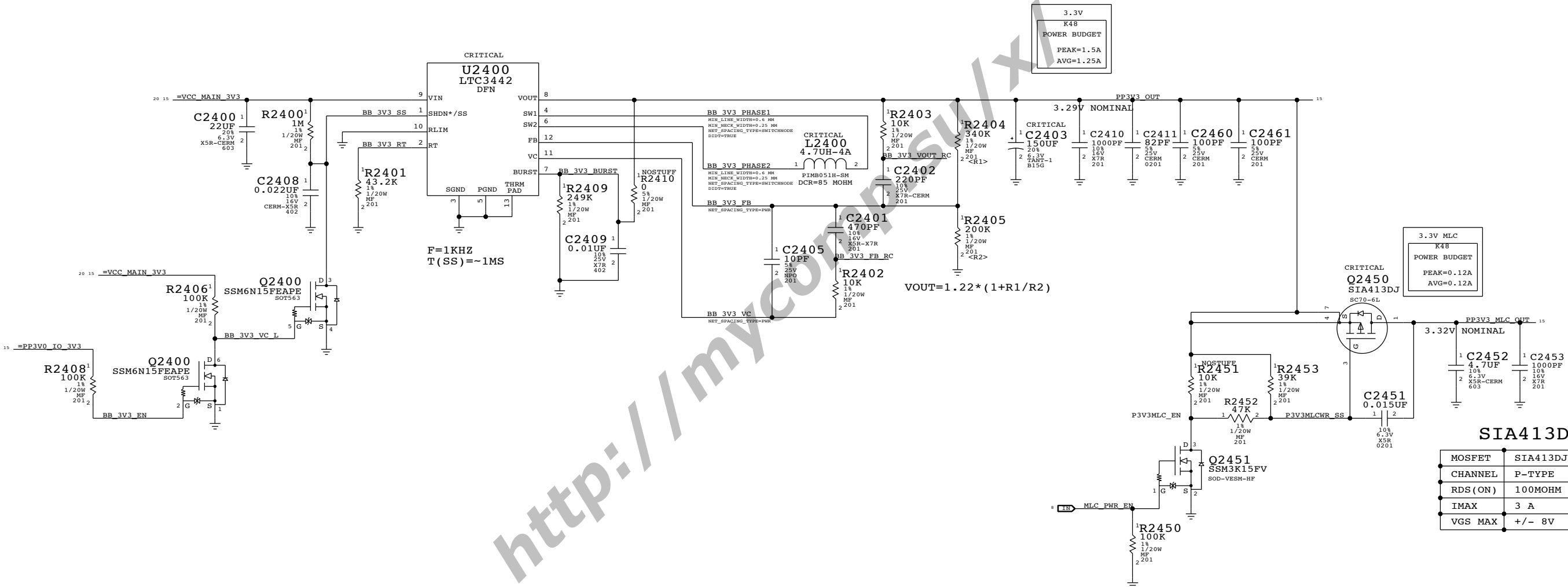


PAGE TITLE		SYNC DATE=12/04/2009	
		DRAWING NUMBER	051-8245
		REVISION	B.0.0
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SYNC MASTER=MARK		SYNC DATE=12/04/2009	
PMU			
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		PAGE	21 OF 119
		SHEET	19 OF 53

3.3V SUPPLY



3.3V
K48
POWER BUDGET
PEAK=1.5A
AVG=1.25A

3.3V MLC
K48
POWER BUDGET
PEAK=0.12A
AVG=0.12A

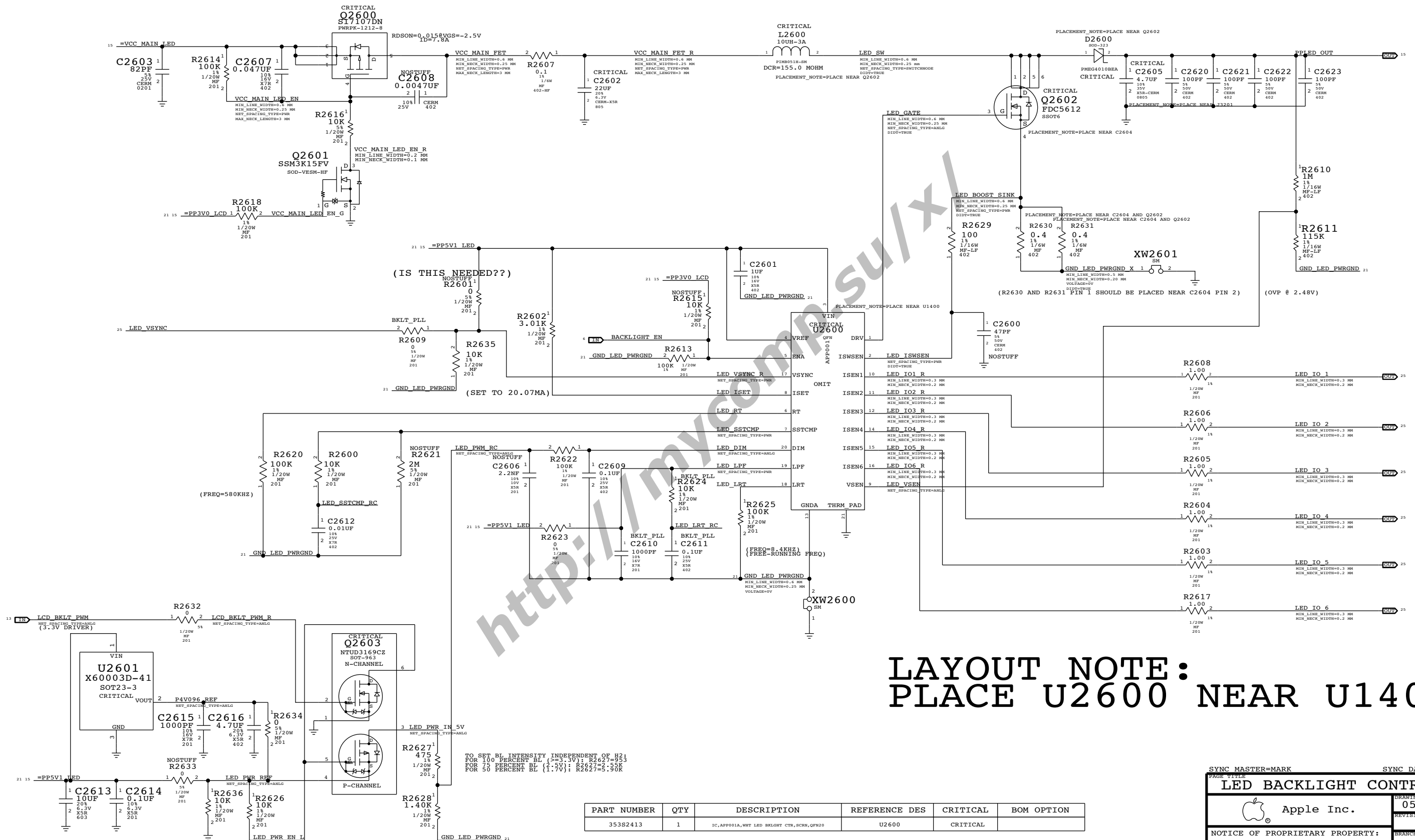
SIA413DJ

MOSFET	SIA413DJ
CHANNEL	P-TYPE
RDS (ON)	100MOHM @-1.5V
IMAX	3 A
VGS MAX	+/- 8V

SYNC MASTER=MARK SYNC DATE=12/04/2009

PAGE TITLE		3.3V SUPPLY	
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LED BOOST/BACKLIGHT CONTROLLER



LAYOUT NOTE:
PLACE U2600 NEAR U1400

TO SET BL INTENSITY INDEPENDENT OF H₂
FOR 100 PERCENT BL (3.3V): R2627=953
FOR 75 PERCENT BL (1.5V): R2627=2.55K
FOR 50 PERCENT BL (1.7V): R2627=3.90K

PART NUMBER	QTY	DESCRIPTION	REFERENCE DES	CRITICAL	BOM OPTION
353S2413	1	IC,APP001A,MFT LED BKLGHT CTR,SCRN,OPN20	U2600	CRITICAL	

SYNC MASTER=MARK SYNC DATE=12/04/2009

LED BACKLIGHT CONTROLLER

Apple Inc.

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REVISION: B.0.0

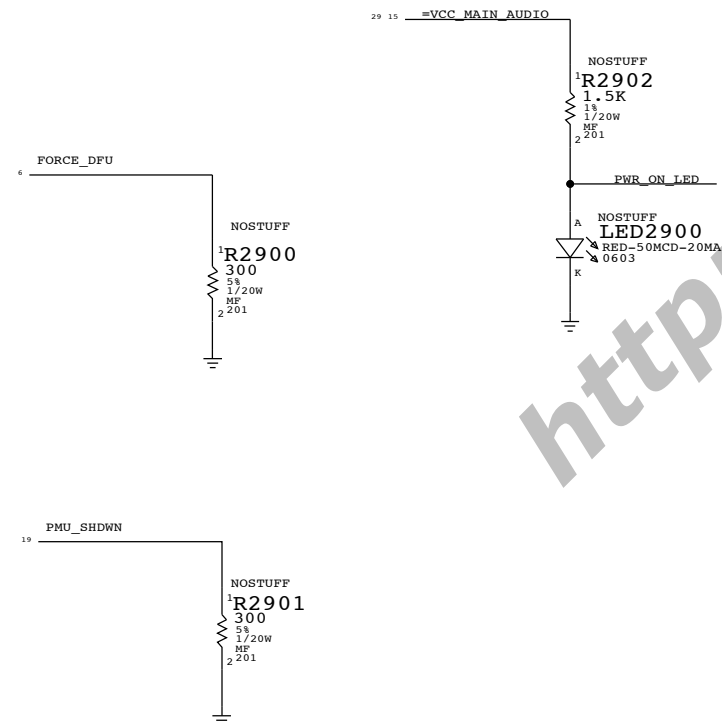
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DEBUG RESET ACCESS



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DEBUG RESET ACCESS		051-8245		D
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29 OF 119		22 OF 53		
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D

D

C

C

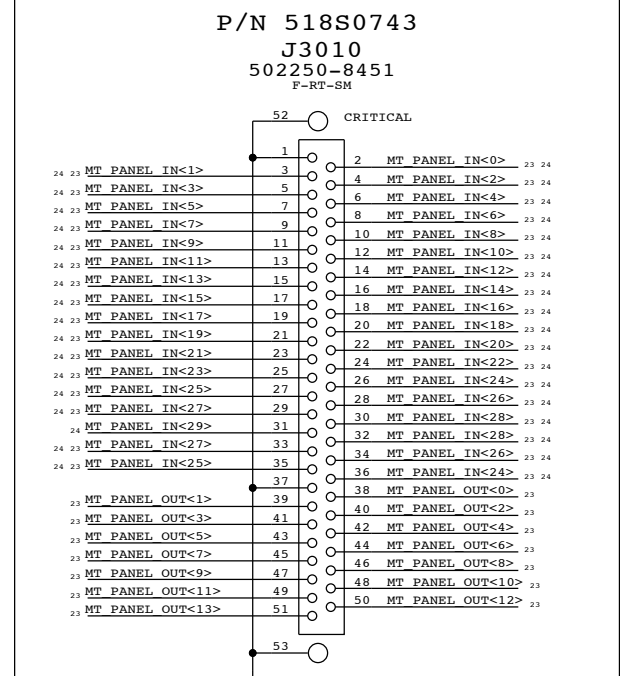
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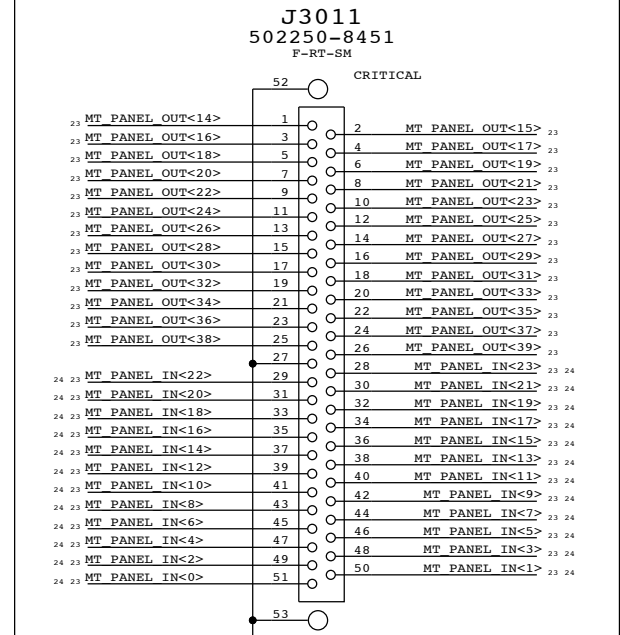
A

A

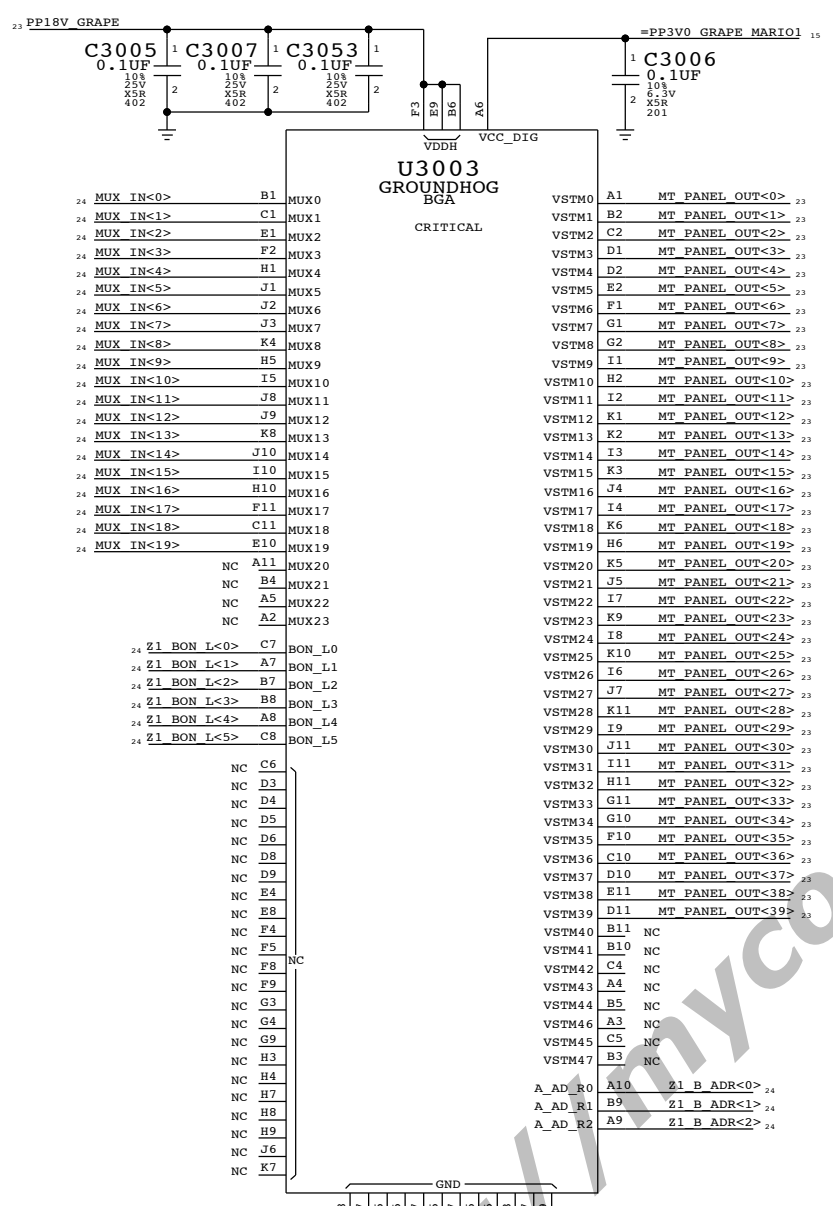
CONNECTORS TO GRAPE FLEX



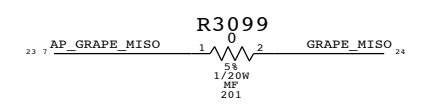
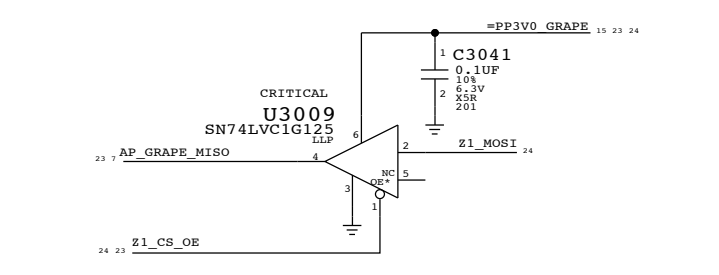
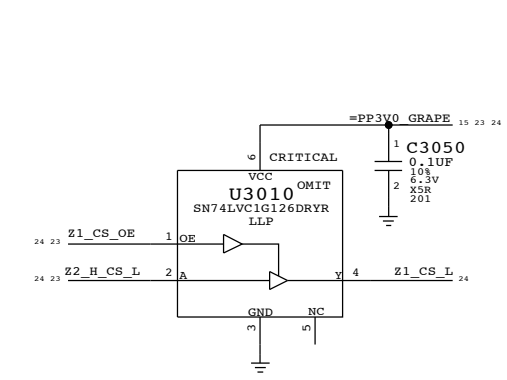
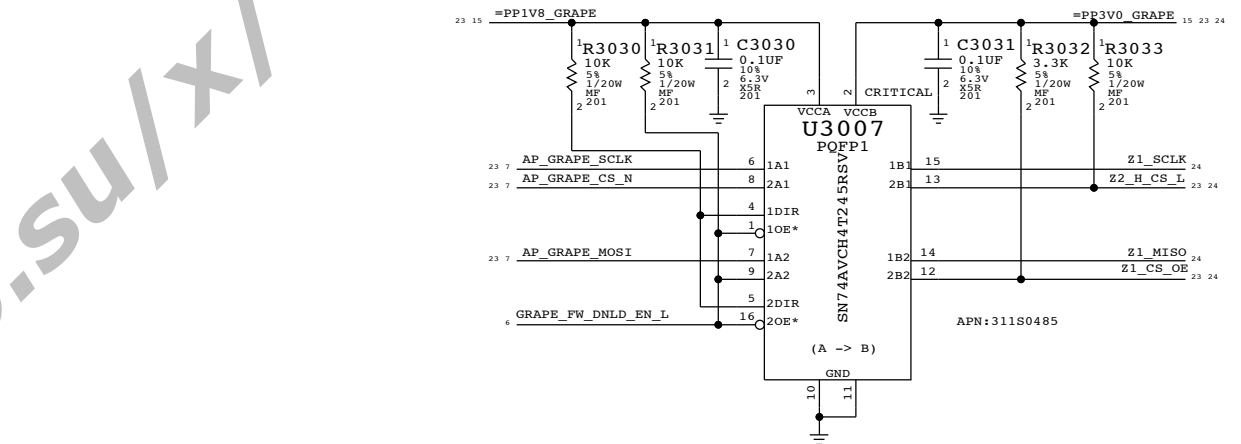
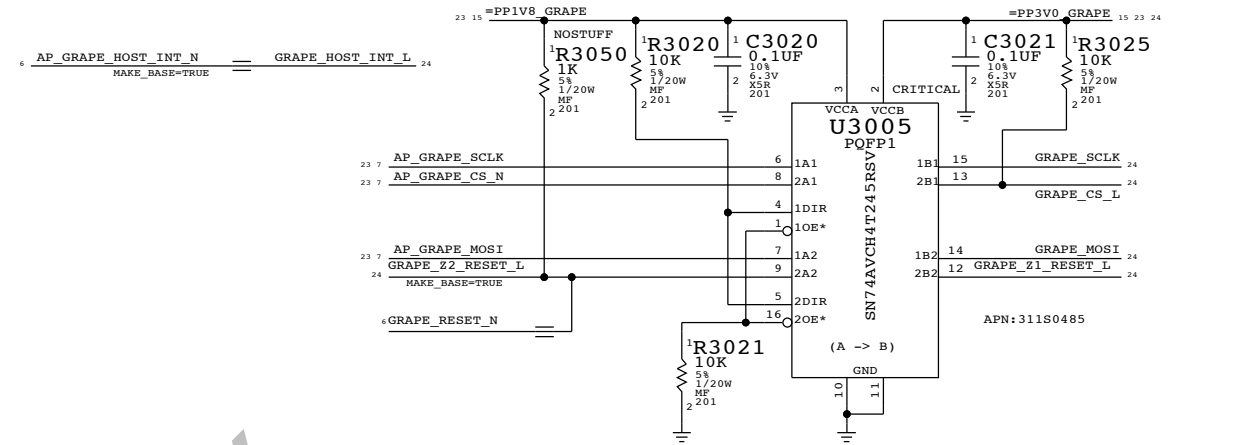
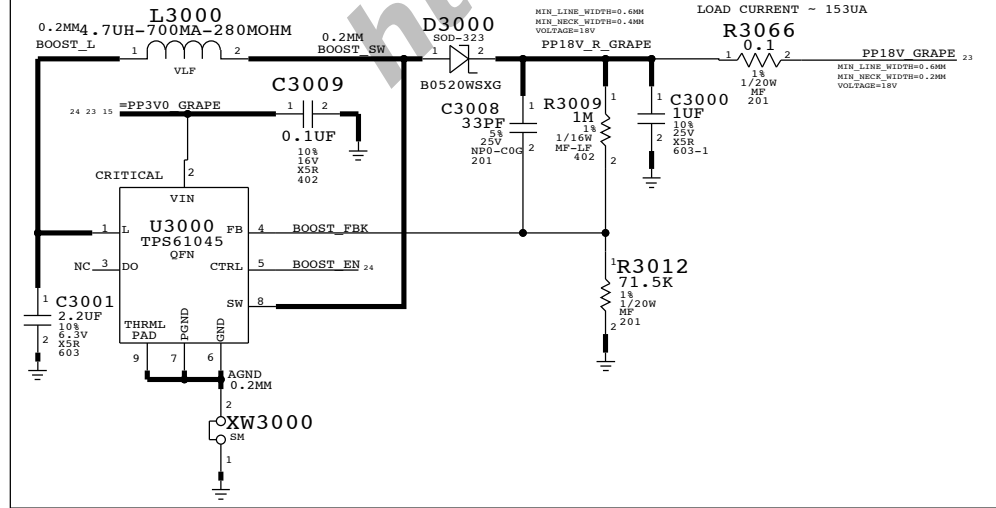
MATES WITH LEFTMOST GRAPE FLEX TAIL



MATES WITH RIGHTMOST GRAPE FLEX TAIL



BOOST CONVERTOR



PART NUMBER	QTY	DESCRIPTION	REFERENCE DES	CRITICAL	BOM OPTION
311S0506	1	IC, SNGL 3-STATE BUFFER, 5-PIN, DRY	U3010	CRITICAL	

SYNC MASTER=JAMES SYNC DATE=12/21/2009

GRAPE 1 OF 2

Apple Inc.

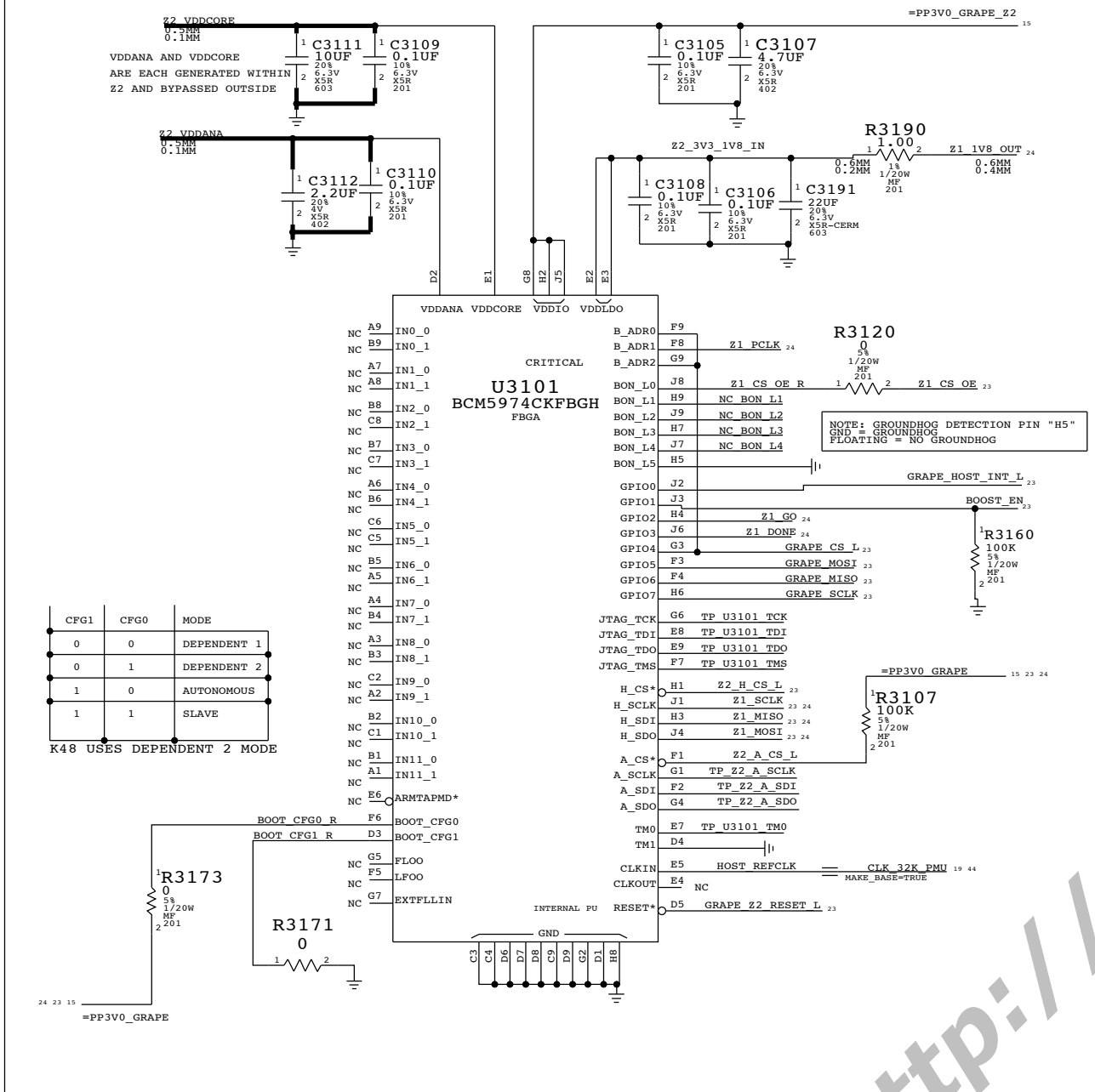
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REVISION: B.0.0

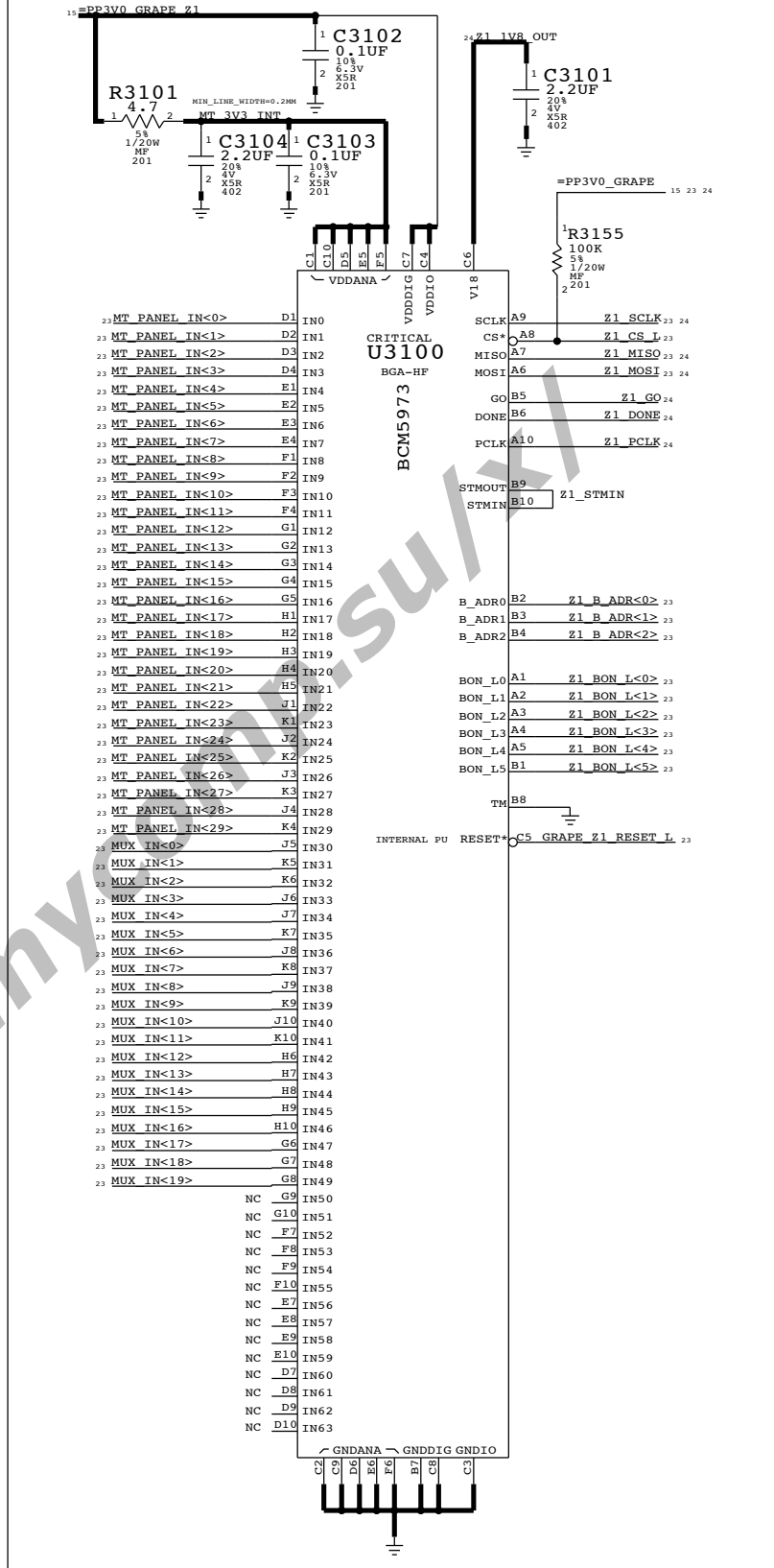
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ARM9 MCU (Z2 BASED)



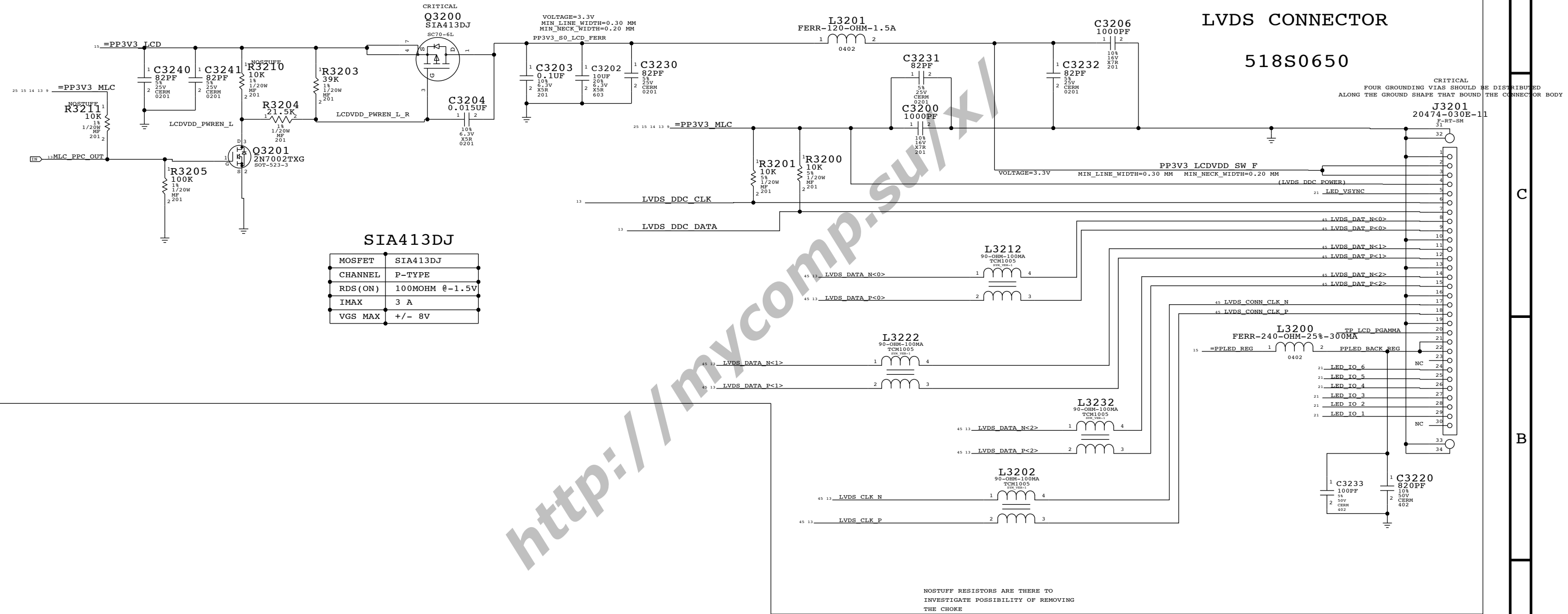
ZEPHYR 1+ ASIC



LVDS CONNECTOR

SIMILAR TO M97

LVDS CONNECTOR
518S0650



SIA413DJ

MOSFET	SIA413DJ
CHANNEL	P-TYPE
RDS (ON)	100MOHM @-1.5V
IMAX	3 A
VGS MAX	+/- 8V

SYNC MASTER=MIAMI SYNC DATE=09/16/2009

LVDS CONNECTOR

Apple Inc.

051-8245 D

REVISION B.0.0

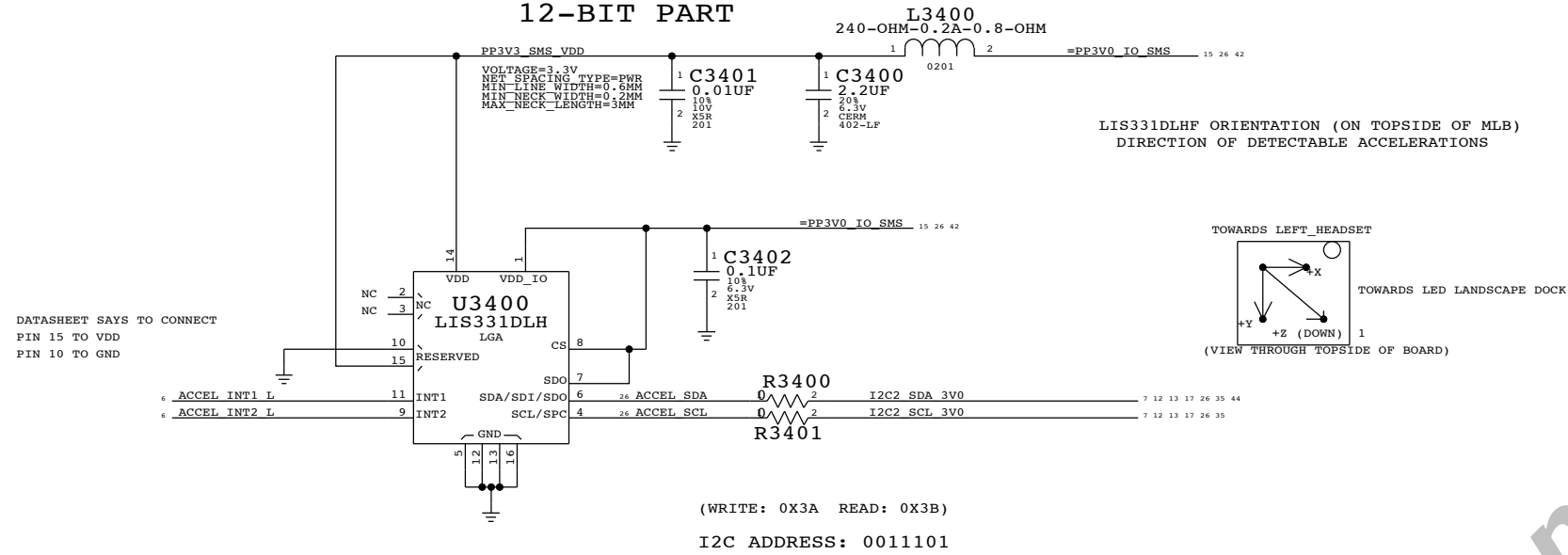
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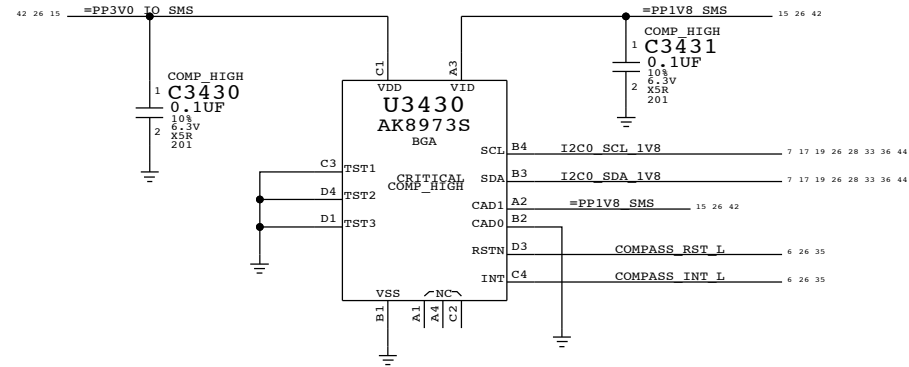
MOTION/GYRO/COMPASS SENSORS

ST MICRO LIS331DLHF MOTION SENSOR 12-BIT PART



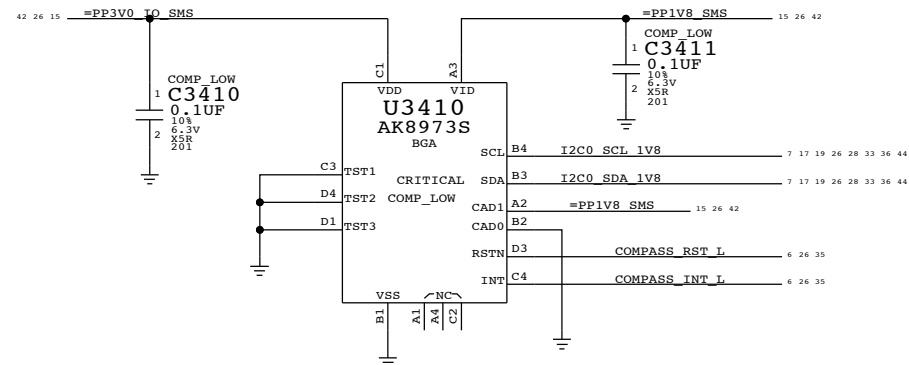
COMPASS HIGH

(HAS THERMAL SENSOR IN IT)
I2C ADDR: 0011110
WRITE: 0X3C READ: 0X3D



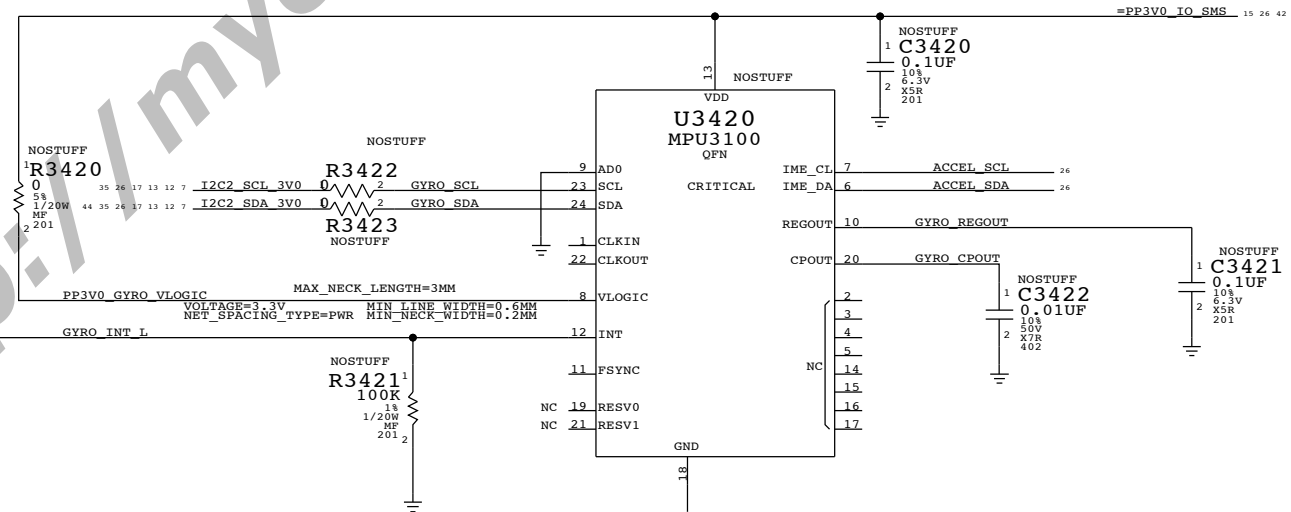
COMPASS LOW

(HAS THERMAL SENSOR IN IT)
I2C ADDR: 0011110
WRITE: 0X3C READ: 0X3D



GYRO

(WRITE: 0XD0 READ: 0XD1)
I2C ADDRESS: 1101000

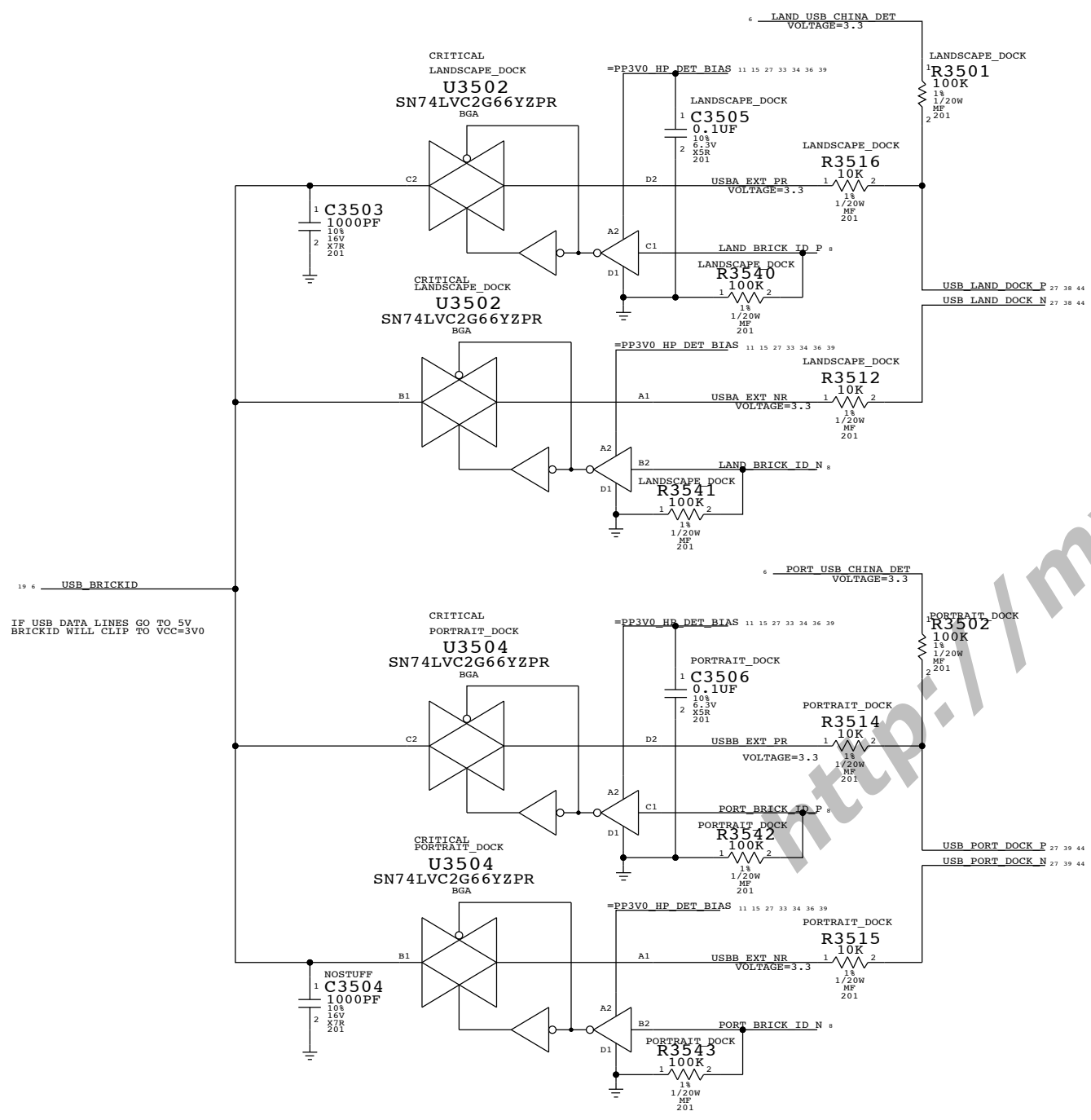


SYNC MASTER=MIAMI SYNC DATE=09/16/2009

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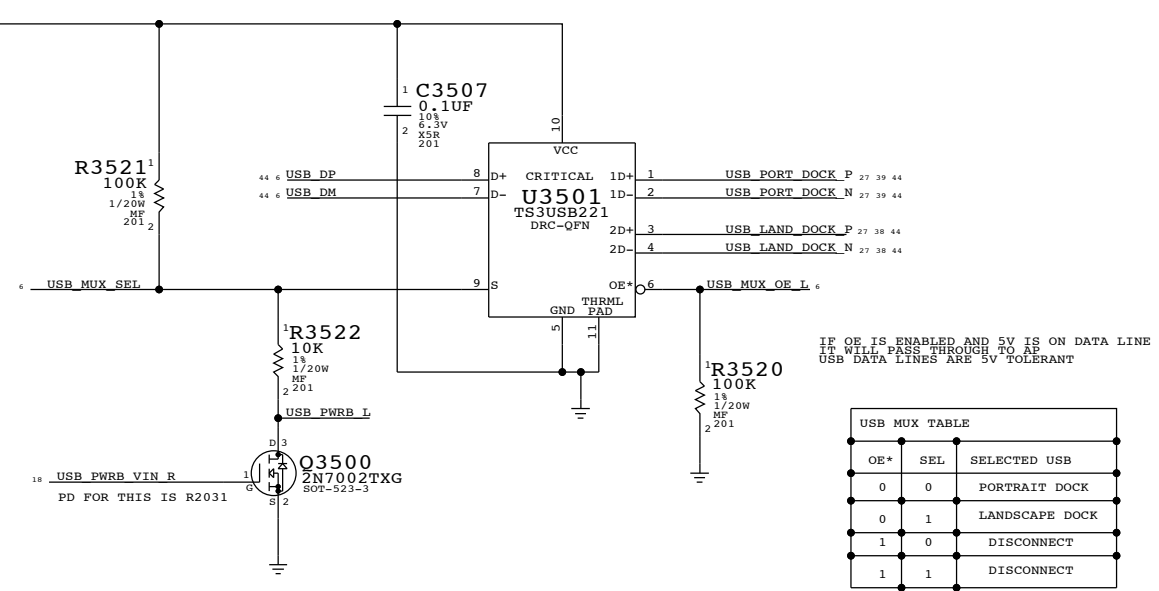
USB MUX/BRICK DETECTION

MAKE SURE RESISTORS ARE ON TOP OF TRACE TO REDUCE STUB



IF USB DATA LINES GO TO 5V BRICKID WILL CLIP TO VCC=3V0

USB MUX FOR DOCK USB



IF OE IS ENABLED AND 5V IS ON DATA LINE IT WILL PASS THROUGH TO AE USB DATA LINES ARE 5V TOLERANT

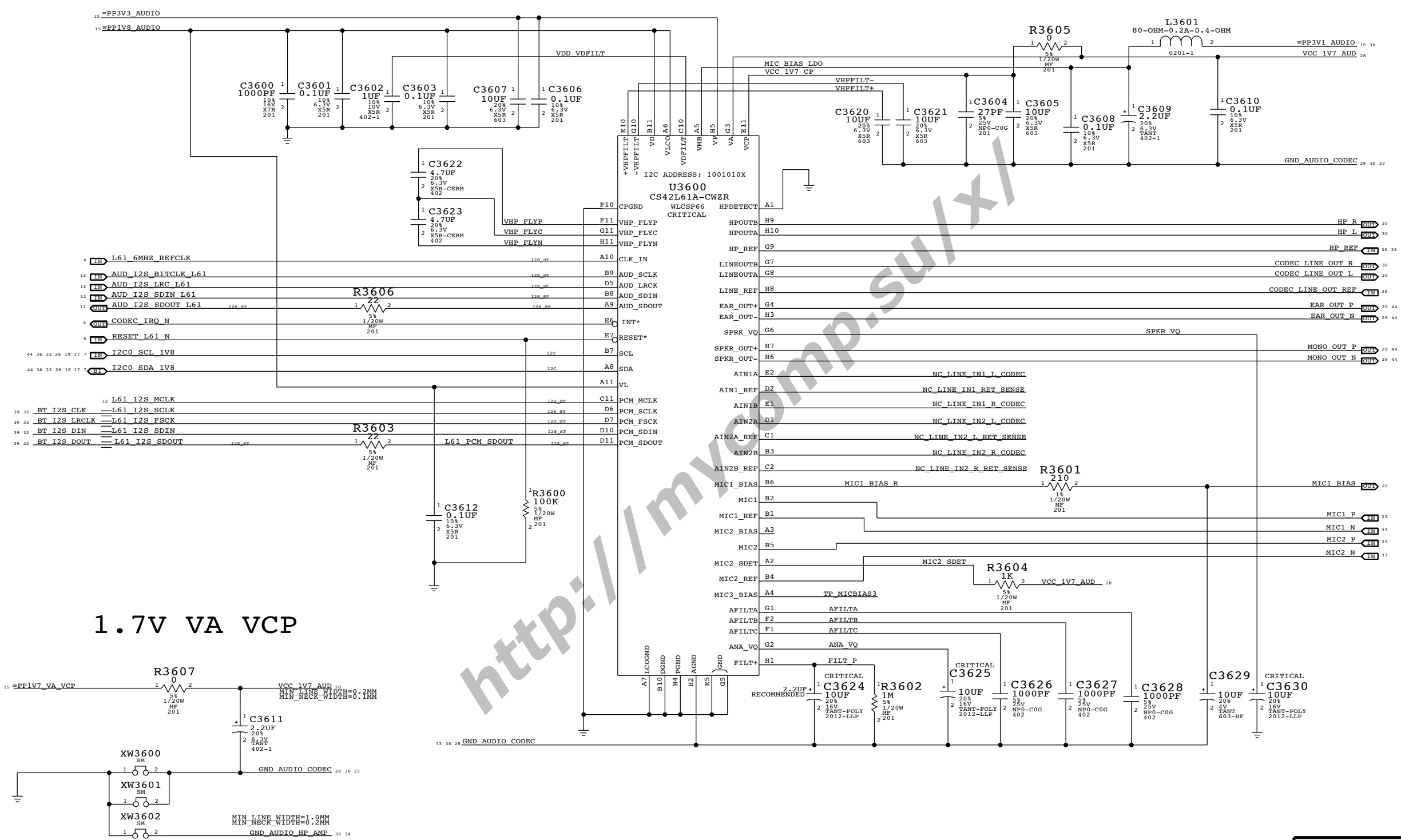
USB MUX TABLE		
OE*	SEL	SELECTED USB
0	0	PORTRAIT DOCK
0	1	LANDSCAPE DOCK
1	0	DISCONNECT
1	1	DISCONNECT

SYNC MASTER=MIAMI SYNC DATE=09/16/2009

PAGE TITLE USB MUX/BRK DET		
Apple Inc.	DRAWING NUMBER 051-8245	SIZE D
	REVISION B.0.0	
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PAGE 35 OF 119		SHEET 27 OF 53

L61 AUDIO CODEC

APN:338S0589



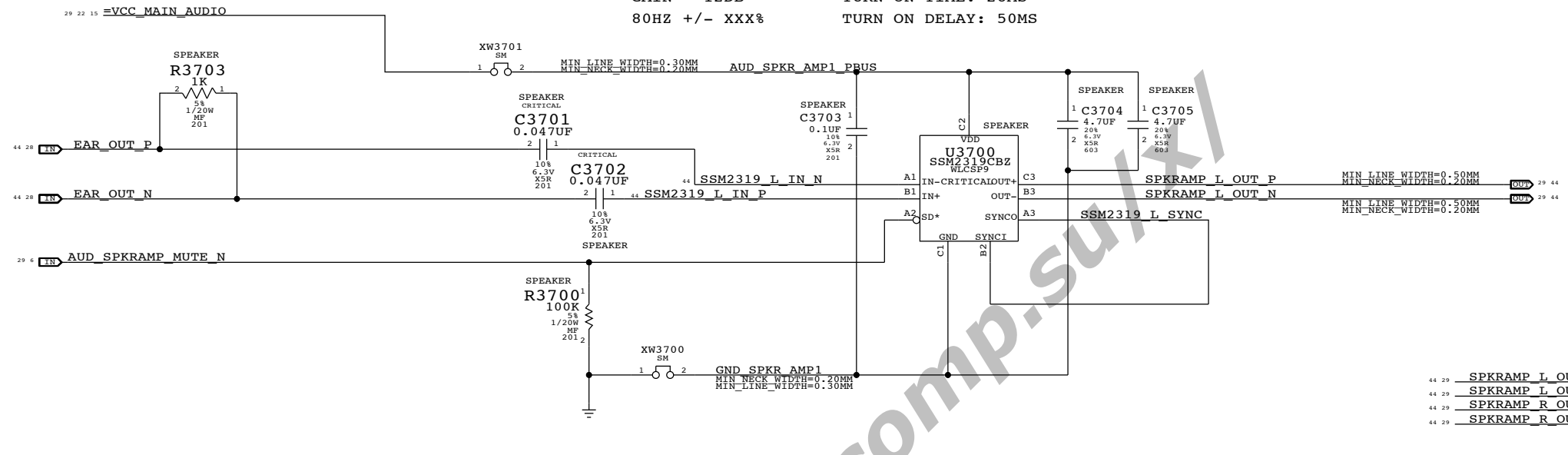
1.7V VA VCP

SYNC MASTER=AUDIO		SYNC DATE=12/04/2009	
L61 AUDIO INTERFACE			
Apple Inc.		DRAWING NUMBER	051-8245
		REVISION	B.0.0
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		SHEET	28 OF 53

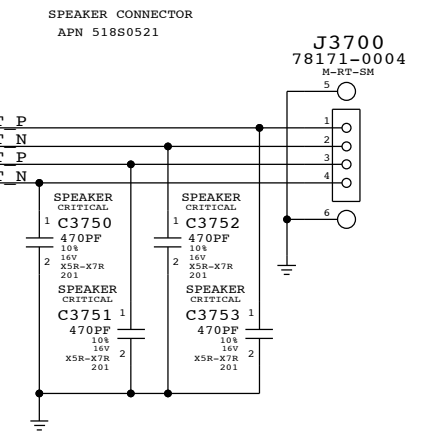
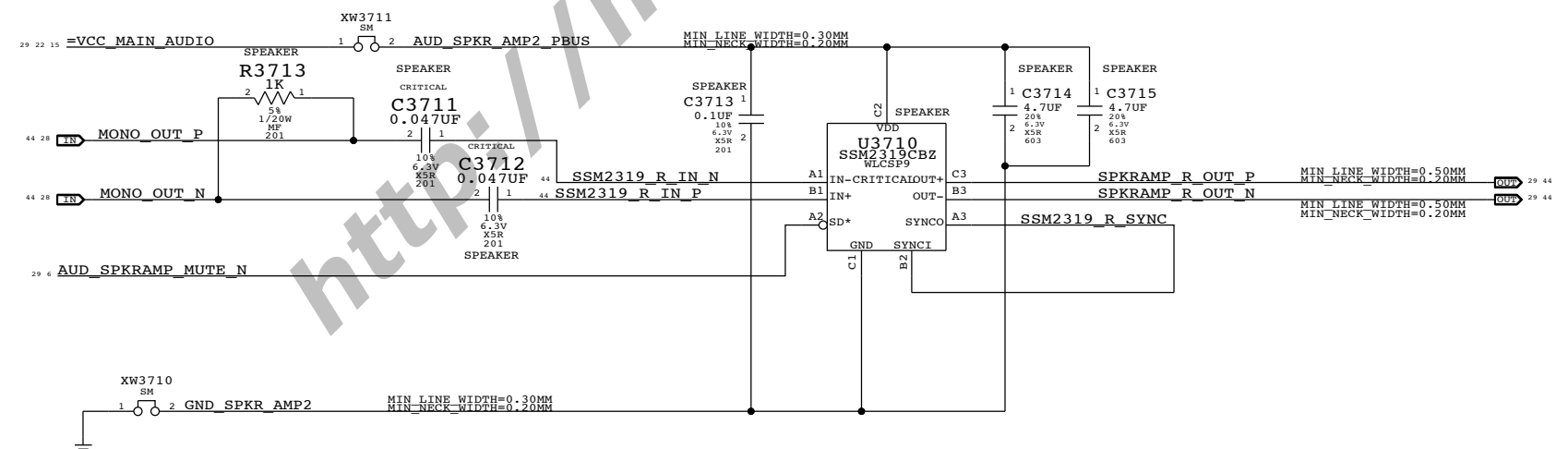
SPEAKER AMPLIFIER

SSM2319 APN:353S2136

GAIN = 12DB TURN ON TIME: 28MS
 80HZ +/- XXX% TURN ON DELAY: 50MS

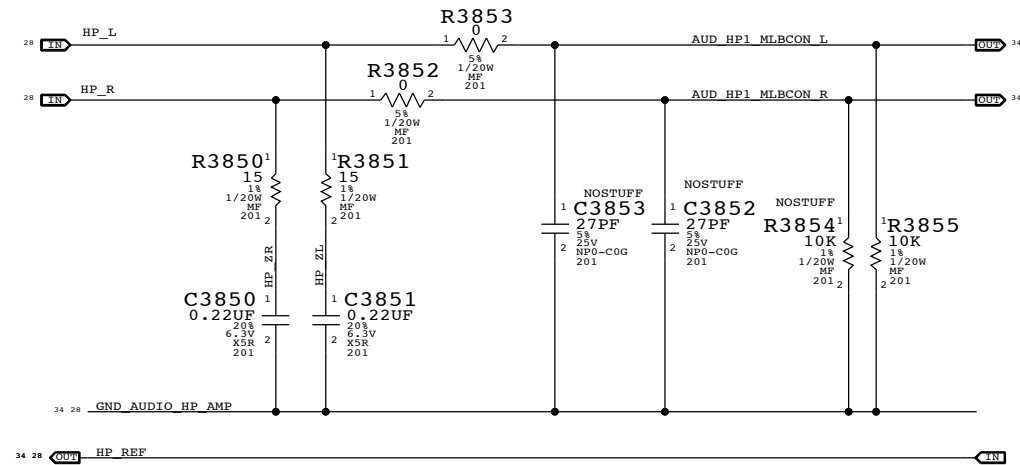


L61 RECEIVER OUTPUT IS CONNECTED TO U3700
 L61 SPEAKER OUTPUT IS CONNECTED TO U3710

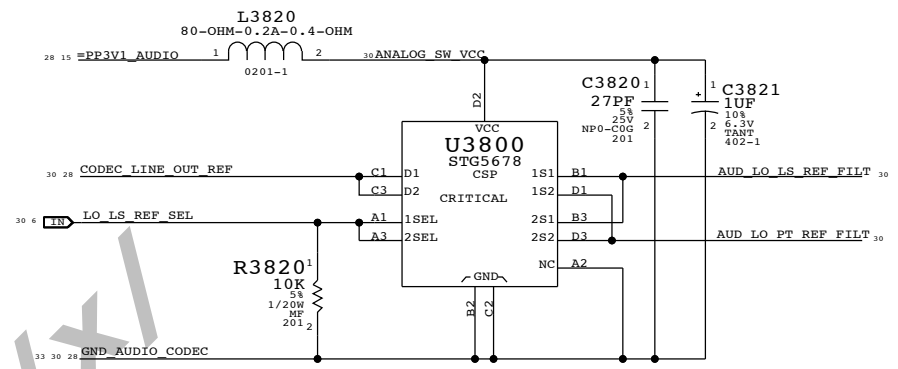


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AUDIO: SPEAKER AMP			
		DRAWING NUMBER	051-8245
		REVISION	B.0.0
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		PAGE	37 OF 119
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HEADPHONE OUTPUT ZOBEL NETWORK



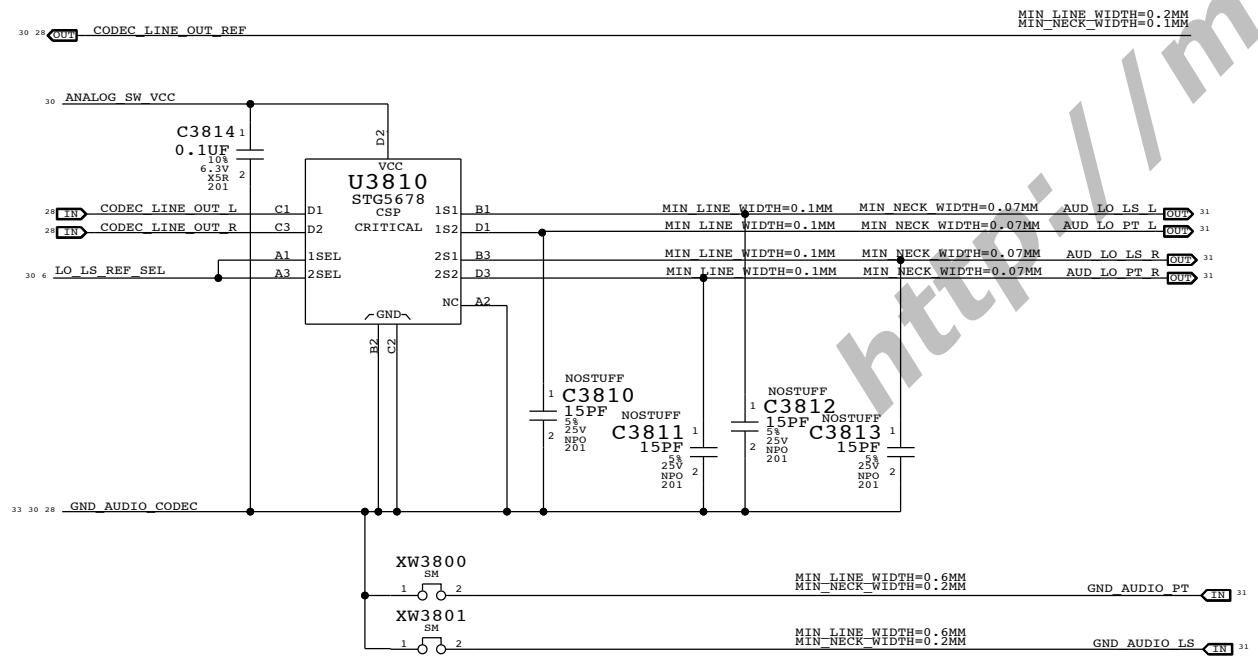
LINE OUTPUT REF SENSE DOCK SELECTOR



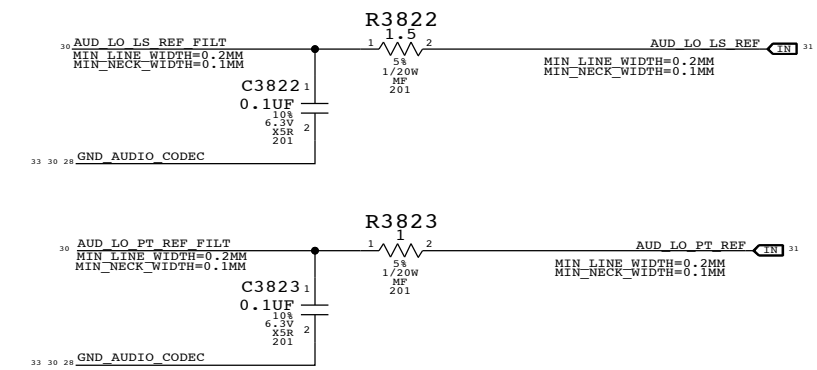
OUTPUT REF SENSE LINE SHOULD BE SWITCHED TO ACTIVE PORT
 LO_LS_REF_SEL = 0: PORTRAIT DOCK SELECTED
 LO_LS_REF_SEL = 1: LANDSCAPE DOCK SELECTED

LINE OUTPUT DOCK SELECTOR

LO_LS_REF_SEL = 0: DAC OUTPUT CONNECTED TO PORTRAIT DOCK
 LO_LS_REF_SEL = 1: DAC OUTPUT CONNECTED TO LANDSCAPE DOCK



LINE OUTPUT REF SENSE FILTER



PAGE TITLE		SYNC DATE=12/04/2009	
AUDIO: HEADPHONE OUT			
Apple Inc.		DRAWING NUMBER	SIZE
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		REVISION	BRANCH
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		30 OF 53	

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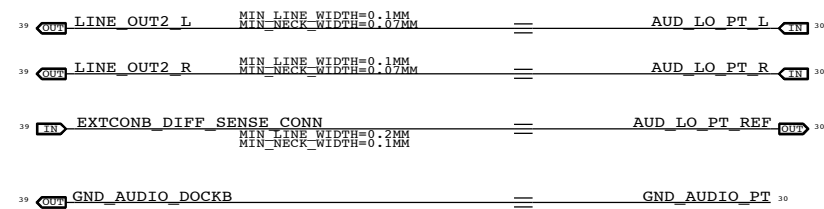
A

A

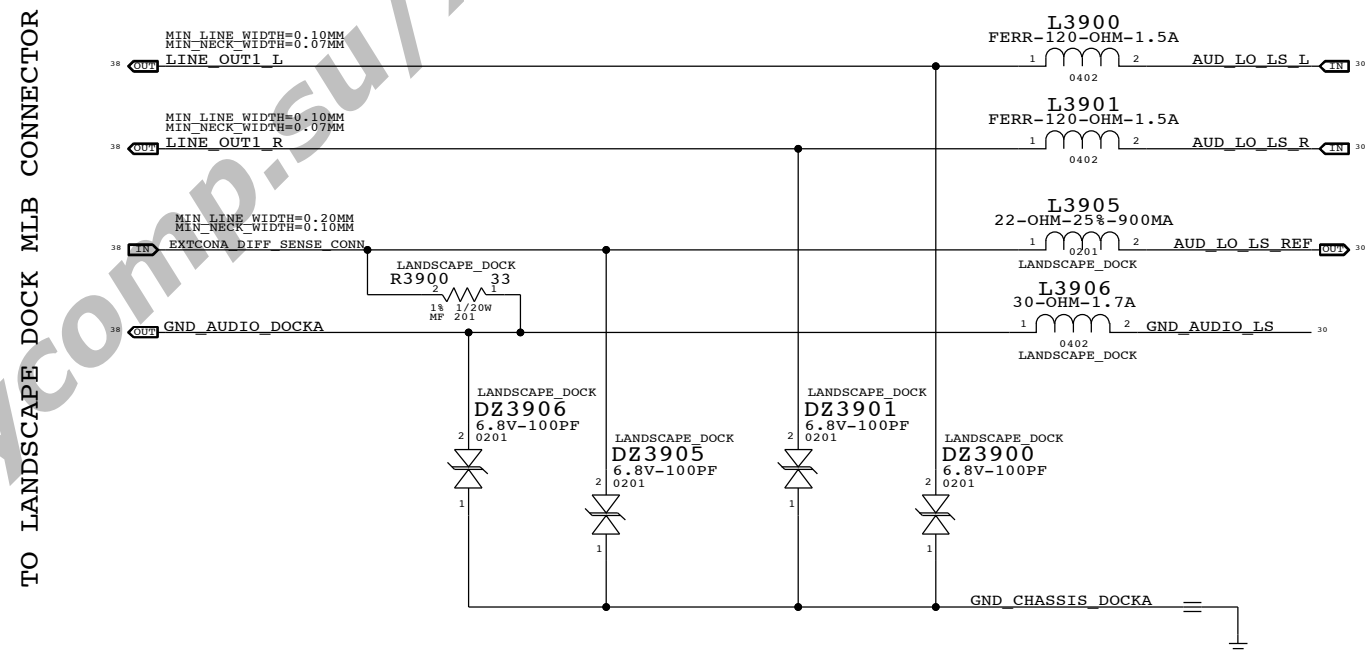
DOCKS OUTPUTS

NOTE: PORTRAIT DOCK IS PRIMARY DOCK

PORTRAIT DOCK LINE OUTPUT



LANDSCAPE DOCK LINE OUTPUT ESD CIRCUIT



PAGE TITLE		SYNC DATE=12/04/2009	
AUDIO: LINE OUT DOCK ESD CIRCUIT			
DRAWING NUMBER		SIZE	
051-8245		D	
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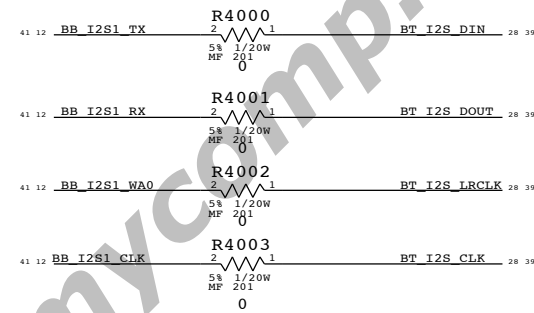
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AUDIENCE BYPASS SHUNTS



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PAGE TITLE		SYNC MASTER=AUDIO		SYNC DATE=12/04/2009	
AUDIO: AUDIENCE					
		DRAWING NUMBER		051-8245	
		REVISION		B.0.0	
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		SHEET		32 OF 53	

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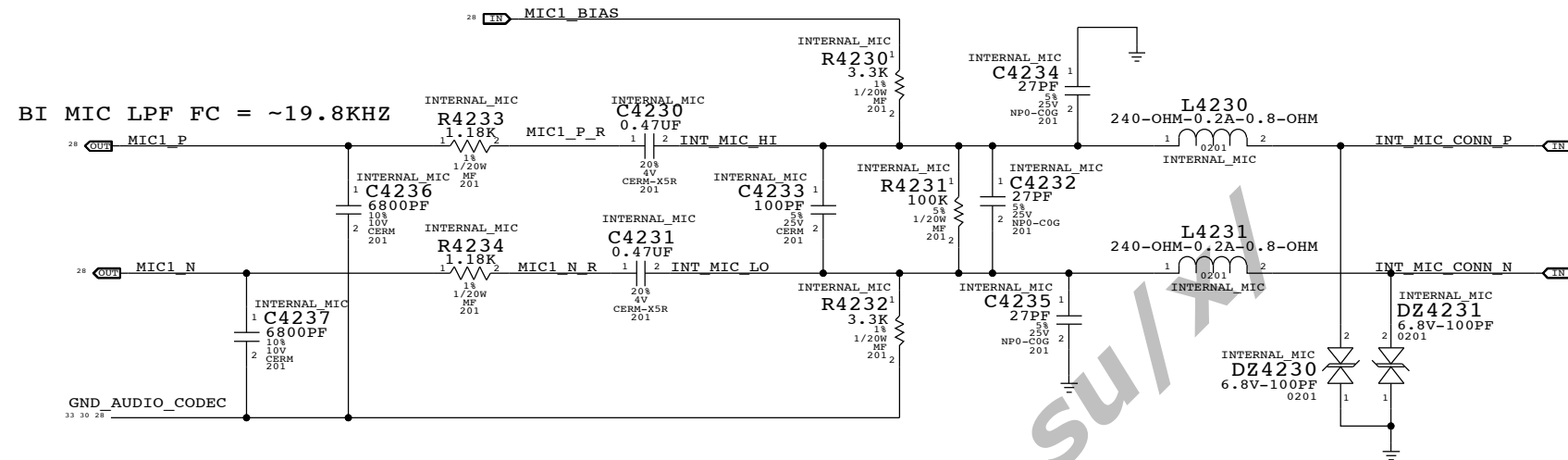
2

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INTERNAL (BUILT-IN) ANALOG MIC BIAS & FILTER



C

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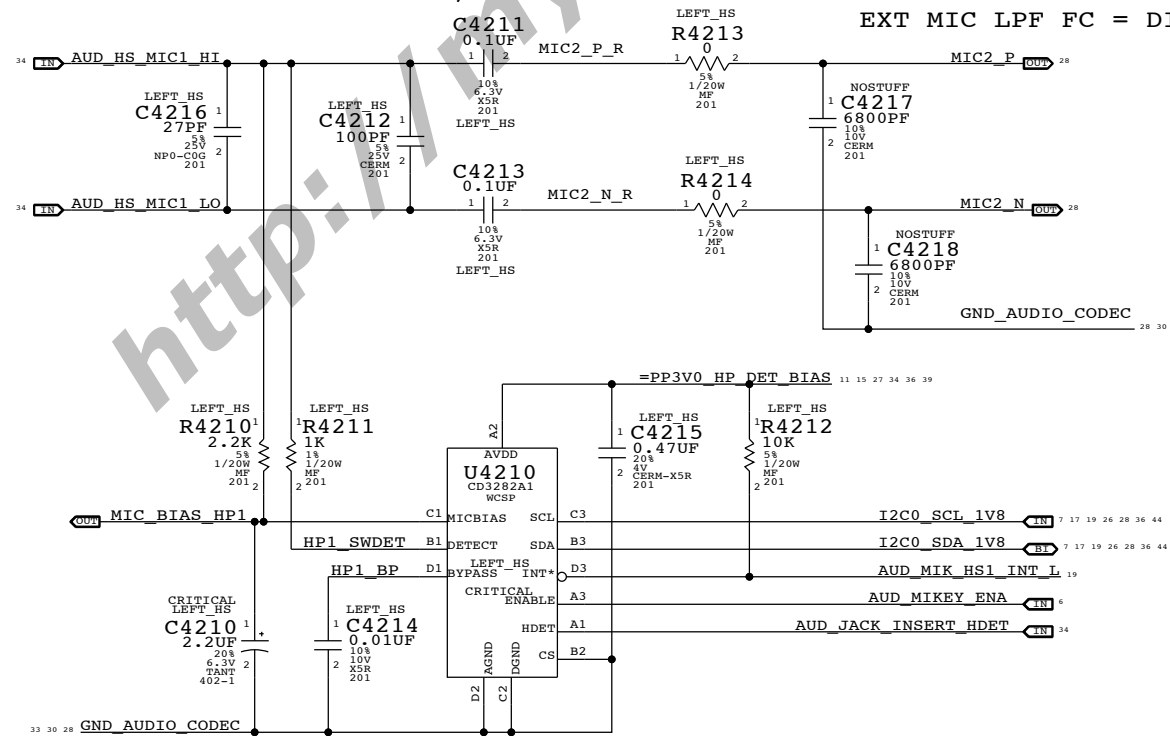
EXTERNAL MIC INPUT CIRCUITRY

APN:353S2640

I2C ADD: READ=72H, WRITE=73H

NOTE: INT IS OPEN DRAIN, PULL UP ON MIKEY SIDE

EXT MIC LPF FC = DISABLED



B

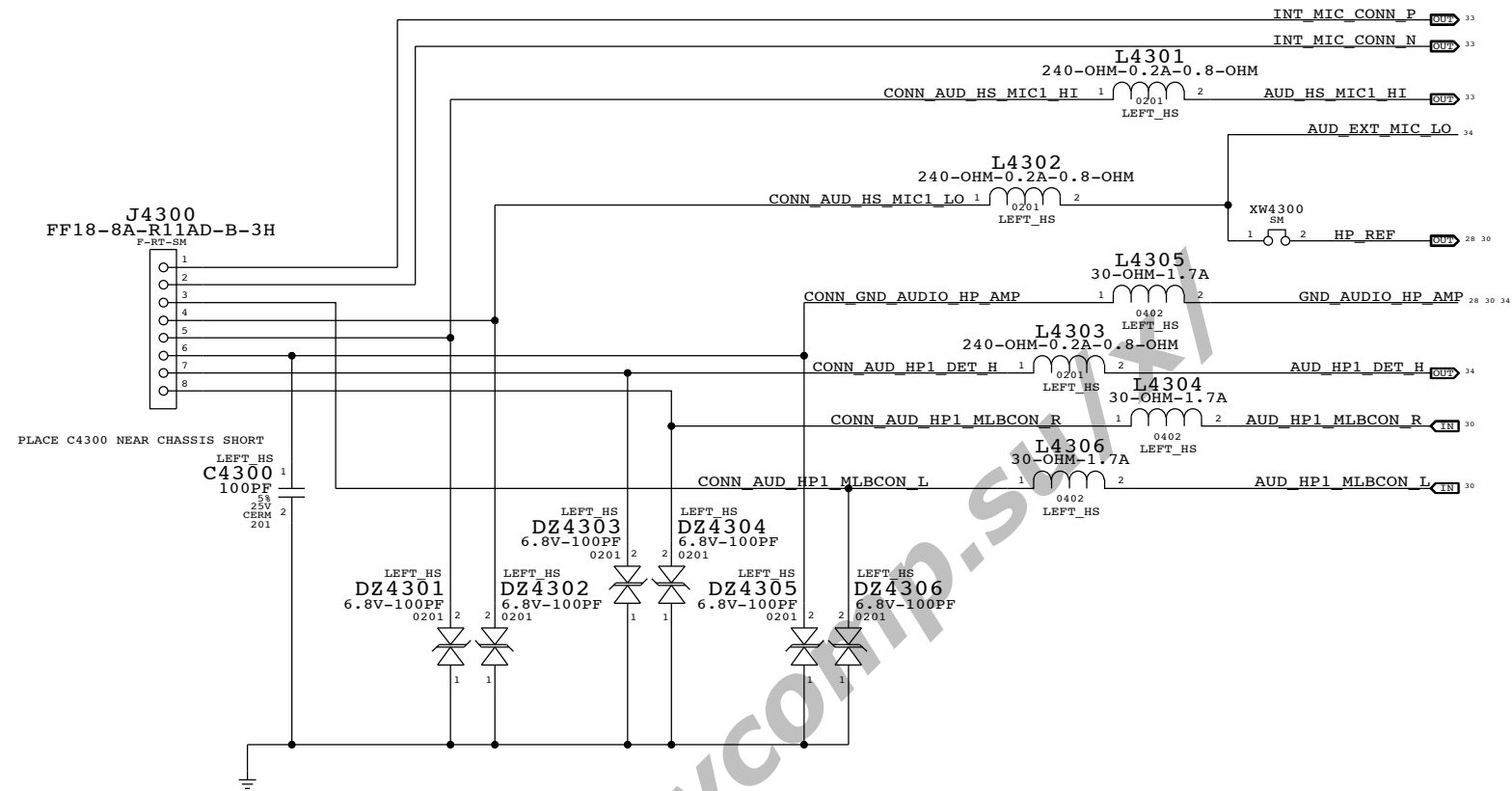
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A

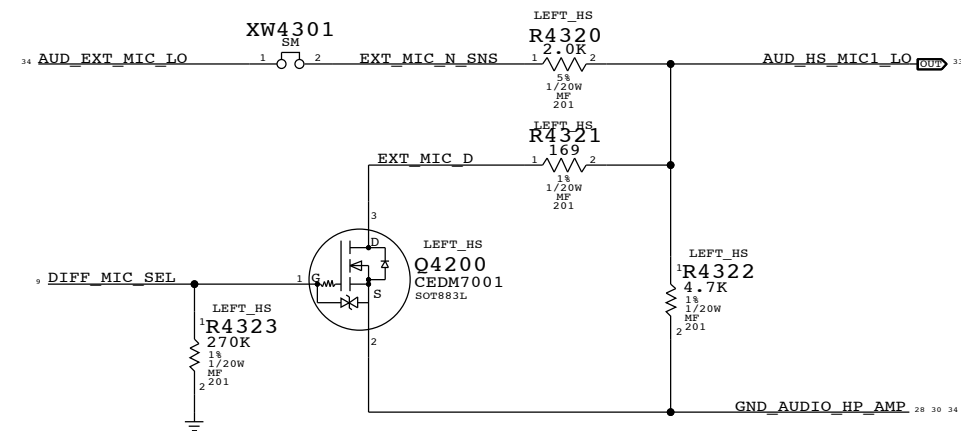
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PAGE TITLE		SYNC MASTER=AUDIO		SYNC DATE=12/04/2009	
AUDIO: DETECT/MIC BIAS					
		DRAWING NUMBER	051-8245	SIZE	D
		REVISION	B.0.0	BRANCH	
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				33 OF 53	

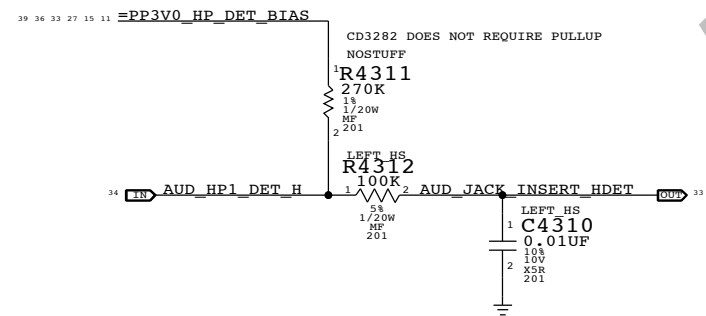
JACK 1 MLB CONNECTOR: HEADPHONE/HS_MIC/INT_MIC
 APN: 518S0693



HEADSET HP/MIC CROSSTALK MITIGATION (NOT USED)



HEADSET JACK INSERTION DETECT

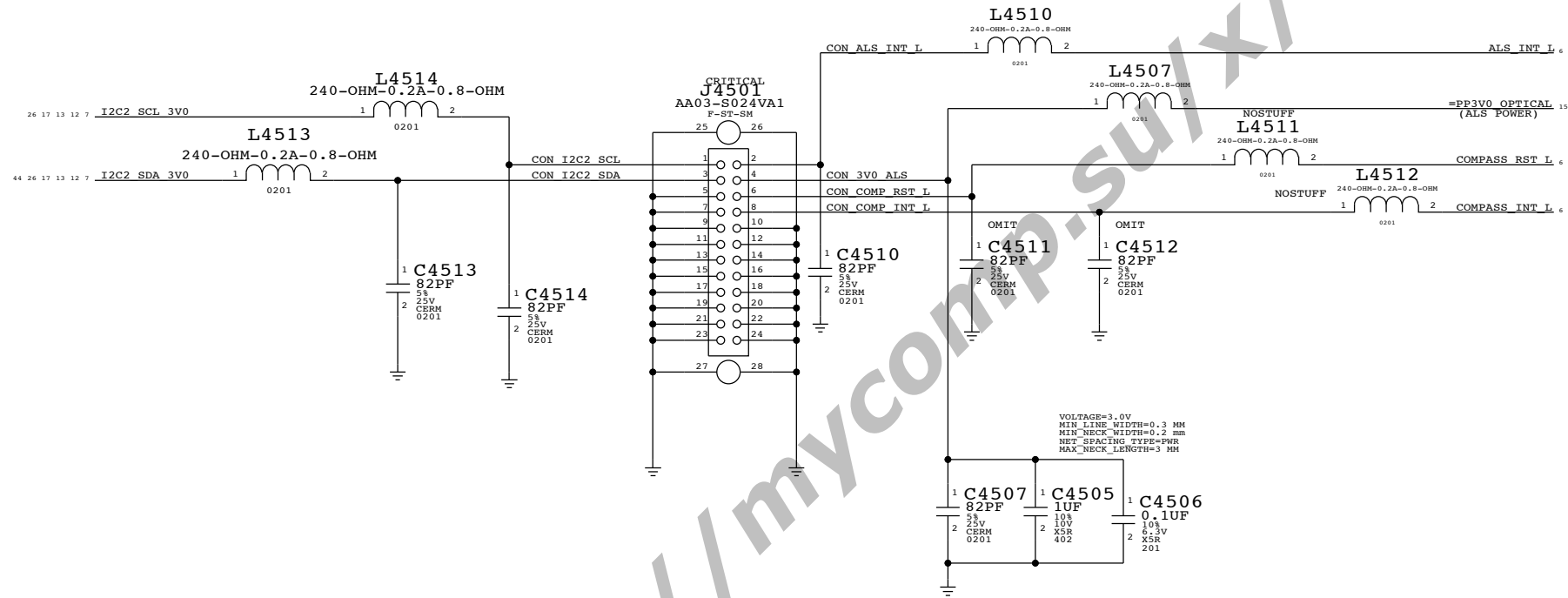


PAGE TITLE		SYNC DATE=12/04/2009	
AUDIO: HP CONN			
Apple Inc.	DRAWING NUMBER	051-8245	SIZE D
	REVISION	B.0.0	
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ALS CONN.

FPC CONNECTOR

APN: 516S0498



PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
11780002	2	0-OHM, 5%, 1/20W, MF, 0201	C4511, C4512	

SYNC MASTER=MIAMI SYNC DATE=09/16/2009

ALS CONNECTOR

Apple Inc.

DRAWING NUMBER: 051-8245 SIZE: D

REVISION: B.0.0

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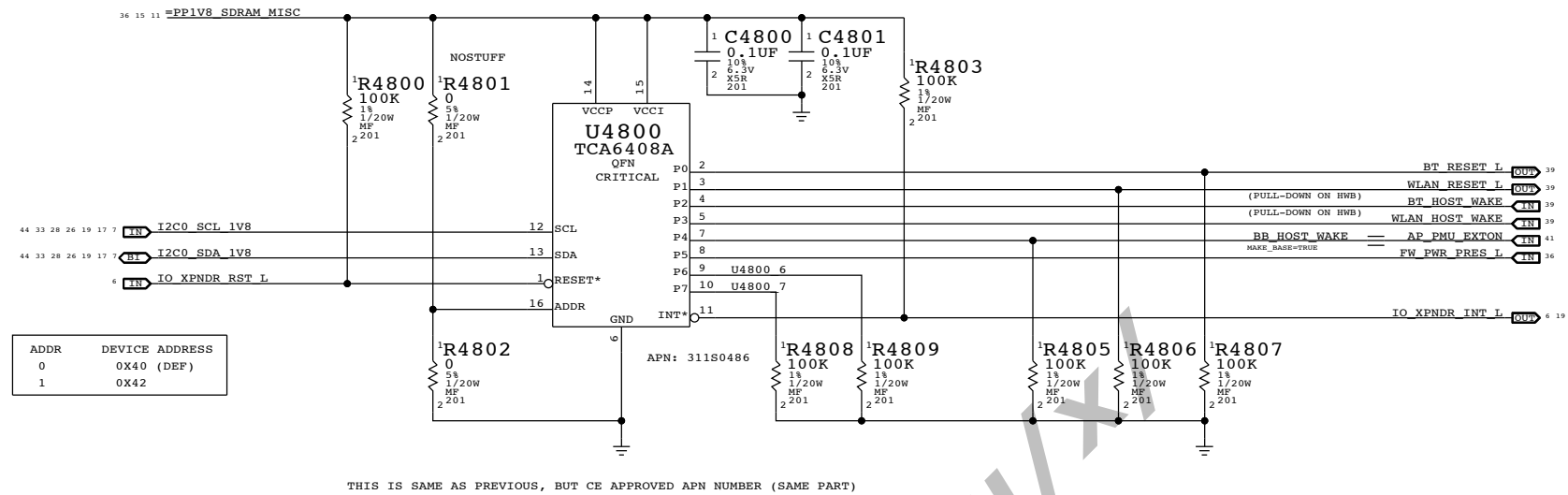
PAGE: 45 OF 119
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D

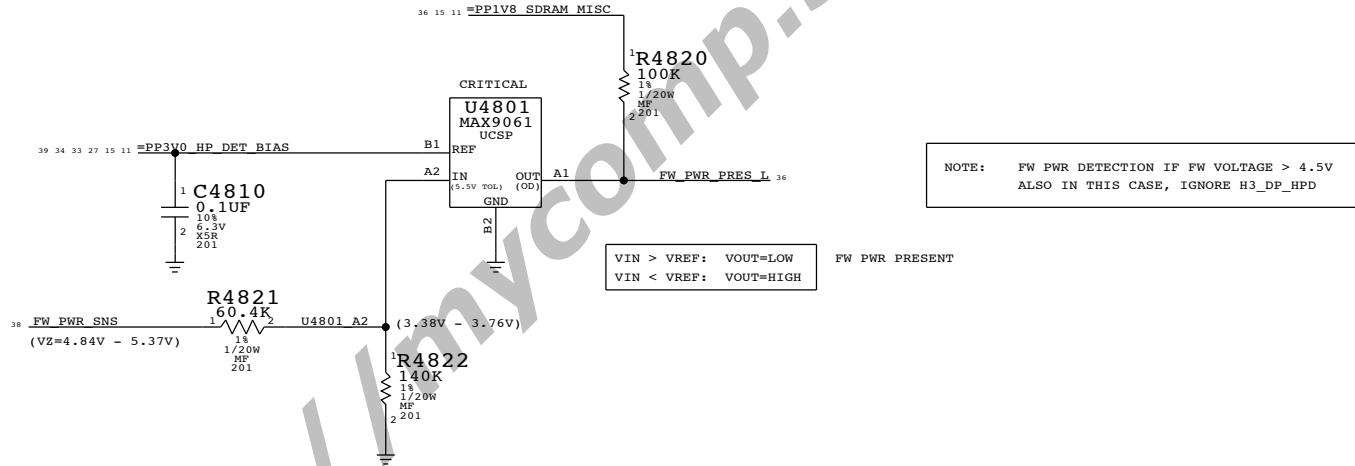
C

B

A



THIS IS SAME AS PREVIOUS, BUT CE APPROVED APN NUMBER (SAME PART)

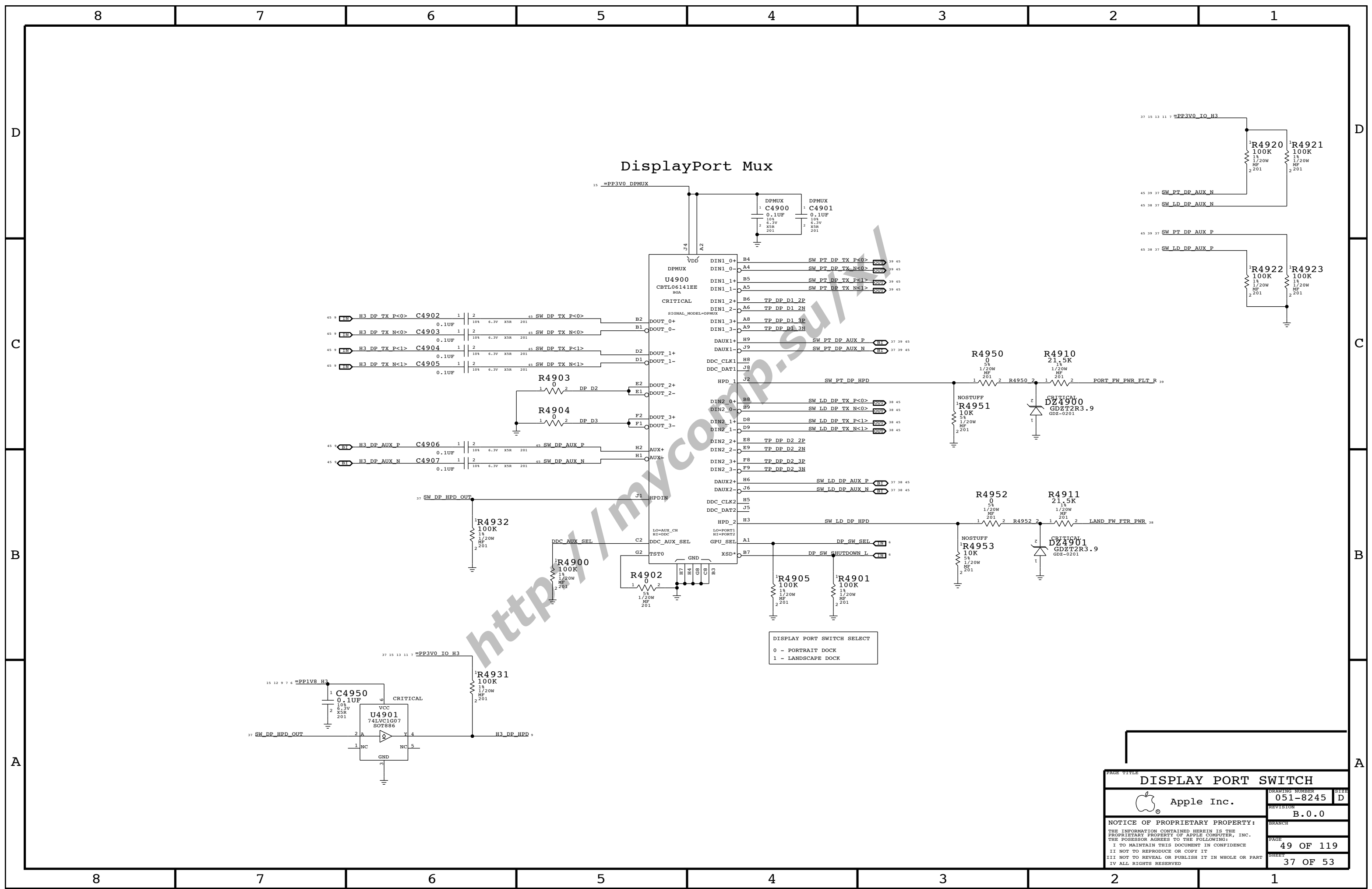


NOTE: FW PWR DETECTION IF FW VOLTAGE > 4.5V
ALSO IN THIS CASE, IGNORE H3_DP_HPD

VIN > VREF: VOUT=LOW FW PWR PRESENT
VIN < VREF: VOUT=HIGH

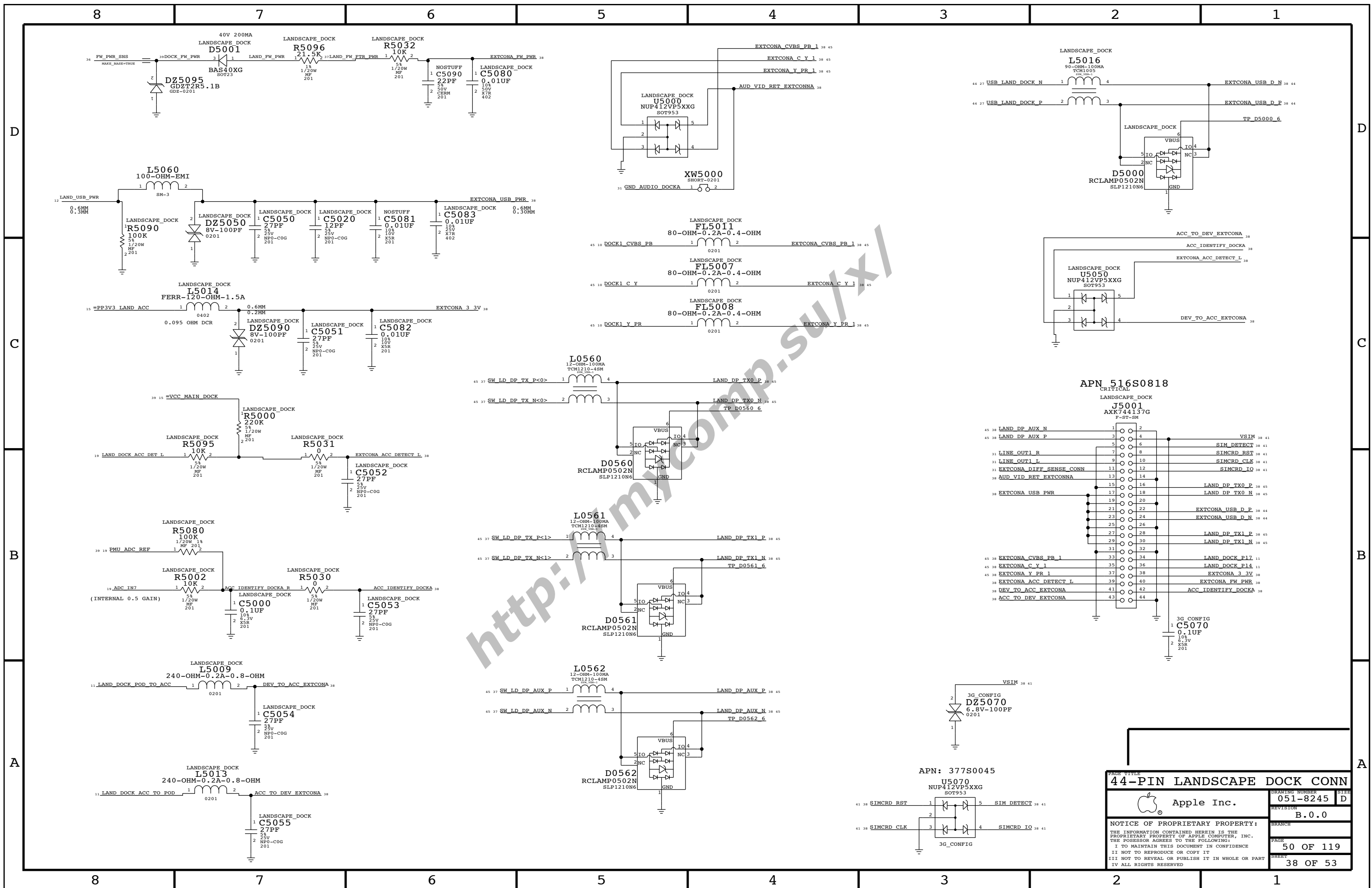
<http://mycomp.su/>

PAGE TITLE		
I/O EXPANDER		
Apple Inc.	DRAWING NUMBER	051-8245
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	PAGE	48 OF 119
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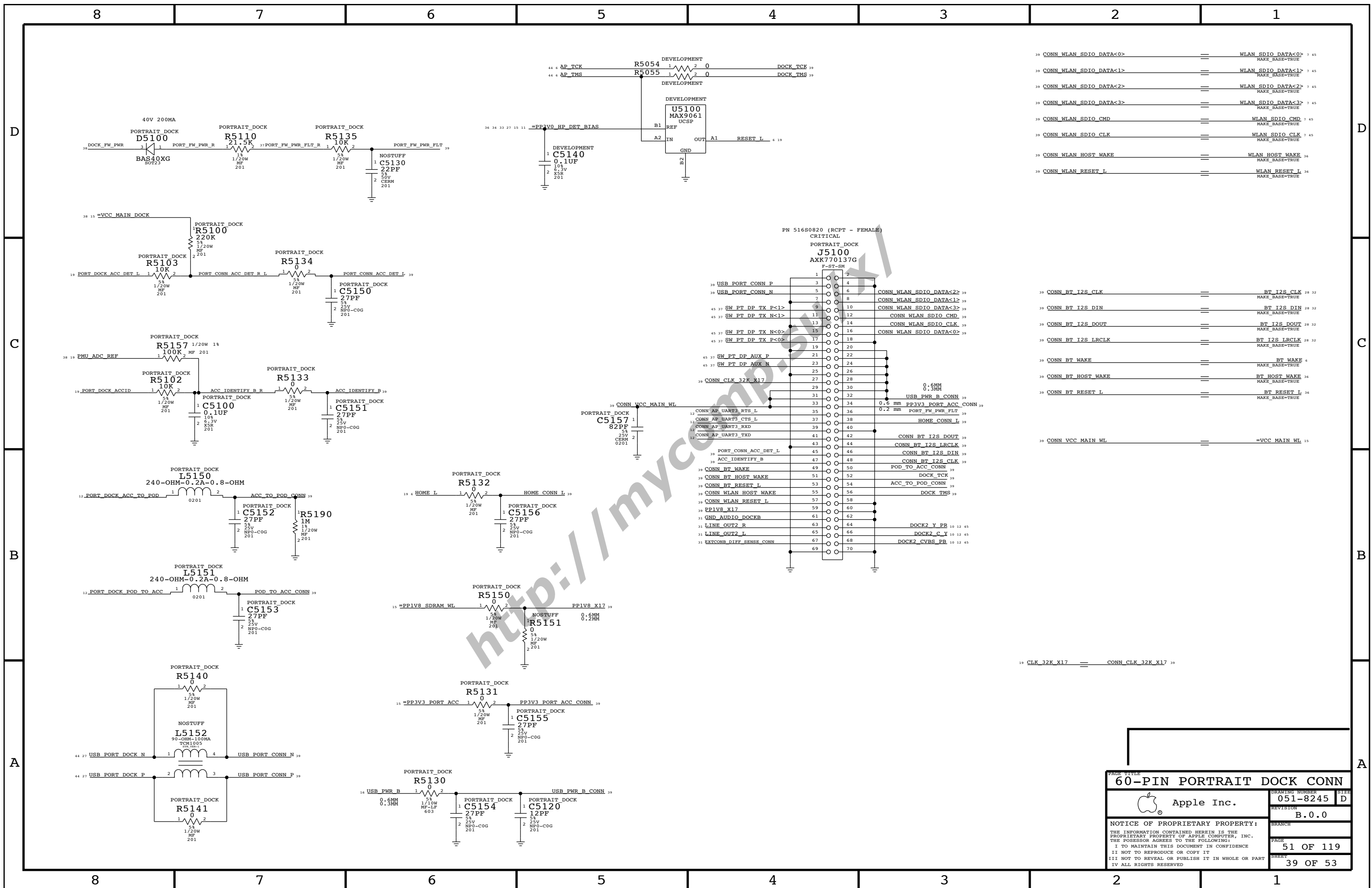


DISPLAY PORT SWITCH SELECT
 0 - PORTRAIT DOCK
 1 - LANDSCAPE DOCK

PAGE TITLE		
DISPLAY PORT SWITCH		
Apple Inc.	DRAWING NUMBER	051-8245
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44-PIN LANDSCAPE DOCK CONN Apple Inc.		DRAWING NUMBER 051-8245	SIZE D
REVISION B.0.0		BRANCH	
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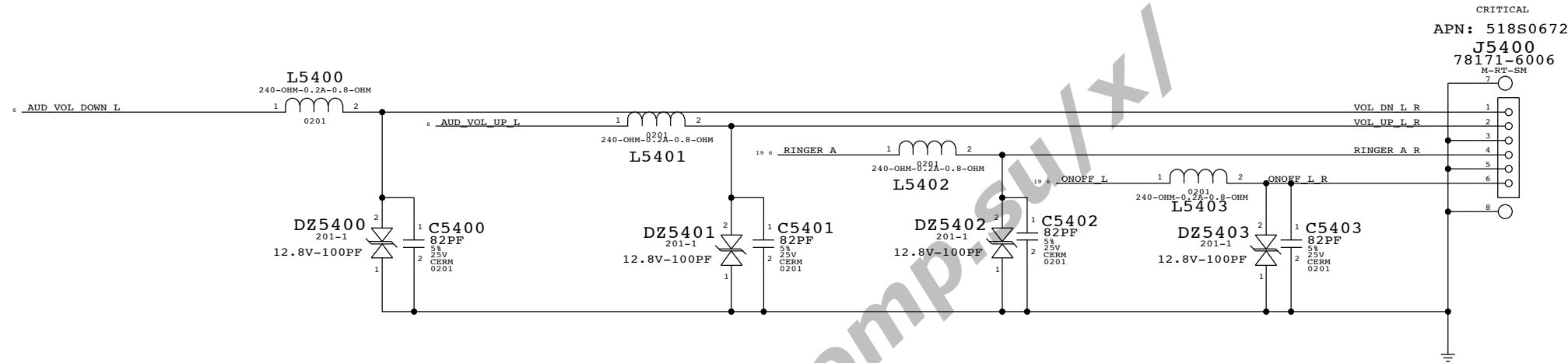
39 CONN WLAN SDIO DATA<0>	==	WLAN SDIO DATA<0>	7 45
39 CONN WLAN SDIO DATA<1>	==	WLAN SDIO DATA<1>	7 45
39 CONN WLAN SDIO DATA<2>	==	WLAN SDIO DATA<2>	7 45
39 CONN WLAN SDIO DATA<3>	==	WLAN SDIO DATA<3>	7 45
39 CONN WLAN SDIO CMD	==	WLAN SDIO CMD	7 45
39 CONN WLAN SDIO CLK	==	WLAN SDIO CLK	7 45
39 CONN WLAN HOST WAKE	==	WLAN HOST WAKE	36
39 CONN WLAN RESET L	==	WLAN RESET L	36


39 CONN BT I2S_CLK	==	BT I2S_CLK	28 32
39 CONN BT I2S DIN	==	BT I2S DIN	28 32
39 CONN BT I2S DOUT	==	BT I2S DOUT	28 32
39 CONN BT I2S LRCLK	==	BT I2S LRCLK	28 32
39 CONN BT WAKE	==	BT WAKE	6
39 CONN BT_HOST WAKE	==	BT_HOST WAKE	36
39 CONN BT RESET L	==	BT RESET L	36
39 CONN VCC MAIN WL	==	=VCC MAIN WL	15

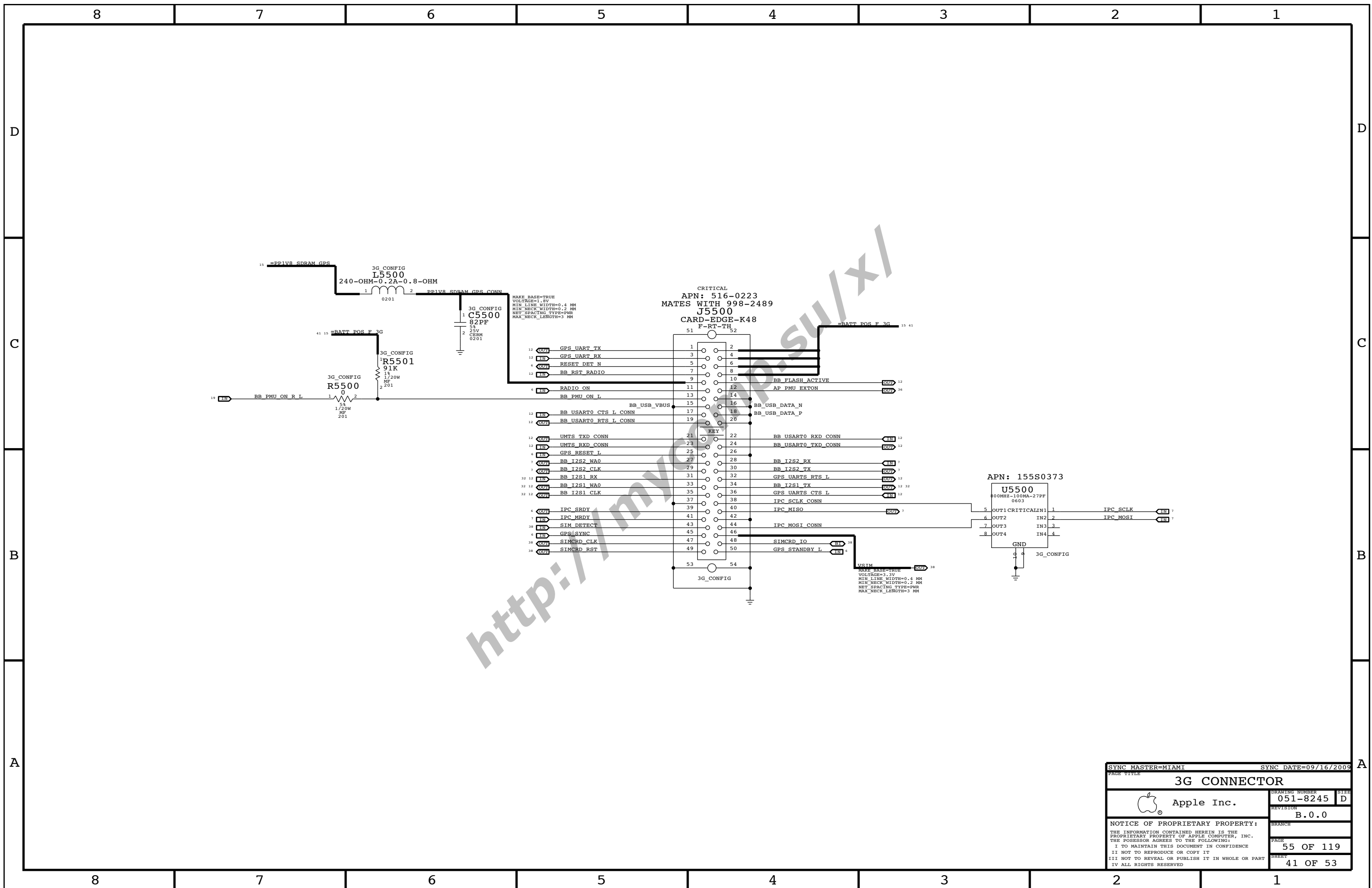
19 CLK 32K X17 == CONN_CLK 32K X17 39

PAGE TITLE		
60-PIN PORTRAIT DOCK CONN		
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	REVISION	B.0.0
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BUTTON CONNECTOR



SYNC MASTER=MIAMI		SYNC DATE=09/16/2009	
BUTTONS CONNECTOR			
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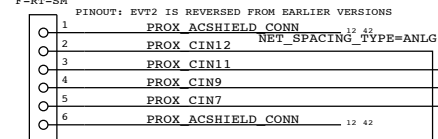
SYNC MASTER=MIAMI		SYNC DATE=09/16/2009	
3G CONNECTOR			
Apple Inc.		DRAWING NUMBER	SIZE
		051-8245	D
		REVISION	
		B.0.0	
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VDRIVE IS 3.0V TO DRIVE I2C, AND GPIO. INT WILL BE PULLED UP TO 1.8V

NET_SPACING_TYPE=ANLG
NET_SPACING_TYPE=ANLG
NET_SPACING_TYPE=ANLG
NET_SPACING_TYPE=ANLG

CIN9 AND CIN11 GO TO ACTUAL SENSOR
(ONE TOP SENSOR LAYER, ONE BOTTOM SHIELD LAYER).
CIN7 AND CIN12 GO UP FLEX
BUT DO NOT CONNECT TO SENSOR.
USED FOR DIFFERENTIAL MEASUREMENT TECHNIQUE.

CRITICAL
3G_CONFIG
J5700
FF18-6A-R11AD-B-3H
F-RT-SH



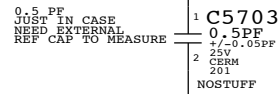
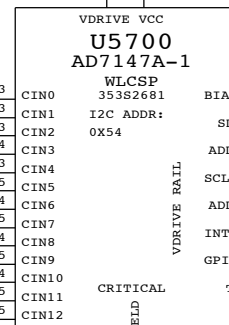
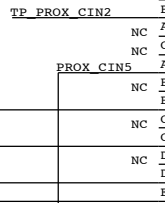
518S0692 NET_SPACING_TYPE=ANLG
NET_SPACING_TYPE=ANLG

PCB: ACSHIELD NEEDS TO BE
A PLANE UNDER PROX CIN NETS
AND ALSO TIE TO CONNECTOR.

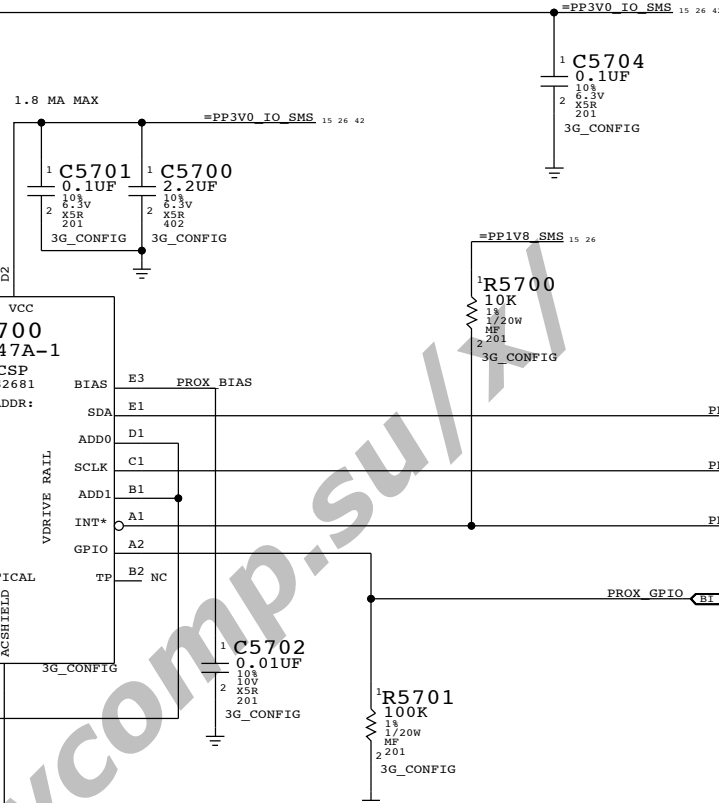
ON DEV BOARD: CIN11/ACSHIELD GOES TO COAX CONNECTOR.
ON MIAMI, CIN7/CIN9/CIN11/CIN12/ACSHIELD GOES TO 6-PIN ZIF.
CHOSE CIN NUMBERS FOR LAYOUT EASE

ALIAS TO PROX_ACSHIELD_CONN 12 _PROX_ACSHIELD

NET_SPACING_TYPE=ANLG



PCB: ENSURE ACSHIELD PLANE UNDER
U5700, NO GND PLANE NEAR PROX_CIN NETS..



PROX_GPIO IS 3.0V LEVEL.
INT IS 1.8V LEVEL.

SYNC MASTER=MARKSIN		SYNC DATE=10/14/2009	
PAGE TITLE			
PROX SENSOR			
Apple Inc.	DRAWING NUMBER	051-8245	SIZE D
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16GB FLASH CONFIGURATIONS

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
335S0648	2	TOSHIBA 43NM 8GB	U6700,U6710	16GB_PROD

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
335S0683	335S0648	16GB_PROD	U6700,U6710	SAMSUNG 35NM 8GB

32GB FLASH CONFIGURATIONS

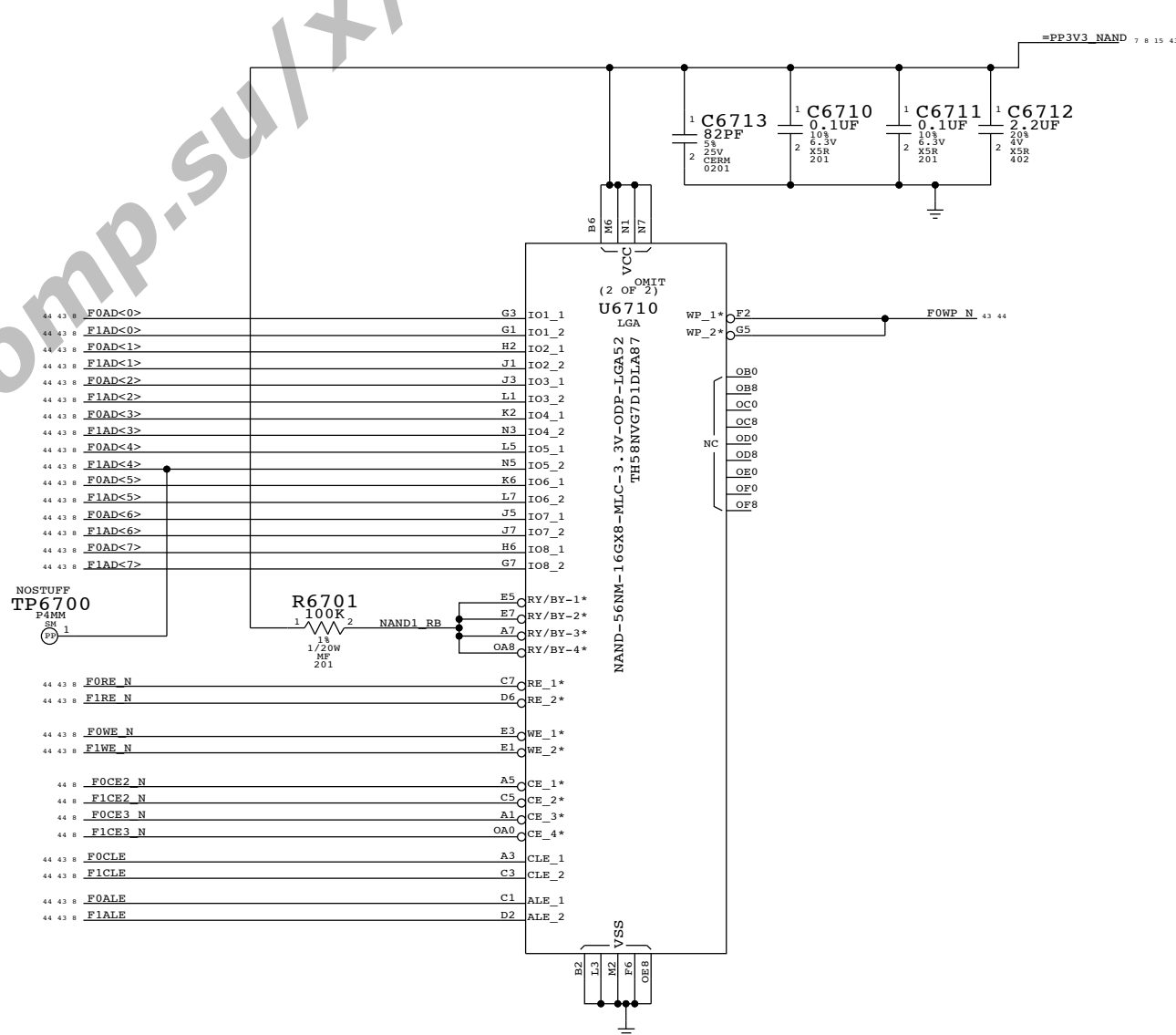
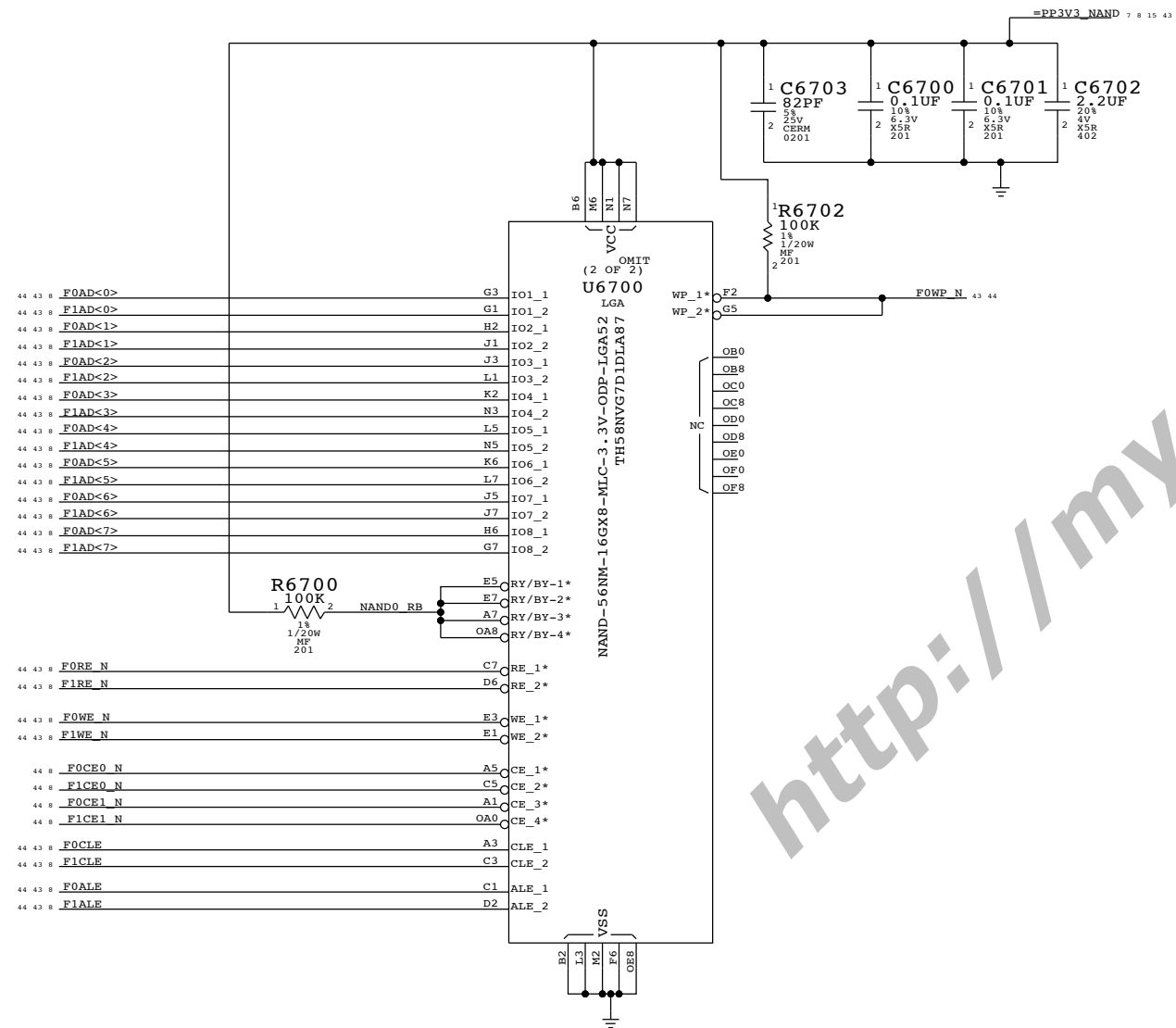
PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
335S0649	2	TOSHIBA 43NM 16GB	U6700,U6710	32GB_PROD

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
335S0682	335S0649	32GB_PROD	U6700,U6710	SAMSUNG 35NM 16GB

64GB FLASH CONFIGURATIONS

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
335S0650	2	TOSHIBA 43NM 32GB	U6700,U6710	64GB_PROD

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
335S0665	335S0650	64GB_PROD	U6700,U6710	SAMSUNG 35NM 32GB



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SYNC MASTER=MIAMI		SYNC DATE=09/16/2009	
FLASH			
Apple Inc.		DRAWING NUMBER	SIZE
		051-8245	D
		REVISION	
		B.0.0	
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Clock Signal Constraints

NET_PHYSICAL_TYPE	AREA_TYPE	PHYSICAL_RULE_SET
CLK_50S	*	50_OHM_SE

NET_SPACING_TYPE1	NET_SPACING_TYPE2	AREA_TYPE	SPACING_RULE_SET
CLK	*	*	0P5MM_SPACING

USB 2.0 Interface Constraints

NET_PHYSICAL_TYPE	AREA_TYPE	PHYSICAL_RULE_SET
USB_90D	*	90_OHM_DIFF

NET_SPACING_TYPE1	NET_SPACING_TYPE2	AREA_TYPE	SPACING_RULE_SET
USB	*	*	0P5MM_SPACING

OTHER CONSTRAINTS


NET_PHYSICAL_TYPE	AREA_TYPE	PHYSICAL_RULE_SET
I2C_50S	*	50_OHM_SE
NAND_50S	*	50_OHM_SE
AUDIO	*	1:1_DIFFPAIR
SPEAKER	*	SPEAKER

NET_SPACING_TYPE1	NET_SPACING_TYPE2	AREA_TYPE	SPACING_RULE_SET
NAND	*	*	1.5:1_SPACING
I2C	*	*	1.5:1_SPACING
AUDIO	*	*	3:1_SPACING

ELECTRICAL_CONSTRAINT_SET	NET_TYPE				
	PHYSICAL	SPACING			
E115		JTAG	AP_TCK	6 39	
E116		JTAG	AP_TMS	6 39	
E117		JTAG	AP_TDI	6 12	
E118		JTAG	AP_TDO	12	
E120		JTAG	AP_RTCK		
E18	USB	USB_90D	USB_LAND DOCK P	27 38	
E19	USB	USB_90D	USB_LAND DOCK N	27 38	
E20	USB	USB_90D	USB_PORT DOCK P	27 39	
E21	USB	USB_90D	USB_PORT DOCK N	27 39	
E22		USB_90D	EXTCONA USB D P	38	
E23		USB_90D	EXTCONA USB D N	38	
E24	USB	USB_90D	USB_DP	6 27	
E25	USB	USB_90D	USB_DM	6 27	
E222			I2S1_DOUT	7 12	
E223			I2S1_DIN	7 12	
E224			I2S1_BCLK	7 12	
E225			I2S1_LRCLK	7 12	
E226			I2S1_MCLK		
E283		CLK_50S	CLK	CLK_32K_PMU	19 24
E349		NAND_50S	NAND	F1AD<7..0>	8 43
E350		NAND_50S	NAND	F0AD<7..0>	8 43
E351		NAND_50S	NAND	F0CE0 N	8 43
E352		NAND_50S	NAND	F0CE1 N	8 43
E353		NAND_50S	NAND	F0CE2 N	8 43
E354		NAND_50S	NAND	F0CE3 N	8 43
E355		NAND_50S	NAND	F0CLE	8 43
E356		NAND_50S	NAND	F0ALE	8 43
E357		NAND_50S	NAND	F0RE N	8 43
E358		NAND_50S	NAND	F0WE N	8 43
E359		NAND_50S	NAND	F0WP N	43
E361		NAND_50S	NAND	F1CE0 N	8 43
E362		NAND_50S	NAND	F1CE1 N	8 43
E363		NAND_50S	NAND	F1CE2 N	8 43
E364		NAND_50S	NAND	F1CE3 N	8 43
E365		NAND_50S	NAND	F1CLE	8 43
E366		NAND_50S	NAND	F1ALE	8 43
E367		NAND_50S	NAND	F1RE N	8 43
E368		NAND_50S	NAND	F1WE N	8 43
E369		NAND_50S	NAND	F1WP N	
E378	SPEAKER	AUDIO	SPKRAMP_L_OUT_P	29	
E379	SPEAKER	AUDIO	SPKRAMP_L_OUT_N	29	
E380	SPEAKER	AUDIO	SPKRAMP_R_OUT_P	29	
E381	SPEAKER	AUDIO	SPKRAMP_R_OUT_N	29	
E382	SPEAKER_ECS	AUDIO	EAR_OUT_P	28 29	
E383	AUDIO	AUDIO	EAR_OUT_N	28 29	
E389	AUDIO	AUDIO	SSM2319_L_IN_P	29	
E390	AUDIO	AUDIO	SSM2319_L_IN_N	29	
E396	SPEAKER_ECS	AUDIO	MONO_OUT_P	28 29	
E397	AUDIO	AUDIO	MONO_OUT_N	28 29	
E398	AUDIO	AUDIO	SSM2319_R_IN_P	29	
E399	AUDIO	AUDIO	SSM2319_R_IN_N	29	

I2C BUS NET PROPERTIES

ELECTRICAL_CONSTRAINT_SET	NET_TYPE				
	PHYSICAL	SPACING			
E11	I2C1_ECS	I2C_50S	I2C	I2C1_SDA_1V8	7
E12	I2C1_ECS	I2C_50S	I2C	I2C1_SCL_1V8	7
E13	I2C0_ECS	I2C_50S	I2C	I2C0_SDA_1V8	7 17 19 26 28 33 36
E14	I2C0_ECS	I2C_50S	I2C	I2C0_SCL_1V8	7 17 19 26 28 33 36
E15	I2C2_ECS	I2C_50S	I2C	I2C2_SDA_3V0	7 12 13 17 26 35 44
E16	I2C2_ECS	I2C_50S	I2C	I2C2_SCL_3V0	7 12 13 17 26 35 44

SYNC MASTER=MIAMI		SYNC DATE=09/16/2009	
CONSTRAINTS			
 Apple Inc.		DRAWING NUMBER	051-8245
		REVISION	B.0.0
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Video Signal Constraints

PHYSICAL_RULE_SET	LAYER	ALLOW ROUTE ON LAYER?	MINIMUM LINE WIDTH	MINIMUM NECK WIDTH	MAXIMUM NECK LENGTH	DIFFPAIR PRIMARY GAP	DIFFPAIR NECK GAP
VID_50S	*	Y	=50_OHM_SE	=50_OHM_SE	=50_OHM_SE	=STANDARD	=STANDARD

NET_PHYSICAL_TYPE	AREA_TYPE	PHYSICAL_RULE_SET
LVDS_100D	*	90_OHM_DIFF
MIPI_100D	*	90_OHM_DIFF
SMIA_100D	*	90_OHM_DIFF
DP_100D	*	90_OHM_DIFF

NET_SPACING_TYPE1	NET_SPACING_TYPE2	AREA_TYPE	SPACING_RULE_SET
ANALOG_VIDEO	*	*	2.5:1_SPACING
LVDS	*	*	4:1_SPACING
MIPI	*	*	4:1_SPACING
SMIA	*	*	4:1_SPACING
DP	*	*	4:1_SPACING

SDIO SIGNAL CONSTRAINTS

NET_PHYSICAL_TYPE	AREA_TYPE	PHYSICAL_RULE_SET
SDIO_50S	*	50_OHM_SE

NET_SPACING_TYPE1	NET_SPACING_TYPE2	AREA_TYPE	SPACING_RULE_SET
SDIO	*	*	1.5:1_SPACING

MIPI, SMIA AND DISPLAYPORT BUS CONSTRAINTS

ELECTRICAL_CONSTRAINT_SET	NET_TYPE				
	PHYSICAL	SPACING			
E230	MIPI_ECS	MIPI_100D	MIPI	H3 MIPID DATA P<0>	9 14
E231	MIPI_ECS	MIPI_100D	MIPI	H3 MIPID DATA N<0>	9 14
E232	MIPI_ECS	MIPI_100D	MIPI	H3 MIPID DATA P<1>	9 14
E233	MIPI_ECS	MIPI_100D	MIPI	H3 MIPID DATA N<1>	9 14
E234	MIPI_ECS	MIPI_100D	MIPI	H3 MIPID DATA P<2>	9 14
E235	MIPI_ECS	MIPI_100D	MIPI	H3 MIPID DATA N<2>	9 14
E236	MIPI_ECS	MIPI_100D	MIPI	H3 MIPID DATA P<3>	9 14
E237	MIPI_ECS	MIPI_100D	MIPI	H3 MIPID DATA N<3>	9 14
E238	MIPI_ECS	MIPI_100D	MIPI	H3 MIPID CLK P	9 14
E239	MIPI_ECS	MIPI_100D	MIPI	H3 MIPID CLK N	9 14
E240	SMIA_ECS	SMIA_100D	SMIA	CAM SMIA DATA P	
E241	SMIA_ECS	SMIA_100D	SMIA	CAM SMIA DATA N	
E242	SMIA_ECS	SMIA_100D	SMIA	CAM SMIA CLK P	
E243	SMIA_ECS	SMIA_100D	SMIA	CAM SMIA CLK N	
E244		SMIA_100D	SMIA	CONN SMIA CLK P	
E245		SMIA_100D	SMIA	CONN SMIA CLK N	
E246	DP_H3_ECS	DP_100D	DP	H3 DP TX P<0>	9 37
E247	DP_H3_ECS	DP_100D	DP	H3 DP TX N<0>	9 37
E248	DP_H3_ECS	DP_100D	DP	H3 DP TX P<1>	9 37
E249	DP_H3_ECS	DP_100D	DP	H3 DP TX N<1>	9 37
E250	DP_H3_ECS	DP_100D	DP	H3 DP AUX P	9 37
E251	DP_H3_ECS	DP_100D	DP	H3 DP AUX N	9 37
E252		DP_100D	DP	SW DP TX P<0>	37
E253		DP_100D	DP	SW DP TX N<0>	37
E254		DP_100D	DP	SW DP TX P<1>	37
E255		DP_100D	DP	SW DP TX N<1>	37
E256		DP_100D	DP	SW DP AUX P	37
E257		DP_100D	DP	SW DP AUX N	37
E258	DP_PORT_ECS	DP_100D	DP	SW_PT DP TX P<0>	37 39
E259	DP_PORT_ECS	DP_100D	DP	SW_PT DP TX N<0>	37 39
E260	DP_PORT_ECS	DP_100D	DP	SW_PT DP TX P<1>	37 39
E261	DP_PORT_ECS	DP_100D	DP	SW_PT DP TX N<1>	37 39
E262	DP_PORT_ECS	DP_100D	DP	SW_PT DP AUX P	37 39
E263	DP_PORT_ECS	DP_100D	DP	SW_PT DP AUX N	37 39
E264	DP_LAND_ECS	DP_100D	DP	SW_LD DP TX P<0>	37 38
E265	DP_LAND_ECS	DP_100D	DP	SW_LD DP TX N<0>	37 38
E266	DP_LAND_ECS	DP_100D	DP	SW_LD DP TX P<1>	37 38
E267	DP_LAND_ECS	DP_100D	DP	SW_LD DP TX N<1>	37 38
E268	DP_LAND_ECS	DP_100D	DP	SW_LD DP AUX P	37 38
E269	DP_LAND_ECS	DP_100D	DP	SW_LD DP AUX N	37 38
E270		DP_100D	DP	LAND DP TX0 P	38
E271		DP_100D	DP	LAND DP TX0 N	38
E272		DP_100D	DP	LAND DP TX1 P	38
E273		DP_100D	DP	LAND DP TX1 N	38
E274		DP_100D	DP	LAND DP AUX P	38
E275		DP_100D	DP	LAND DP AUX N	38
E276		DP_100D	DP	LAND DP TX0 P	38
E277		DP_100D	DP	LAND DP TX0 N	38
E278		DP_100D	DP	LAND DP TX1 P	38
E279		DP_100D	DP	LAND DP TX1 N	38
E280		DP_100D	DP	LAND DP AUX P	38
E281		DP_100D	DP	LAND DP AUX N	38

ANALOG VIDEO CONSTRAINTS


ELECTRICAL_CONSTRAINT_SET	NET_TYPE			
	PHYSICAL	SPACING		
E282	VID_50S	ANALOG_VIDEO	DAC_OUT1	10
E283	VID_50S	ANALOG_VIDEO	DAC_OUT2	10
E284	VID_50S	ANALOG_VIDEO	DAC_OUT3	10
E285	VID_50S	ANALOG_VIDEO	LAND YOUT	10
E286	VID_50S	ANALOG_VIDEO	LAND CVBS_OUT	10
E287	VID_50S	ANALOG_VIDEO	LAND COUT	10
E288	VID_50S	ANALOG_VIDEO	PORT YOUT	10
E289	VID_50S	ANALOG_VIDEO	PORT CVBS_OUT	10
E290	VID_50S	ANALOG_VIDEO	PORT COUT	10
E291	VID_50S	ANALOG_VIDEO	DOCK1 CVBS_PB	10 38
E292	VID_50S	ANALOG_VIDEO	DOCK1 C Y	10 38
E293	VID_50S	ANALOG_VIDEO	DOCK1 Y PR	10 38
E294	VID_50S	ANALOG_VIDEO	EXTCONA CVBS_PB 1	38
E295	VID_50S	ANALOG_VIDEO	EXTCONA C Y 1	38
E296	VID_50S	ANALOG_VIDEO	EXTCONA Y PR 1	38
E297	VID_50S	ANALOG_VIDEO	DOCK2 CVBS_PB	10 12 39
E298	VID_50S	ANALOG_VIDEO	DOCK2 C Y	10 12 39
E299	VID_50S	ANALOG_VIDEO	DOCK2 Y PR	10 12 39

LVDS CONSTRAINTS

ELECTRICAL_CONSTRAINT_SET	NET_TYPE				
	PHYSICAL	SPACING			
E300	LVDS_ECS	LVDS_100D	LVDS	LVDS DATA P<2..0>	13 25
E301	LVDS_ECS	LVDS_100D	LVDS	LVDS DATA N<2..0>	13 25
E302	LVDS_ECS	LVDS_100D	LVDS	LVDS CLK P	13 25
E303	LVDS_ECS	LVDS_100D	LVDS	LVDS CLK N	13 25
E304		LVDS_100D	LVDS	LVDS CONN CLK P	25
E305		LVDS_100D	LVDS	LVDS CONN CLK N	25
E306		LVDS_100D	LVDS	LVDS DAT P<2..0>	25
E307		LVDS_100D	LVDS	LVDS DAT N<2..0>	25

HX SDIO CONSTRAINTS

ELECTRICAL_CONSTRAINT_SET	NET_TYPE				
	PHYSICAL	SPACING			
E308	WLAN_SDIO_ECS	SDIO_50S	SDIO	WLAN SDIO CLK	7 39
E309	WLAN_SDIO_CMD_ECS	SDIO_50S	SDIO	WLAN SDIO CMD	7 39
E310	WLAN_SDIO_ECS	SDIO_50S	SDIO	WLAN SDIO DATA<3..0>	7 39

SYNC MASTER=MIAMI		SYNC DATE=09/16/2009	
MORE CONSTRAINTS			
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MIAMI BOARD-SPECIFIC SPACING & PHYSICAL CONSTRAINTS (10-LAYER)

BOARD LAYERS		BOARD AREAS		BOARD UNITS (MIL OR MM)	ALLEGRO VERSION
TOP, ISL2, ISL3, ISL4, ISL5, ISL6, ISL7, ISL8, ISL9, BOTTOM		NO_TYPE, BGA		MM	15.2

PHYSICAL_RULE_SET	LAYER	ALLOW ROUTE ON LAYER?	MINIMUM LINE WIDTH	MINIMUM NECK WIDTH	MAXIMUM NECK LENGTH	DIFFPAIR PRIMARY GAP	DIFFPAIR NECK GAP
DEFAULT	*	Y	=50_OHM_SE	=50_OHM_SE	30 MM	0 MM	0 MM
STANDARD	*	Y	=DEFAULT	=DEFAULT	12.7 MM	=DEFAULT	=DEFAULT

PHYSICAL_RULE_SET	LAYER	ALLOW ROUTE ON LAYER?	MINIMUM LINE WIDTH	MINIMUM NECK WIDTH	MAXIMUM NECK LENGTH	DIFFPAIR PRIMARY GAP	DIFFPAIR NECK GAP
50_OHM_SE	TOP, BOTTOM	Y	0.230 MM	0.070 MM	3.0 MM		
50_OHM_SE	ISL2, ISL9	Y	0.076 MM	0.070 MM	3.0 MM		
50_OHM_SE	ISL4, ISL7	Y	0.076 MM	0.070 MM	3.0 MM		
50_OHM_SE	*	N	0.070 MM	0.070 MM	3.0 MM		

PHYSICAL_RULE_SET	LAYER	ALLOW ROUTE ON LAYER?	MINIMUM LINE WIDTH	MINIMUM NECK WIDTH	MAXIMUM NECK LENGTH	DIFFPAIR PRIMARY GAP	DIFFPAIR NECK GAP
BGA	*	Y	0.075 MM	0.075 MM	=STANDARD	0.076 MM	0.075 MM

PHYSICAL_RULE_SET	LAYER	ALLOW ROUTE ON LAYER?	MINIMUM LINE WIDTH	MINIMUM NECK WIDTH	MAXIMUM NECK LENGTH	DIFFPAIR PRIMARY GAP	DIFFPAIR NECK GAP
90_OHM_DIFF	*	Y	=STANDARD	=STANDARD	=STANDARD	=STANDARD	=STANDARD
90_OHM_DIFF	ISL4, ISL7	Y	0.070 MM	0.070 MM		0.200 MM	0.100 MM
90_OHM_DIFF	TOP, BOTTOM	Y	0.070 MM	0.070 MM		0.200 MM	0.200 MM

PHYSICAL_RULE_SET	LAYER	ALLOW ROUTE ON LAYER?	MINIMUM LINE WIDTH	MINIMUM NECK WIDTH	MAXIMUM NECK LENGTH	DIFFPAIR PRIMARY GAP	DIFFPAIR NECK GAP
1:1_DIFFPAIR	*	Y	=STANDARD	=STANDARD	=STANDARD	0.075 MM	0.075 MM
SPEAKER	*	Y	0.3 MM	0.19MM	10 MM	0.075 MM	0.075 MM

SPACING_RULE_SET	LAYER	LINE-TO-LINE SPACING	WEIGHT
DEFAULT	*	0.08 MM	?
STANDARD	*	=DEFAULT	?
BGA	*	=DEFAULT	?

NET_SPACING_TYPE1	NET_SPACING_TYPE2	AREA_TYPE	SPACING_RULE_SET
*	*	BGA	BGA
CLK	*	BGA	BGA
PWR	*	*	PWR_P1SPACING
GND	*	*	GND_P1SPACING
SWITCHNODE	*	*	SWITCHNODE
PWR	*	*	PWR_P1SPACING
ANLG	*	*	3:1_SPACING
CRYSTAL	*	*	3:1_SPACING
JTAG	*	*	2:1_SPACING
I2S_ST	*	*	2:1_SPACING
I2S_ST	I2S_ST	*	1.5:1_SPACING

NET_PHYSICAL_TYPE	AREA_TYPE	PHYSICAL_RULE_SET
*	BGA	BGA

NOTES:

- 0.075 MM ~ 3 MIL
- 0.089 MM ~ 3.5 MIL
- 0.102 MM ~ 4 MIL
- 0.114 MM ~ 4.5 MIL
- 0.125 MM ~ 5 MIL
- 0.140 MM ~ 5.5 MIL
- 0.15 MM ~ 6 MIL
- 0.18 MM ~ 7 MIL
- 0.2 MM ~ 8 MIL
- 0.25 MM ~ 10 MIL
- 0.3 MM ~ 12 MIL
- 0.33 MM ~ 13 MIL
- 0.4 MM ~ 16 MIL
- 1.0 MM = 39.37 MIL

SPACING_RULE_SET	LAYER	LINE-TO-LINE SPACING	WEIGHT
1:1_SPACING	*	0.075 MM	?
1.5:1_SPACING	*	0.114 MM	?
1.8:1_SPACING	*	0.136 MM	?
2:1_SPACING	*	0.152 MM	?
2.5:1_SPACING	*	0.190 MM	?
3:1_SPACING	*	0.228 MM	?
4:1_SPACING	*	0.304 MM	?
0P64MM_SPACING	*	0.64 MM	?
0P5MM_SPACING	*	0.5 MM	?
PWR_P1SPACING	*	0.1 MM	900
GND_P1SPACING	*	0.1 MM	950
SWITCHNODE	*	0.5 MM	1000
SWITCHNODE	TOP, BOTTOM	0.2 MM	1000

SYNC MASTER=MIAMI		SYNC DATE=09/16/2009	
PHYSICAL/SPACING RULES			
Apple Inc.		DRAWING NUMBER	051-8245
		REVISION	B.0.0
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Title: Basenet Report
Design: 19
Date: Feb 19 11:01:06 2008
Base Signal Synonyms Location((Zone)|(dir))
Base Signal Synonyms Location((Zone)|(dir))
=PPMEM_S0_VTT_SCH =PPMEM_S0_VTT_SCH =PPMEM_S0_VTT_SCH =PPMEM_S0_VTT_SCH
=PPVCORE_S0_CPU =PPVCORE_S0_CPU =PPVCORE_S0_CPU =PPVCORE_S0_CPU
=PPV05_S0_OUT =PPV05_S0_OUT =PPV05_S0_OUT =PPV05_S0_OUT

PPMEMV_S3_OUT @lost_lib.LOST 1705 2381 2384 23C4 23D6
=PPMEMV_S3_WL @lost_lib.LOST 1704 96C5
=PPMEMV_S3_SCH @lost_lib.LOST 1288 17D4
=PPMEMV_S3_MEM @lost_lib.LOST 11C1 14A5 14B4 14B8 14D4

AUD_LO2_PORT_N_R @lost_lib.LOST 36C2 37B7
AUD_LO2_PORT_P_L @lost_lib.LOST 36C2 37B7 76B7
AUD_LO2_PORT_P_R @lost_lib.LOST 36C2 37B7 76B7
AUD_LO2_R @lost_lib.LOST 39A5 39C1
AUD_LO2_RET @lost_lib.LOST 39A5 39C1

SCH_CLK_DREFFSCLKIN_N @lost_lib.LOST 10A5 16A3 105D2
@lost_lib.LOST
CK505_SRC11_P @lost_lib.LOST 15B4 16A5
SCH_CLK_DREFFSCLKIN_P @lost_lib.LOST 10A5 16A3 105D2

CK505_USB0_FSA @lost_lib.LOST 15B6 15C2 105C2
CK505_XI @lost_lib.LOST 15C6
CK505_XO @lost_lib.LOST 15C6
CLK_LPC_0 @lost_lib.LOST 9D6 9B7 105C2

	8	7	6	5	4	3	2	1
D	TP_LVDS_DATAP3 TP_LVDS_VSYNC_OUT TP_PCIE_PER1_N TP_PCIE_PER1_P TP_PCIE_PER2_N TP_PCIE_PER2_P TP_PCIE_PET1_N TP_PCIE_PET1_P TP_PCIE_PET2_N TP_PCIE_PET2_P TP_SCART_CVBS TP_SCART_VGA_B TP_SCART_VGA_G TP_SCART_VGA_R TP_SCH_CLK_LPC_1 TP_SCH_CLK_LPC_2 TP_SCH_GPIOSUB0 TP_SCH_GPIO_9 TP_SCH_PILMON1 TP_SCH_PILMON1_L TP_SCH_RESVRD0 TP_SCH_RESVRD8 TP_SCH_SD0_DATA6 TP_SCH_SD1_CLK TP_SCH_SD2_CLK TP_U0600_G28 TP_U0600_R27 TP_U0600_U30 TP_U0600_V27 TP_U3101_TCK TP_U3101_TDI TP_U3101_TDO TP_U3101_TMS TP_USB_D_N TP_USB_D_P TP_USB_G_N TP_USB_G_P TP_USB_H_N TP_USB_H_P TP_WL_TCK TP_WL_TDI TP_WL_TDO TP_WL_TMS TP_WL_TRST_L TVCLK_N TVCLK_P TVOUT_DOCKS_EN U6700_R1 U6700_R2 U6700_R3 U6700_R4 U6710_R1 U6710_R2 U6710_R3 U6710_R4 USBA_EXT_N USBA_EXT_PR USBA_GATE USBA_GATE_D USBA_PWR_DETECT_L USBA_PWR_SLCT USBB_EXT_N USBB_EXT_PR USBB_GATE USBB_GATE_D USBB_PWR_DETECT_L USBB_PWR_SLCT USBC USB_BT_N USB_BT_P USB_CAMERA_CONN_N USB_CAMERA_CONN_P USB_CAMERA_N USB_CAMERA_P USB_C_N USB_C_P USB_C_P USB_FLASH_CON_N USB_FLASH_CON_P USB_FLASH_N USB_FLASH_P USB_GRAPE_N USB_GRAPE_P USB_LAND_DOCK_N USB_LAND_DOCK_P USB_PORT_DOCK_N USB_PORT_DOCK_P USB_PSOC_N USB_PSOC_P USB_PWR_A USB_PWR_B USB_RBIA_PN VCORE_BG VCORE_BOOST VCORE_BOOST_RCD VCORE_CLSET VCORE_CS VCORE_CSN VCORE_CSP VCORE_CSP_R VCORE_DAC VCORE_DRN VCORE_ERRROUT VCORE_FBN VCORE_FBP VCORE_GND VCORE_HYS VCORE_RAMP VCORE_SS VCORE_TG VCORE_TTRIP VCORE_VCCA VCORE_VREF VR_FWRD0_CLKEN_L VSSAPCIEBG WL_HOST_WAKE WL_RESET_L WL_WAKE_L XDP_BPM_L<0> XDP_BPM_L<4..0> XDP_BPM_L<1> XDP_BPM_L<2> XDP_BPM_L<3>	TP_LVDS_DATAP3 - @lost_lib.LOST 9C6 TP_LVDS_VSYNC_OUT - @lost_lib.LOST 5D6 32C2 TP_PCIE_PER1_N - @lost_lib.LOST 9B4 TP_PCIE_PER1_P - @lost_lib.LOST 9B4 TP_PCIE_PER2_N - @lost_lib.LOST 9B4 TP_PCIE_PER2_P - @lost_lib.LOST 9B4 TP_PCIE_PET1_N - @lost_lib.LOST 9B4 TP_PCIE_PET1_P - @lost_lib.LOST 9B4 TP_PCIE_PET2_N - @lost_lib.LOST 9B4 TP_PCIE_PET2_P - @lost_lib.LOST 9B4 TP_SCART_CVBS - @lost_lib.LOST 94C5 TP_SCART_VGA_B - @lost_lib.LOST 94C5 TP_SCART_VGA_G - @lost_lib.LOST 94C5 TP_SCART_VGA_R - @lost_lib.LOST 94C5 TP_SCH_CLK_LPC_1 - @lost_lib.LOST 9D6 TP_SCH_CLK_LPC_2 - @lost_lib.LOST 9D6 TP_SCH_GPIOSUB0 - @lost_lib.LOST 10C5 TP_SCH_GPIO_9 - @lost_lib.LOST 10B5 TP_SCH_PILMON1 - @lost_lib.LOST 8A6 TP_SCH_PILMON1_L - @lost_lib.LOST 8A6 TP_SCH_RESVRD0 - @lost_lib.LOST 9D4 TP_SCH_RESVRD8 - @lost_lib.LOST 9D4 TP_SCH_SD0_DATA6 - @lost_lib.LOST 9B6 TP_SCH_SD1_CLK - @lost_lib.LOST 9B6 TP_SCH_SD2_CLK - @lost_lib.LOST 9B6 TP_U0600_G28 - @lost_lib.LOST 6B5 TP_U0600_R27 - @lost_lib.LOST 6B7 TP_U0600_U30 - @lost_lib.LOST 6B5 TP_U0600_V27 - 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@lost_lib.LOST 10D7 30D4 103C4 USB_LAND_DOCK_N - @lost_lib.LOST 35A3 35D5 50D3 103D4 USB_LAND_DOCK_P - @lost_lib.LOST 35A3 35D5 50D3 103D4 USB_PORT_DOCK_N - @lost_lib.LOST 35A3 35C5 51D3 103D4 USB_PORT_DOCK_P - @lost_lib.LOST 35A3 35C5 51D3 103D4 USB_PSOC_N - @lost_lib.LOST 30D3 USB_PSOC_P - @lost_lib.LOST 30D3 USB_PWR_A - @lost_lib.LOST 18D2 50D7 USB_PWR_B - @lost_lib.LOST 18C3 51D7 USB_RBIA_PN - @lost_lib.LOST 10C7 VCORE_BG - @lost_lib.LOST 20C5 VCORE_BOOST - @lost_lib.LOST 20C5 VCORE_BOOST_RCD - @lost_lib.LOST 20D4 VCORE_CLSET - @lost_lib.LOST 20C6 VCORE_CS - @lost_lib.LOST 20B2 VCORE_CSN - @lost_lib.LOST 20C6 VCORE_CSP - @lost_lib.LOST 20C6 VCORE_CSP_R - @lost_lib.LOST 20B3 VCORE_DAC - @lost_lib.LOST 20C6 VCORE_DRN - @lost_lib.LOST 20C5 VCORE_ERRROUT - @lost_lib.LOST 20C5 VCORE_FBN - @lost_lib.LOST 20C6 VCORE_FBP - @lost_lib.LOST 20C6 VCORE_GND - @lost_lib.LOST 20A4 20B6 20C8 VCORE_HYS - @lost_lib.LOST 20C6 VCORE_RAMP - @lost_lib.LOST 20B5 VCORE_SS - @lost_lib.LOST 20C6 VCORE_TG - @lost_lib.LOST 20C5 VCORE_TTRIP - @lost_lib.LOST 20C6 VCORE_VCCA - @lost_lib.LOST 20D6 VCORE_VREF - @lost_lib.LOST 20C4 20C8 VR_FWRD0_CLKEN_L - @lost_lib.LOST 20A7 20C4 VSSAPCIEBG - @lost_lib.LOST 12B2 WL_HOST_WAKE - @lost_lib.LOST 28A8 96B3 WL_RESET_L - @lost_lib.LOST 28D5 96B3 WL_WAKE_L - @lost_lib.LOST 29B4 XDP_BPM_L<0> - @lost_lib.LOST 6C5 90C6 XDP_BPM_L<4..0> - @lost_lib.LOST 100A3 XDP_BPM_L<1> - @lost_lib.LOST 6C5 90C6 XDP_BPM_L<2> - @lost_lib.LOST 6C5 90C6 XDP_BPM_L<3> - @lost_lib.LOST 6C5 90C6	XDP_BPM_L<4> XDP_BPM_L<5> XDP_CPURST_L XDP_DBRESET_L XDP_PWRGD XDP_SCH_TCK<1> XDP_SCH_TDI XDP_SCH_TDO XDP_SCH_TMS XDP_SCL XDP_SDA XDP_TCK XDP_TDI XDP_TDO XDP_TRST_L XTAL_FLASH_IN XTAL_FLASH_OUT Y9400_2 Y_PR Z1_V18_OUT Z1_BON_L<0> Z1_BON_L<1> Z1_BON_L<2> Z1_BON_L<3> Z1_BON_L<4> Z1_BON_L<5> Z1_B_ADDR<0> Z1_B_ADDR<1> Z1_B_ADDR<2> Z1_CS_L Z1_DONE Z1_GO Z1_MISO Z1_MOSI Z1_PCLK Z1_SCLK Z1_STWIN Z2_3V3_V18_IN Z2_VDDANA Z2_VDDCORE	XDP_BPM_L<4> - @lost_lib.LOST 6C5 90C6 XDP_BPM_L<5> - @lost_lib.LOST 6C5 6C5 90C6 100A3 XDP_CPURST_L - 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	8	7	6	5	4	3	2	1

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	8			7			6			5			4			3			2			1								
D	Title: Cref Part Report			C1800 CAP_0805 lost[18C7]			C2704 CAP_402 lost[27B6]			C4224 CAP_201 lost[42A7]																				
	Design: lost			C1801 CAP_402 lost[18B5]			C2705 CAP_402 lost[27B6]			C4225 CAP_201 lost[42B6]																				
	Date: Feb 19 11:01:06 2008			C1802 CAP_402 lost[18C5]			C2706 CAP_402 lost[27C2]			C4300 CAP_402 lost[43C5]																				
	C0500	CAP_1210	lost[5D1]	C1803	CAP_402	lost[18D4]	C2707	CAP_201	lost[27B5]	C4301	CAP_402	lost[43D4]	C5000	CAP_201	lost[50C7]	C5084	CAP_402	lost[50D7]	C5084	CAP_402	lost[50D8]	C5090	CAP_201	lost[50E6]	C5100	CAP_201	lost[51C7]	C5184	CAP_402	lost[51D7]
	C0501	CAP_1210	lost[5D1]	C1929	CAP_0805	lost[19D8]	C2757	CAP_201	lost[27A6]	C5086	CAP_402	lost[50D8]	C5200	CAP_201	lost[52C6]	C5201	CAP_201	lost[52C6]	C5202	CAP_201	lost[52C6]	C5203	CAP_201	lost[52B6]	C5208	CAP_201	lost[52C3]	C5600	CAP_201	lost[56A7]
	C0502	CAP_1210	lost[5D1]	C1930	CAP_0805	lost[19D8]	C2758	CAP_201	lost[27A6]	C5088	CAP_402	lost[50D8]	C5600	CAP_201	lost[56A7]	C6601	CAP_201	lost[66A6]	C6602	CAP_201	lost[66A6]	C6603	CAP_402	lost[66A6]	C6604	CAP_201	lost[66D7]	C6605	CAP_201	lost[66D7]
	C0503	CAP_1210	lost[5D1]	C1931	CAP_201	lost[19B8]	C2759	CAP_201	lost[27A5]	C5090	CAP_201	lost[50E6]	C6604	CAP_201	lost[66D7]	C6605	CAP_201	lost[66D7]	C6606	CAP_201	lost[66D6]	C6607	CAP_201	lost[66D6]	C6608	CAP_402	lost[66D6]	C6610	CAP_201	lost[66C2]
	C0504	CAP_1210	lost[5D1]	C1932	CAP_201	lost[19B8]	C2760	CAP_201	lost[27A5]	C6601	CAP_201	lost[66A6]	C6602	CAP_201	lost[66A6]	C6603	CAP_402	lost[66A6]	C6604	CAP_201	lost[66D7]	C6605	CAP_201	lost[66D7]	C6606	CAP_201	lost[66D6]	C6607	CAP_201	lost[66D6]
	C0505	CAP_1210	lost[5D1]	C1933	CAP_201	lost[19B8]	C2803	CAP_201	lost[28D3]	C6606	CAP_201	lost[66D6]	C6607	CAP_201	lost[66D6]	C6608	CAP_402	lost[66D6]	C6610	CAP_201	lost[66C2]	C6700	CAP_201	lost[67C5]	C6701	CAP_201	lost[67C5]	C6702	CAP_402	lost[67C5]
	C0506	CAP_1210	lost[5C1]	C1934	CAP_402	lost[19D5]	C2804	CAP_201	lost[28D3]	C6701	CAP_201	lost[67C5]	C6702	CAP_402	lost[67C5]	C6710	CAP_201	lost[67C2]	C6711	CAP_201	lost[67C2]	C6712	CAP_402	lost[67C2]	C7502	CAP_201	lost[75C7]	C7510	CAP_201	lost[75D3]
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C0705	CAP_402	lost[7D4]	C1948	CAP_201	lost[19B4]	C2909	CAP_402	lost[29D7]	C7673	CAP_201	lost[76B7]	C7674	CAP_201	lost[76B7]	C7675	CAP_201	lost[76B7]	C7676	CAP_201	lost[76B7]	C7677	CAP_201	lost[76B7]	C7678	CAP_201	lost[76B7]	C7679	CAP_201	lost[76B7]	
C0706	CAP_402	lost[7D3]	C1949	CAP_603	lost[19C2]	C2910	CAP_201	lost[29D8]	C7680	CAP_201	lost[76B7]	C7681	CAP_201	lost[76B7]	C7682	CAP_201	lost[76B7]	C7683	CAP_201	lost[76B7]	C7684	CAP_201	lost[76B7]	C7685	CAP_201	lost[76B7]	C7686	CAP_201	lost[76B7]	
C0708	CAP_201	lost[7C6]	C1950	CAP_201	lost[19B2]	C3000	CAP_603	lost[30A3]	C7687	CAP_201	lost[76B7]	C7688	CAP_201	lost[76B7]	C7689	CAP_201	lost[76B7]	C7690	CAP_201	lost[76B7]	C7691	CAP_201	lost[76B7]	C7692	CAP_201	lost[76B7]	C7693	CAP_201	lost[76B7]	
C0709	CAP_201	lost[7C6]	C1951	CAP_201	lost[19C6]	C3001	CAP_603	lost[30A5]	C7694	CAP_201	lost[76B7]	C7695	CAP_201	lost[76B7]	C7696	CAP_201	lost[76B7]	C7697	CAP_201	lost[76B7]	C7698	CAP_201	lost[76B7]	C7699	CAP_201	lost[76B7]	C7700	CAP_201	lost[76B7]	
C0710	CAP_402	lost[7C6]	C1952	CAP_201	lost[19C3]	C3002	CAP_402	lost[30D1]	C7701	CAP_201	lost[76B7]	C7702	CAP_201	lost[76B7]	C7703	CAP_201	lost[76B7]	C7704	CAP_201	lost[76B7]	C7705	CAP_201	lost[76B7]	C7706	CAP_201	lost[76B7]	C7707	CAP_201	lost[76B7]	
C0711	CAP_402	lost[7D1]	C1953	CAP_201	lost[19D2]	C3003	CAP_201	lost[30D2]	C7708	CAP_201	lost[76B7]	C7709	CAP_201	lost[76B7]	C7710	CAP_201	lost[76B7]	C7711	CAP_201	lost[76B7]	C7712	CAP_201	lost[76B7]	C7713	CAP_201	lost[76B7]	C7714	CAP_201	lost[76B7]	
C0712	CAP_402	lost[7D2]	C1954	CAP_201	lost[19C6]	C3004	CAP_201	lost[30D6]	C7715	CAP_201	lost[76B7]	C7716	CAP_201	lost[76B7]	C7717	CAP_201	lost[76B7]	C7718	CAP_201	lost[76B7]	C7719	CAP_201	lost[76B7]	C7720	CAP_201	lost[76B7]	C7721	CAP_201	lost[76B7]	
C0713	CAP_402	lost[7D2]	C1955	CAP_201	lost[19C3]	C3005	CAP_603	lost[30D6]	C7722	CAP_201	lost[76B7]	C7723	CAP_201	lost[76B7]	C7724	CAP_201	lost[76B7]	C7725	CAP_201	lost[76B7]	C7726	CAP_201	lost[76B7]	C7727	CAP_201	lost[76B7]	C7728	CAP_201	lost[76B7]	
C0714	CAP_402	lost[7D2]	C1956	CAP_201	lost[19B2]	C3006	CAP_201	lost[30D7]	C7729	CAP_201	lost[76B7]	C7730	CAP_201	lost[76B7]	C7731	CAP_201	lost[76B7]	C7732	CAP_201	lost[76B7]	C7733	CAP_201	lost[76B7]	C7734	CAP_201	lost[76B7]	C7735	CAP_201	lost[76B7]	
C0715	CAP_402	lost[7D2]	C1957	CAP_201	lost[19A3]	C3007	CAP_603	lost[30D8]	C7736	CAP_201	lost[76B7]	C7737	CAP_201	lost[76B7]	C7738	CAP_201	lost[76B7]	C7739	CAP_201	lost[76B7]	C7740	CAP_201	lost[76B7]	C7741	CAP_201	lost[76B7]	C7742	CAP_201	lost[76B7]	
C0716	CAP_402	lost[7D3]	C1958	CAP_402	lost[19D4]	C3008	CAP_0201	lost[30A3]	C7743	CAP_201	lost[76B7]	C7744	CAP_201	lost[76B7]	C7745	CAP_201	lost[76B7]	C7746	CAP_201	lost[76B7]	C7747	CAP_201	lost[76B7]	C7748	CAP_201	lost[76B7]	C7749	CAP_201	lost[76B7]	
C0717	CAP_201	lost[7D6]	C1971	CAP_201	lost[19C2]	C3009	CAP_402	lost[30A4]	C7750	CAP_201	lost[76B7]	C7751	CAP_201	lost[76B7]	C7752	CAP_201	lost[76B7]	C7753	CAP_201	lost[76B7]	C7754	CAP_201	lost[76B7]	C7755	CAP_201	lost[76B7]	C7756	CAP_201	lost[76B7]	
C0718	CAP_201	lost[7D5]	C1990	CAP_201	lost[19A6]	C3052	CAP_201	lost[30D5]	C7757	CAP_201	lost[76B7]	C7758	CAP_201	lost[76B7]	C7759	CAP_201	lost[76B7]	C7760	CAP_201	lost[76B7]	C7761	CAP_201	lost[76B7]	C7762	CAP_201	lost[76B7]	C7763	CAP_201	lost[76B7]	
C0719	CAP_402	lost[7C6]	C1991	CAP_201	lost[19A6]	C3053	CAP_603	lost[30D5]	C7764	CAP_201	lost[76B7]	C7765	CAP_201	lost[76B7]	C7766	CAP_201	lost[76B7]	C7767	CAP_201	lost[76B7]	C7768	CAP_201	lost[76B7]	C7769	CAP_201	lost[76B7]	C7770	CAP_201	lost[76B7]	
C0750	CAP_402	lost[7D3]	C1992	CAP_201	lost[19A6]	C3100	CAP_201	lost[31D2]	C7771	CAP_201	lost[76B7]	C7772	CAP_201	lost[76B7]	C7773	CAP_201	lost[76B7]	C7774	CAP_201	lost[76B7]	C7775	CAP_201	lost[76B7]	C7776	CAP_201	lost[76B7]	C7777	CAP_201	lost[76B7]	
C0751	CAP_402	lost[7D2]	C1993	CAP_201	lost[19A5]	C3101	CAP_402	lost[31D3]	C7778	CAP_201	lost[76B7]	C7779	CAP_201	lost[76B7]	C7780	CAP_201	lost[76B7]	C7781	CAP_201	lost[76B7]	C7782	CAP_201	lost[76B7]	C7783	CAP_201	lost[76B7]	C7784	CAP_201	lost[76B7]	
C0752	CAP_402	lost[7D2]	C1994	CAP_201	lost[19C2]	C3102	CAP_201	lost[31D4]	C7785	CAP_201	lost[76B7]	C7786	CAP_201	lost[76B7]	C7787	CAP_201	lost[76B7]	C7788	CAP_201	lost[76B7]	C7789	CAP_201	lost[76B7]	C7790	CAP_201	lost[76B7]	C7791	CAP_201	lost[76B7]	
C0753	CAP_402	lost[7D2]	C2000	CAP_805	lost[20C3]	C3103	CAP_201																							

	8	7	6	5	4	3	2	1
D2600	DIODE_SCHOT_SOD-323	last[2604]	L7501	IND_0603	last[75C5]	R0926	RES_201	last[9B7]
D3000	DIODE_SCHOT_SOD-323	last[30B3]	L7502	IND_0603	last[75B5]	R0934	RES_201	last[9C3]
D5000	RCLAMP0502N_SLP1210N	last[50D2]	L7600	FILTER_2P_0201	last[76D7]	R0936	RES_201	last[9A2]
D5023	DIODE_SCHOT_6P_4C_2A	last[50D7]	L9400	IND_0402-LF	last[94D7]	R0937	RES_201	last[9D3]
D5100	RCLAMP0502N_SLP1210N	last[51D2]	L9401	IND_0402-LF	last[94D7]	R0938	RES_201	last[9D3]
D5120	DIODE_SCHOT_6P_4C_2A	last[51D7]	L9402	IND_0402-LF	last[94D7]	R0940	RES_201	last[9C6]
DZ3900	SUPPR_TRANSIENT1_402	last[39C6]	L9403	IND_0402-LF	last[94C7]	R0941	RES_201	last[9C6]
DZ3901	SUPPR_TRANSIENT1_402	last[39C6]	L9405	IND_0402-LF	last[94D4]	R0942	RES_201	last[986]
DZ3902	SUPPR_TRANSIENT1_402	last[39C7]	L9406	IND_0402-LF	last[94C4]	R0943	RES_201	last[985]
DZ3903	SUPPR_TRANSIENT1_402	last[39B7]	Q1801	TRA_DUAL_PWRPK_PCHN_6P_PWRPK-1212-8	last[18B4 18C5]	R0944	RES_201	last[986]
DZ3904	SUPPR_TRANSIENT1_402	last[39B6]	Q1803	TRA_DUAL_PWRPK_PCHN_6P_PWRPK-1212-8	last[18B3 18D3]	R0945	RES_201	last[986]
DZ3905	SUPPR_TRANSIENT1_402	last[39B6]	Q1807	TRA_DUAL_2N7002A_SOT	last[18C4 18C4]	R0966	RES_201	last[9C3]
DZ3910	SUPPR_TRANSIENT1_402	last[39C3]	Q1808	TRA_DUAL_2N7002A_SOT	last[18A3 18A4]	R0967	RES_201	last[9A1]
DZ3911	SUPPR_TRANSIENT1_402	last[39C3]	Q1809	TRA_PWRPK_PCHN_5P_PWR	last[18B6]	R0970	RES_201	last[9A7]
DZ3912	SUPPR_TRANSIENT1_402	last[39C4]	Q1810	TRA_DUAL_2N7002A_SOT	last[18A5 18A6]	R0971	RES_201	last[9A7]
DZ3913	SUPPR_TRANSIENT1_402	last[39B3]	Q1816	TRA_DUAL_2N7002A_SOT	last[18C3 18D2]	R0980	RES_201	last[9D7]
DZ3914	SUPPR_TRANSIENT1_402	last[39B3]	Q1817	TRA_DUAL_2N7002A_SOT	last[18B1 18B2]	R1001	RES_201	last[10A5]
DZ3915	SUPPR_TRANSIENT1_402	last[39B3]	Q1910	TRA_SI7904DN_PWRPK-1	last[19C5]	R1003	RES_201	last[10A4]
DZ5089	SUPPR_TRANSIENT1_402	last[50B7]	Q1915	TRA_DUAL_2N7002A_SOT	last[19B2 19A2]	R1004	RES_201	last[10C4]
DZ5090	SUPPR_TRANSIENT1_402	last[50C4]	Q1916	TRA_DUAL_2N7002A_SOT	last[19A3 19A1]	R1005	RES_201	last[10C3]
DZ5091	SUPPR_TRANSIENT1_402	last[50C6]	Q1917	TRA_PWRPK_PCHN_5P_PWR	last[19C2]	R1006	RES_201	last[10A4]
DZ5092	SUPPR_TRANSIENT1_402	last[50D4]	Q2000	TRA_FDZ294N_BGA	last[20C3]	R1007	RES_201	last[10A3]
DZ5093	SUPPR_TRANSIENT1_402	last[50A7]	Q2001	TRA_FDZ294N_BGA	last[20B4]	R1008	RES_201	last[10C7]
DZ5189	SUPPR_TRANSIENT1_402	last[51B7]	Q2002	TRA_2N7002T_SOT-523	last[20A7]	R1009	RES_201	last[10C4]
DZ5190	SUPPR_TRANSIENT1_402	last[51D4]	Q2100	TRA_SI7904DN_PWRPK-1	last[21C4]	R1010	RES_201	last[10C7]
DZ5191	SUPPR_TRANSIENT1_402	last[51C6]	Q2101	TRA_SI7904DN_PWRPK-1	last[21C4]	R1011	RES_201	last[10C4]
DZ5192	SUPPR_TRANSIENT1_402	last[51C4]	Q2102	TRA_DUAL_SSM6N15FE_S	last[21C7 21C8]	R1012	RES_201	last[10B2]
DZ5193	SUPPR_TRANSIENT1_402	last[51A7]	Q2103	TRA_DUAL_2N7002A_SOT	last[21A7 21A6]	R1015	RES_201	last[10B2]
F1800	FUSE_603-HF	last[18D7]	Q2320	TRA_SI7904DN_PWRPK-1	last[23B3]	R1017	RES_201	last[10B2]
F1900	FUSE_1206	last[19C2]	Q2400	TRA_SSM3K15FV_SOD-VE	last[24B3]	R1019	RES_201	last[10B2]
FL5007	FILTER_2P_0201	last[50B5]	Q2401	TRA_PCH_CEDM8001_SOT	last[24B3]	R1020	RES_201	last[10C4]
FL5008	FILTER_2P_0201	last[50A5]	Q2701	TRA_FDZ294N_BGA	last[27C5]	R1021	RES_201	last[10B3]
FL5011	FILTER_2P_0201	last[50B5]	Q2702	TRA_FDZ294N_BGA	last[27C5]	R1022	RES_201	last[10B3]
FL5020	FILTER_2P_0201	last[50B3]	Q2703	TRA_FDZ294N_BGA	last[27B5]	R1027	RES_201	last[10A4]
FL5107	FILTER_2P_0201	last[51B5]	Q2709	TRA_SI5486DU_POWERPA	last[27C2]	R1050	RES_201	last[10C4]
FL5108	FILTER_2P_0201	last[51A5]	Q2900	TRA_DUAL_SSM6N15FE_S	last[29A4 29A4]	R1060	RES_201	last[10C2]
FL5111	FILTER_2P_0201	last[51B5]	Q2902	TRA_DUAL_BC847BV_SOT	last[29D2 29D1]	R1061	RES_201	last[10C1]
FL5120	FILTER_2P_0201	last[51B3]	Q3100	TRA_DUAL_2N7002A_SOT	last[31B1 31B1]	R1090	RES_201	last[10D2]
FL7500	FILTER_2P_0201	last[75C7]	Q3101	TRA_2N7002T_SOT-523	last[31A6]	R1091	RES_201	last[10D2]
J1900	CON_M6RT_S2MT_SM1_M-	last[19A5]	Q3102	TRA_2N7002T_SOT-523	last[31B1]	R1092	RES_201	last[10D2]
J3010	CON_F50ST_D4MT_SM_F-	last[30B7]	Q3200	TRA_FDZ293P_BGA	last[32D6]	R1093	RES_201	last[10D1]
J3011	CON_F60ST_D4MT_SM_F-	last[30B6]	Q3201	TRA_2N7002T_SOT-523	last[32D7]	R1094	RES_201	last[10D2]
J3020	CON_F4RT_S2MT_SMA_F-	last[30B1]	Q3500	TRA_NTK3142P_PCHN_3P	last[35C2]	R1095	RES_201	last[10B8]
J3201	CON_F30ST_S5MT_SM_F-	last[32D1]	Q3501	TRA_2N7002T_SOT-523	last[35B3]	R1096	RES_201	last[10B8]
J3700	CON_M4RT_S2MT_SM_M-R	last[37B2]	Q4200	TRA_NCH_SSM3K16CT_CS	last[42C2]	R1097	RES_201	last[10A4]
J3800	CON_M4RT_S2MT_SM_M-R	last[38C2]	Q4201	TRA_NCH_SSM3K16CT_CS	last[42C6]	R1098	RES_201	last[10A4]
J4111	CON_M10RT_S2MT_SM1_M	last[41D5]	R1	THERMISTERS_0402	last[20B2]	R1099	RES_201	last[10C1]
J4161	CON_M10RT_S2MT_SM1_M	last[41B5]	R0600	RES_201	last[6C8]	R1500	RES_201	last[15B6]
J4600	CON_F6RT_S2MT_SM_F-R	last[46C5]	R0601	RES_201	last[6C8]	R1503	RES_201	last[15C1]
J5001	CON_F36ST_D4MT_SM_F-	last[50C2]	R0602	RES_201	last[6C8]	R1504	RES_201	last[15C1]
J5105	CON_F36ST_D4MT_SM_F-	last[51C2]	R0603	RES_201	last[6B5]	R1505	RES_201	last[15C1]
J5400	CON_M4RT_S2MT_SM_M-R	last[54C2]	R0604	RES_201	last[6B5]	R1507	RES_201	last[15B1]
J6500	CON_F6RT_S2MT_SM_F-R	last[65B4]	R0605	RES_201	last[6B5]	R1508	RES_201	last[15B1]
J9000	CON_F60ST_D_SMI_F-ST	last[90C4]	R0606	RES_201	last[6B5]	R1509	RES_201	last[15B2]
L1500	IND_0402-LF	last[15D7]	R0607	RES_201	last[6B5]	R1514	RES_201	last[15C2]
L1501	IND_0402-LF	last[15D7]	R0608	RES_201	last[6D5]	R1515	RES_201	last[15C2]
L1900	IND_ILH2020BE11-SM	last[19A4]	R0609	RES_201	last[6B1]	R1530	RES_201	last[15A7]
L1901	IND_SM-LF	last[19A6]	R0610	RES_201	last[6B1]	R1531	RES_201	last[15A7]
L2000	IND_PCMC042T-SM	last[20C2]	R0611	RES_201	last[6B1]	R1532	RES_201	last[15A7]
L2102	IND_PCMC042T-SM	last[21C4]	R0612	RES_201	last[6D4]	R1533	RES_201	last[15A7]
L2103	IND_PCMC042T-SM	last[21A3]	R0613	RES_201	last[6C5]	R1540	RES_201	last[15C2]
L2200	IND_MDT250CN-SM	last[22B5]	R0614	RES_201	last[6C5]	R1541	RES_201	last[15C2]
L2250	IND_SM	last[22C4]	R0620	RES_201	last[6A7]	R1542	RES_201	last[15B2]
L2320	IND_MMD05C2-SM	last[23B3]	R0621	RES_201	last[6A7]	R1543	RES_201	last[15B2]
L2401	IND_S1024AS-SM	last[24C4]	R0622	RES_201	last[6A7]	R1544	RES_201	last[15B2]
L2450	IND_S1024AS-SM	last[24B4]	R0700	RES_201	last[7B3]	R1804	RES_201	last[18A2]
L2600	IND_ILH2020BE11-SM	last[26D5]	R0701	RES_201	last[7B3]	R1805	RES_201	last[18D4]
L3000	IND_VLF	last[30B4]	R0702	RES_201	last[7B3]	R1806	RES_201	last[18C2]
L3201	IND_0402	last[32D5]	R0703	RES_201	last[7B3]	R1807	RES_201	last[18C5]
L3202	FILTER_4P_TCM1005	last[32C3]	R0704	RES_201	last[7B3]	R1808	RES_201	last[18B4]
L3400	FILTER_2P_0201	last[34B4]	R0705	RES_201	last[7B3]	R1809	RES_201	last[18A5]
L3601	FILTER_2P_0201	last[36A5]	R0706	RES_201	last[7B3]	R1810	RES_201	last[18A4]
L3602	FILTER_2P_0201	last[36A5]	R0800	RES_201	last[8B7]	R1812	RES_201	last[18A5]
L3900	FILTER_2P_0201	last[39D6]	R0801	RES_201	last[8B7]	R1813	RES_201	last[18B3]
L3901	FILTER_2P_0201	last[39D6]	R0802	RES_201	last[8A8]	R1828	RES_201	last[18C4]
L3902	FILTER_2P_0201	last[39C6]	R0804	RES_201	last[8C2]	R1830	RES_402	last[18A4]
L3903	FILTER_2P_0201	last[39C6]	R0805	RES_201	last[8C2]	R1831	RES_201	last[18A4]
L3904	FILTER_2P_0201	last[39B6]	R0806	RES_201	last[8C2]	R1832	RES_402	last[18B2]
L3905	FILTER_2P_0201	last[39B6]	R0807	RES_201	last[8B7]	R1833	RES_201	last[18B2]
L3910	FILTER_2P_0201	last[39D2]	R0808	RES_201	last[8B7]	R1927	RES_402	last[19D6]
L3911	FILTER_2P_0201	last[39D2]	R0900	RES_201	last[9C2]	R1928	RES_201	last[19C8]
L3912	FILTER_2P_0201	last[39C2]	R0901	RES_201	last[9B3]	R1929	RES_402	last[19D6]
L3913	FILTER_2P_0201	last[39C2]	R0902	RES_201	last[9D6]	R1930	RES_402	last[19D6]
L3914	FILTER_2P_0201	last[39C2]	R0903	RES_201	last[9D6]	R1931	RES_805	last[19D7]
L3915	FILTER_2P_0201	last[39B2]	R0904	RES_201	last[9B1]	R1932	RES_201	last[19B7]
L3916	FILTER_2P_0201	last[39B2]	R0905	RES_201	last[9B1]	R1933	RES_201	last[19B8]
L4600	FILTER_4P_TCM1005	last[46C6]	R0909	RES_201	last[9D4]	R1934	RES_201	last[19B8]
L5006	IND_0402	last[50A7]	R0910	RES_201	last[9D3]	R1935	RES_201	last[19D7]
L5009	IND_0402	last[50D5]	R0911	RES_201	last[9D3]	R1936	RES_1206-1	last[19C4]
L5013	IND_0402	last[50B7]	R0912	RES_201	last[9D6]	R1940	RES_201	last[19B3]
L5014	IND_0402	last[50C5]	R0913	RES_201	last[9D6]	R1941	RES_201	last[19B3]
L5015	IND_0402	last[50C7]	R0914	RES_201	last[9D5]	R1942	RES_201	last[19A3]
L5016	FILTER_4P_TCM1005	last[50D2]	R0915	RES_201	last[9D6]	R1943	RES_201	last[19A4]
L5017	IND_0603	last[50D7]	R0916	RES_201	last[9D5]	R1944	RES_201	last[19A3]
L5039	IND_0603	last[50C7]	R0923	RES_201	last[9C7]	R1947	RES_1206	last[19C3]
L5106	IND_0402	last[51A7]				R1948	RES_201	last[19C6]
L5109	IND_0402	last[51C5]				R1949	RES_201	last[19C6]
L5113	IND_0402	last[51B7]				R1950	RES_201	last[19B3]
L5114	IND_0402	last[51D5]				R1951	RES_201	last[19A3]
L5115	IND_0402	last[51C7]				R1952	RES_201	last[19A1]
L5116	FILTER_4P_TCM1005	last[51D2]				R1953	RES_201	last[19D6]
L5117	IND_0603	last[51D7]				R1954	RES_201	last[19B3]
L5139	IND_0603	last[51C7]						
L6500	FILTER_4P_TCM1005	last[65B5]						
L7500	IND_0603	last[75C5]						
R1955	RES_201	last[19C5]						
R1956	RES_201	last[19B8]						
R1957	RES_1206	last[19C2]						
R1960	RES_402	last[19D7]						
R1961	RES_201	last[19C7]						
R1962	RES_201	last[19B1]						
R1964	RES_402	last[19A8]						
R1965	RES_201	last[19A8]						
R1967	RES_201	last[19						

	8		7		6		5		4		3		2		1	
D	R2945	RES_201	lost[29D5]	R6612	RES_201	lost[66B2]	U3501	SWI_FSUSB42_UMLP	lost[35A3]							
	R2946	RES_201	lost[29C2]	R6700	RES_201	lost[67B7]	U3502	74LVC2G66_BGA	lost[35D7 35D7]							
	R2947	RES_201	lost[29B3]	R6701	RES_201	lost[67B7]	U3504	74LVC2G66_BGA	lost[35B7 35C7]							
	R2948	RES_201	lost[29D4]	R6702	RES_201	lost[67B7]	U3600	CS4206_QFN	lost[36D5]							
	R2990	RES_201	lost[29C5]	R6703	RES_201	lost[67B7]	U3601	MAX8840_UDFN	lost[36A4]							
	R2991	RES_201	lost[29C5]	R6704	RES_201	lost[67B7]	U3700	MAX9705_TDFN1	lost[37B4]							
	R3001	RES_201	lost[30D1]	R6705	RES_201	lost[67B7]	U3800	MAX9724A_TQFN	lost[38D6]							
	R3005	RES_201	lost[30D1]	R6706	RES_201	lost[67B7]	U4050	STG3684A_QFN10L	lost[40C2]							
	R3009	RES_201	lost[30A3]	R6707	RES_201	lost[67B7]	U4210	CS3272_WCS9	lost[42B3]							
	R3010	RES_201	lost[30D3]	R6710	RES_201	lost[67B4]	U4220	CD3272_WCS9	lost[42B6]							
	R3011	RES_201	lost[30C3]	R6711	RES_201	lost[67B4]	U4300	EMC1043_MSOP	lost[43D5]							
	R3012	RES_201	lost[30A3]	R6712	RES_201	lost[67B4]	U5000	SUPPR_NUP412VP5_SOT9	lost[50B2]							
	R3050	RES_201	lost[30C4]	R6713	RES_201	lost[67B4]	53									
	R3051	RES_201	lost[30C4]	R6714	RES_201	lost[67B4]	U5100	SUPPR_NUP412VP5_SOT9	lost[51A2]							
	R3052	RES_201	lost[30C4]	R6715	RES_201	lost[67B4]	53									
	R3060	RES_201	lost[30D2]	R6716	RES_201	lost[67B4]	U5200	SWI_LM34902_USMD	lost[52C6]							
	R3066	RES_201	lost[30B3]	R6717	RES_201	lost[67B4]	U5201	SWI_LM34902_USMD	lost[52C6]							
	R3080	RES_402	lost[30D3]	R7503	RES_201	lost[75B7]	U5202	74HC303244_QFN	lost[52C2]							
	R3081	RES_402	lost[30D3]	R7504	RES_201	lost[75B7]	U6600	PS2231_IQFP	lost[66C5]							
	R3090	RES_402	lost[30A1]	R7505	RES_201	lost[75B7]	U6700	FLASH_8GX8_48P7_TSOP	lost[67C6]							
	R3091	RES_402	lost[30A1]	R7506	RES_201	lost[75B3]	U6710	FLASH_8GX8_48P7_TSOP	lost[67C3]							
	R3100	RES_201	lost[31B3]	R7507	RES_201	lost[75B3]	U7501	THS7318_BGA	lost[75C7]							
	R3101	RES_201	lost[31D4]	R7508	RES_201	lost[75C3]	U7600	A1010_WLCSF48	lost[76C5]							
	R3102	RES_201	lost[31D2]	R7509	RES_201	lost[75B3]	U7601	MAX8510_SC70-5	lost[76D6]							
	R3107	RES_201	lost[31B6]	R7510	RES_201	lost[75C3]	U9400	CH7021A_QFN	lost[94C6]							
	R3108	RES_201	lost[31B6]	R7511	RES_201	lost[75C3]	U9600	BT_WIFI_MOD_M53_SM	lost[96C5]							
	R3109	RES_201	lost[31A6]	R7520	RES_201	lost[75B7]	U9700	FT232R_QFN	lost[97C5]							
	R3150	RES_201	lost[31B3]	R7521	RES_201	lost[75B7]	XW1201	SHORT_SM	lost[12B1]							
	R3160	RES_201	lost[31C5]	R7522	RES_201	lost[75B7]	XW1700	SHORT_SHORT-0201	lost[17B7]							
	R3190	RES_201	lost[31D6]	R7600	RES_201	lost[76C7]	XW1701	SHORT_SHORT-0201	lost[17C2]							
	R3191	RES_201	lost[31D6]	R7610	RES_201	lost[76B7]	XW1900	SHORT_SHORT-0201	lost[19C5]							
	R3200	RES_201	lost[32C4]	R7611	RES_201	lost[76B7]	XW1901	SHORT_SHORT-0201	lost[19D7]							
	R3201	RES_201	lost[32C4]	R7612	RES_201	lost[76B6]	XW1902	SHORT_SHORT-0201	lost[19D7]							
	R3203	RES_201	lost[32D7]	R7613	RES_201	lost[76C7]	XW1903	SHORT_SHORT-0201	lost[19B4]							
	R3204	RES_201	lost[32D7]	R7614	RES_201	lost[76C7]	XW1904	SHORT_SHORT-0201	lost[19B4]							
	R3205	RES_201	lost[32C8]	R7615	RES_201	lost[76C6]	XW1905	SHORT_SHORT-0201	lost[19C3]							
	R3206	RES_201	lost[32C3]	R7616	RES_201	lost[76C4]	XW1906	SHORT_SHORT-0201	lost[19C3]							
	R3207	RES_201	lost[32C3]	R7617	RES_201	lost[76C3]	XW2000	SHORT_SHORT-0201	lost[20B5]							
	R3300	RES_201	lost[33C5]	R7618	RES_201	lost[76B3]	XW2100	SHORT_SHORT-0201	lost[21B3]							
	R3301	RES_201	lost[33C5]	R7620	RES_201	lost[76C6]	XW2101	SHORT_SHORT-0201	lost[21A4]							
	R3302	RES_201	lost[33C6]	R7621	RES_201	lost[76C6]	XW2201	SHORT_SHORT-0201	lost[22A5]							
	R3303	RES_201	lost[33C6]	R7650	RES_201	lost[76A5]	XW2300	SHORT_SHORT-0201	lost[23B5]							
	R3304	RES_201	lost[33C3]	R9003	RES_201	lost[90C6]	XW2301	SHORT_SHORT-0201	lost[23C5]							
	R3500	RES_201	lost[35D2]	R9004	RES_201	lost[90B2]	XW2302	SHORT_SHORT-0201	lost[23C4]							
	R3501	RES_201	lost[35C2]	R9005	RES_201	lost[90B7]	XW2303	SHORT_SHORT-0201	lost[23A5]							
	R3504	RES_201	lost[35B4]	R9010	RES_201	lost[90B6]	XW2400	SHORT_SHORT-0201	lost[24C3]							
	R3505	RES_201	lost[35C3]	R9011	RES_201	lost[90A4]	XW2450	SHORT_SHORT-0201	lost[24A5]							
	R3512	RES_201	lost[35D6]	R9012	RES_201	lost[90A5]	XW2600	SHORT_SHORT-0201	lost[26B3]							
	R3514	RES_201	lost[35C6]	R9013	RES_201	lost[90A4]	XW2800	SHORT_SM	lost[28C2]							
	R3515	RES_201	lost[35B6]	R9020	RES_201	lost[90C4]	XW3000	SHORT_SM	lost[30A4]							
	R3516	RES_201	lost[35D6]	R9050	RES_201	lost[90B4]	XW3600	SHORT_SM	lost[36A5]							
	R3518	RES_201	lost[35C1]	R9051	RES_201	lost[90B6]	XW3601	SHORT_SM	lost[36B6]							
	R3519	RES_201	lost[35B1]	R9052	RES_201	lost[90B5]	XW3602	SHORT_SM	lost[36B6]							
	R3600	RES_201	lost[36A5]	R9407	RES_201	lost[94B4]	XW3700	SHORT_SM	lost[37A6]							
	R3610	RES_201	lost[36C7]	R9408	RES_201	lost[94A4]	XW3701	SHORT_SM	lost[37B6]							
	R3611	RES_201	lost[36B6]	R9409	RES_201	lost[94A5]	XW3950	SHORT_SM	lost[39A6]							
	R3612	RES_201	lost[36B6]	R9410	RES_201	lost[94A5]	XW3951	SHORT_SM	lost[39A6]							
	R3613	RES_201	lost[36B5]	R9418	RES_201	lost[94B4]	XW3952	SHORT_SM	lost[39A6]							
	R3614	RES_201	lost[36B5]	R9419	RES_201	lost[94A6]	XW3953	SHORT_SM	lost[39A6]							
	R3615	RES_201	lost[36B5]	R9450	RES_201	lost[94A7]	XW3954	SHORT_SM	lost[39A6]							
	R3616	RES_201	lost[36D7]	R9460	RES_201	lost[94C5]	XW3955	SHORT_SM	lost[39A6]							
	R3617	RES_201	lost[36B3]	R9490	RES_201	lost[94B4]	XW4210	SHORT_SM	lost[42B3]							
	R3701	RES_201	lost[37B6]	R9491	RES_201	lost[94B4]	XW4220	SHORT_SM	lost[42B6]							
	R3702	RES_201	lost[37B6]	R9492	RES_201	lost[94B4]	XW5000	SHORT_SHORT-0201	lost[50A2]							
	R3703	RES_201	lost[37B6]	R9600	RES_201	lost[96B2]	XW5100	SHORT_SHORT-0201	lost[51A2]							
	R3704	RES_201	lost[37B6]	R9601	RES_201	lost[96B5]	XW7600	SHORT_SM	lost[76C6]							
	R3710	RES_201	lost[37A5]	TP9900		TP_SM-TP25-TOP	Y0900	CRYSTAL_3.2X1.5X.6-S	lost[9D3]							
	R3810	RES_201	lost[38B6]	TP9901		TP_SM-TP25-TOP	N									
	R3811	RES_201	lost[38B6]	U0600	SILVERTHORNE_BGA	lost[6D7 6C3]	Y1500	CRYSTAL_4PIN_SM-2.5X	lost[15C6]							
	R3820	RES_201	lost[38A6]	U0600	SILVERTHORNE_BGA	lost[7D8 7D5]	2.0MM									
	R3821	RES_201	lost[38A6]	U0800	POULSBO_BGA	lost[8D5]	Y2900	CRYSTAL_4PIN_SM-2.5X	lost[29C7]							
	R3840	RES_201	lost[38B3]	U0800	POULSBO_BGA	lost[9D5]	2.0MM									
	R3841	RES_201	lost[38B3]	U0800	POULSBO_BGA	lost[10D6]	Y6600	CRYSTAL_4PIN_SM-3.2X	lost[66C7]							
	R3842	RES_201	lost[38B3]	U0800	POULSBO_BGA	lost[11D5]	2.5MM									
	R3843	RES_201	lost[38B2]	U0800	POULSBO_BGA	lost[12D7]	Y9400	CRYSTAL_4PIN_SM-2.5X	lost[94B7]							
	R3950	RES_201	lost[39A7]	U0800	POULSBO_BGA	lost[13D8 13D6 13D4 13D2]	2.0MM									
	R3951	RES_201	lost[39A6]	U1400	SDRAM_32MX16_84P_FBG	lost[14D2]										
	R4000	RES_402	lost[40C7]	A												
	R4001	RES_402	lost[40C7]	U1401	SDRAM_32MX16_84P_FBG	lost[14B3]										
	R4010	RES_201	lost[40C6]	A												
	R4011	RES_201	lost[40C6]	U1402	SDRAM_32MX16_84P_FBG	lost[14D7]										
	R4015	RES_201	lost[40C7]	A												
	R4016	RES_201	lost[40C7]	U1403	SDRAM_32MX16_84P_FBG	lost[14B7]										
	R4020	RES_201	lost[40B6]	A												
	R4021	RES_201	lost[40B6]	U1500	CLK_GEN_SLG8SP528_QF	lost[15C5]										